

**MILLIMETER-WAVE MONOLITHIC
SCHOTTKY DIODE-GRID PHASE SHIFTER**

**Thesis by
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TO MY PARENTS

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**Millimeter-Wave Monolithic Schottky
Diode-Grid Phase Shifter**

Abstract

Many applications at millimeter wavelengths require fast electronic phase shifters. In this study, the design of diode-grid phase shifters is presented, the fabrication of diode-grids on monolithic gallium-arsenide substrates is demonstrated, and the measurement of these grids is discussed. A new computer-aided design tool is developed to provide an interactive environment for design and to form a basis for comparing theory and experimental results. Diode-grids have been fabricated on 2 cm by 3 cm gallium-arsenide wafers with 2000 aluminum Schottky diodes. A novel small aperture reflectometer is computerized to use a wave-front division interference technique to measure the reflection coefficient of the grids. A 70° phase shift with a 6.5-dB loss was measured at 93 GHz when the bias on the diode-grid was changed from -3 V to $+1$ V.

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Chapter 1

Introduction

Unique features of millimeter waves have attracted a growing interest in the wavelength region from 1 mm to 10 mm. Millimeter waves offer broader bandwidths, higher resolution and smaller component size than microwaves, and provide better penetration of fog and dust than infrared [1]. In radio astronomy, the measurement of molecular resonance lines, which occur significantly in the millimeter range, have provided important physical insights into the state of interstellar clouds [2]. In plasma diagnostics, the measurement of electron density and temperature profile, and fluctuation have increased the understanding of particle confinement mechanisms in a fusion plasma [3]. Thermography at millimeter wavelengths has been used in tumor detection [4]. Other applications including active radiometry [4], satellite communication [5], and remote-sensing of the earth's surface [1] are beginning to receive more attention.

As the number of applications increases, demands for new and improved systems follow. Currently standard components available for millimeter applications are based on metal cavities, waveguides, and horns. Although they are adequate for small systems, they become quite expensive to use in large systems because intensive labor is required in machining these parts. In situations where only a limited amount of space is available, they can be very difficult to manage, since their physical dimensions tend to be much bigger than a wavelength. These are the driving forces that have led to the recently increased efforts in research and development of monolithic integrated circuits for millimeter waves. Special issues on this subject have appeared in the IEEE Transactions [6,7]. Recently, Stiglitz [9] presented a special report on the topic of gallium-arsenide technology and microwave and millimeter-wave monolithic integrated circuits for 1987.

1.1 Applications of Diode-Grids

Much of the current research activities in monolithic integrated circuits for millimeter waves tend to revolve around a variety of planar transmission-line structures and dielectric waveguides. Recently a different structure based on integrating solid-state devices into a periodic grid has emerged. Rutledge and Schwarz demonstrated a multi-mode detector array by integrating microbolometers into a periodic grid [10]. Tong *et al.* built a two-dimensional tracking array, also with microbolometers and a periodic grid [11]. Figure 1.1 shows a periodic grid loaded with Schottky varactor-diodes, hence the name diode-grid. Designs based on the diode-grid for electronic beam-steering and frequency multiplication had been proposed [12], and the fabrication of a diode-grid was demonstrated on a 2 cm by 2 cm gallium-arsenide wafer with 2000 diodes [13].

Periodic grids offer many advantages. They present a planar geometry that is both simple and compact; therefore, it has a tremendous capacity for interconnecting thousands of solid-state devices on a single substrate. The periodic grid lends itself to a variety of high-power applications, since power is distributed among a large number of electronic devices throughout a planar surface. They are exciting because they open up a new area of monolithic integrated circuits for plane waves. This provides an extra degree of freedom to the circuit designer. Basically, the longitudinal dimension can be used for guiding high-frequency signals and feeding electromagnetic energy to the devices, while low-frequency control signals and bias can be routed in the transverse dimension. The system design is analogous to an optics design. Furthermore, the diode-grid approach is compatible with the semiconductor fabrication technology. This leads to lower cost, smaller size, and more reliable components. No transmission lines or waveguides are used. This makes fabrication simpler and losses lower.

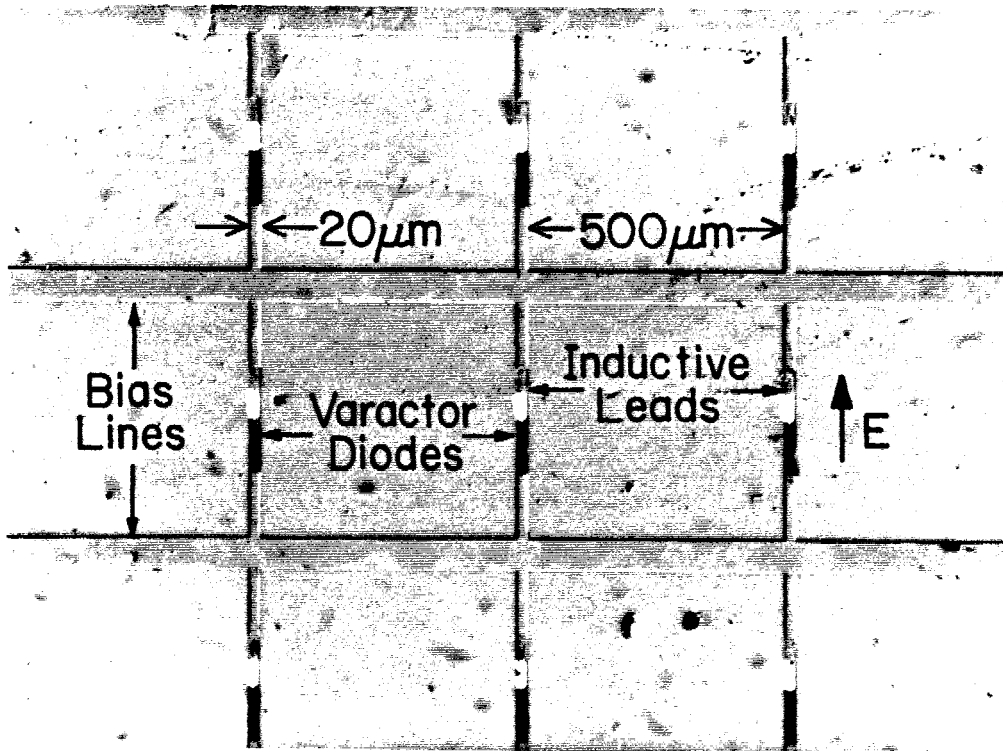


Figure 1.1. Part of a periodic diode-grid fabricated on a gallium-arsenide wafer.

One of the applications of the diode-grid is in electronic beam-steering. Currently, beam-steering plays a vital role in advanced radars that track and image multiple objects simultaneously. The key element that enables a beam of radiation to be steered at electronic speeds is the phase shifter. Typically, many thousands of phase-shifting elements are required. Conventional phase-shifters based on microwave hybrids of striplines and waveguides lead to high cost and system interconnecting complexity as the wavelength approaches 1 mm. Recently Horn *et al.* [14] demonstrated an electronically modulated line scanning antenna. However, many applications require more gain than a line source can provide. The use of variable-permittivity media for phase-shifting is an intriguing alternative possibility [15,16]. Figure 1.2 shows a varactor-diode grid design for electronic beam-steering. In the beam-steering array, the incident beam reflects off the programmable diode-grid phase-shifting surface, where changing the dc bias on the diodes changes the reactance, and this controls the phase of the reflected waves. A linear variation of the phase across the aperture sets the direction of the reflected beam. In addition, a quadratic variation of the phase across the aperture focuses the reflected beam. No transmission lines or waveguides are required. This architecture makes the system design simpler and the fabrication cost lower. Since the power is distributed among all the diodes, the power handling capability can be designed specifically for a particular application by choosing the array size properly.

Another application of the diode-grid is in harmonic power generation. As the wavelength approaches 1 mm, the varactor multiplier plays an important role in providing local power to heterodyne receivers because other sources present many undesirable features. Tubes require cumbersome and dangerous high-voltage supplies, Impatt oscillators are generally too noisy, and Gunn oscillators are not able to provide sufficient power at frequencies above 100 GHz. Recently, Archer [17]

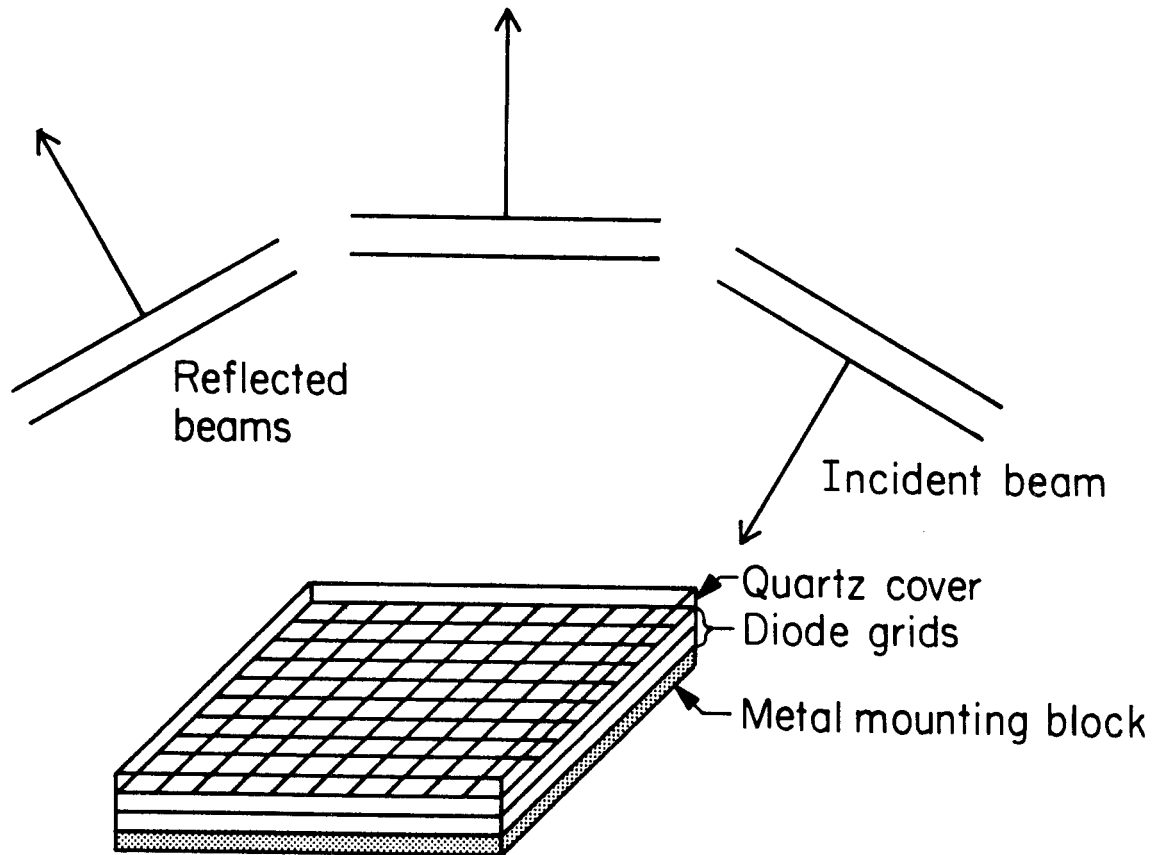


Figure 1.2. Electronically programmable beam-steering array.

summarized state-of-the-art performances for millimeter wavelength frequency multiplier. They are primarily based on using a single whisker-contacted varactor diode chip in a cross-waveguide configuration. Although very high conversion efficiency has been obtained, they are basically limited to milliwatts of output power. The fundamental limitation is that only a few varactors can be used simultaneously in a practical manner. However, power can be increased significantly when thousands of varactors are combined together in a suitable manner for synchronous operation. Figure 1.3 shows a second harmonic power-combiner that uses an array of nonlinear capacitors to generate and spatially combine power at the harmonic frequencies. In this frequency doubler design, power at the fundamental frequency enters through the input filter, arrives at the varactor-diode grid, and pumps the nonlinear capacitance of the diodes to generate power at harmonics. The second harmonic is spatially combined and transmitted through the output filter.

Loading a grid with negative resistance diodes offers the possibility of dc-to-rf power conversion that is analogous to a laser. Currently, oscillators with single electronic device such as Gunn or IMPATT diode are highly developed. However, they are capable of providing continuous-wave power only from about 500 mW at 40 GHz to 10 mW at 230 GHz [18]. Many applications in radars, imaging arrays, and heterodyne receivers require much more power than this. Although a number of power-combining circuits have been demonstrated including chip-level, circuit-level, and spatial power-combining [19], they do not take full advantage of what the solid-state semiconductor technology can offer. Recently Wandinger and Nalbandian demonstrated a dual oscillator quasi-optical power-combining resonator at 60 GHz with dielectric antennas [20]. However, extending this approach to higher-level combining would be expensive and difficult. Mink investigated theoretically a very interesting distributed source planar array

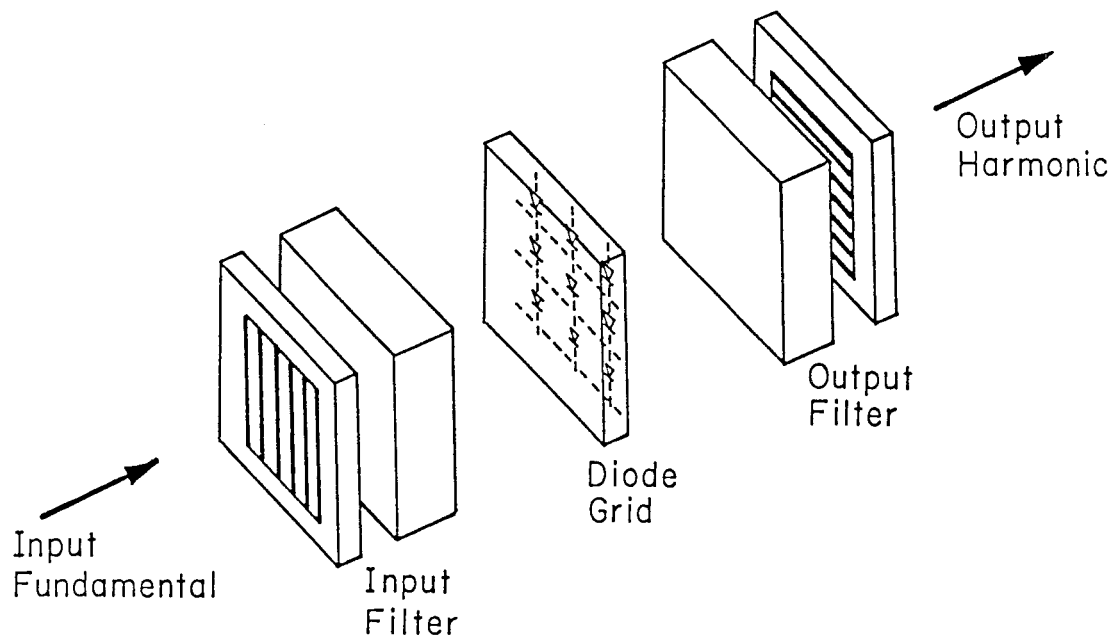


Figure 1.3. Second harmonic power-combiner.

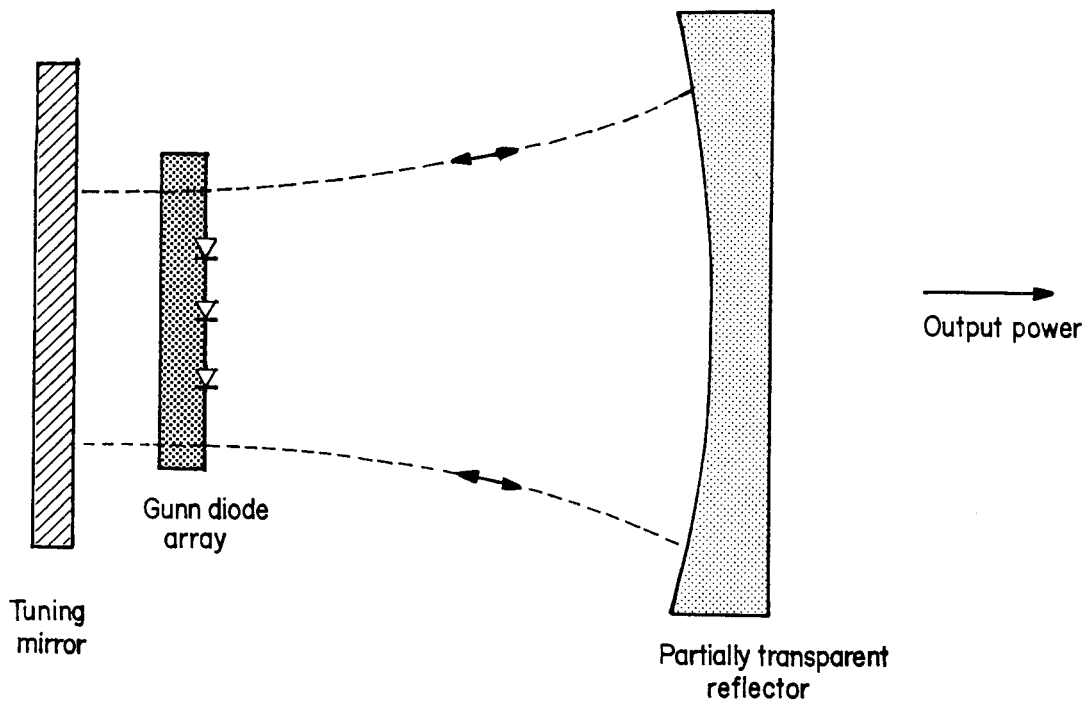


Figure 1.4. Gunn-diode resonator.

resonator [21]. Figure 1.4 shows a Gunn diode-grid resonator. Power generated by the Gunn diodes are combined in a semi-confocal cavity. A metal mirror is used as a tuning short for matching the impedance of the Gunn diode. Power can be coupled out through a partially transparent mirror. In addition, a union of the Gunn diode-grid resonator and the harmonic power-combiner would be an exciting all solid-state alternative for high-power local oscillator source.

Another interesting application of the diode-grid is in signal detection. Recently, Zah *et al.* [22] demonstrated a one-dimensional Schottky-diode imaging array at 90 GHz. Figure 1.5 shows a typical lens-coupled optical system that has been used in imaging experiments today [23,24]. The idea is to focus energy onto an imaging array at the focal plane, so that an image of the object can be constructed at electronic speed. Figure 1.6 is a close-up view of a two-dimensional imaging array based on the diode-grid approach. The idea is to use the horizontally connected diodes in the back layer as tuning elements for the vertically connected diodes in the front layer. The intersection of a column and a row defines a pixel element. The amplitude at each pixel can be measured from the column with the proper dc bias applied to the corresponding row. Furthermore, being able to ascertain informations from each unit cell of a finite periodic structure raises the possibility of studying edge effects due to finite periodicity. This should provide important insight into future works concerning circuit interactions between diode-grids on different planes.

Finally, applications of the diode-grid, analogous to holography and nonlinear optics, also appear possible. In optical phase conjugation, a nonlinear medium is used to generate the complex conjugate of a wave. This is useful in real-time imaging through a phase distorting medium [25]. The technique of using a nonlinear surface for generating a phase-conjugated wave has been demonstrated at optical wavelengths [26]. Figure 1.7 shows how this can be done with a varactor-

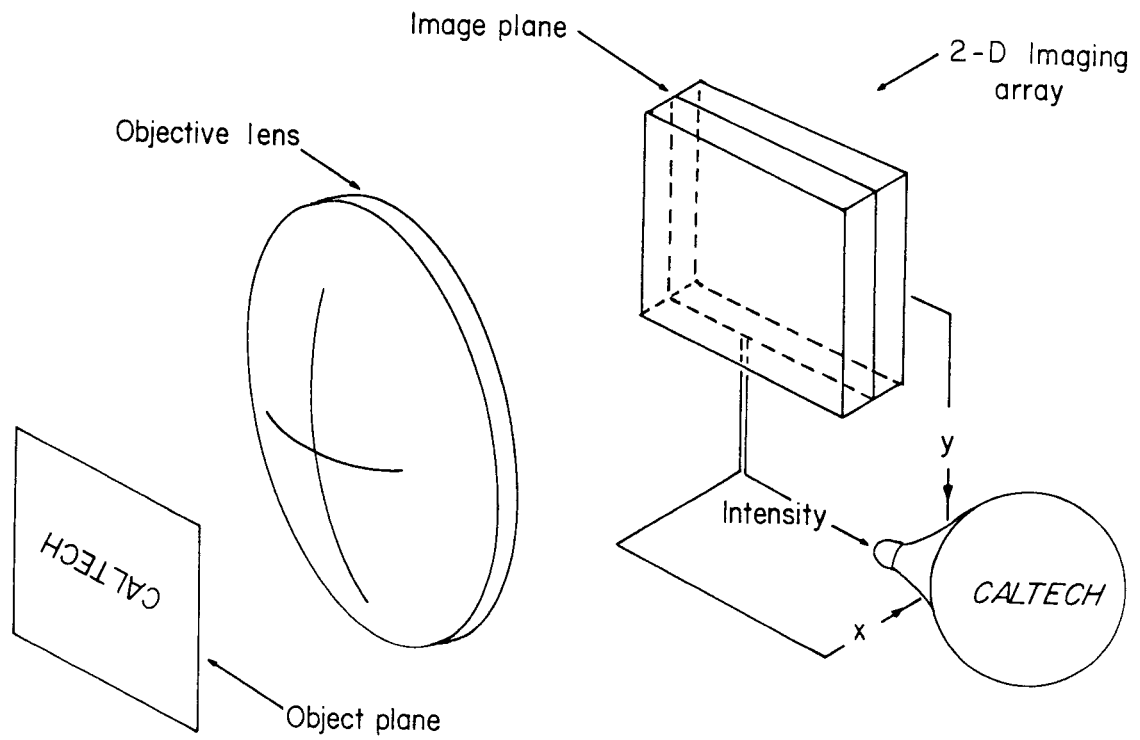


Figure 1.5. Two-dimensional imaging system.

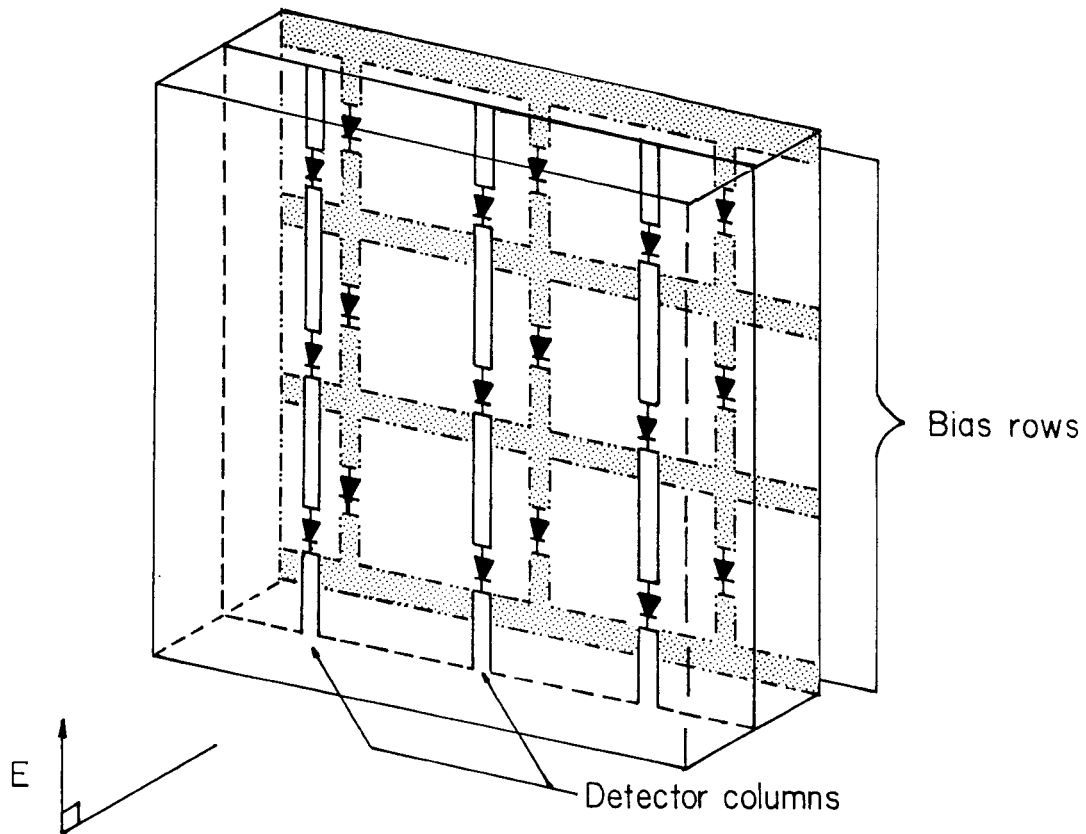


Figure 1.6. Two-dimensional imaging array.

diode grid at millimeter wavelengths. The idea is to mix the signal-beam with a normally incident pump-beam on the surface of the varactor diode-grid, so that a hologram in the form of reflectivity modulation is developed. During this mixing process, the phase-conjugated beam is generated and the replica of the signal beam is reproduced as the conjugated beam evolves through the phase-distorting medium. Also signal amplification can be obtained.

1.2 Overview of Thesis

The numerous potential applications of the diode-grid is the motivation behind this research. The purpose of this thesis is to lay down the groundwork and to demonstrate feasibility. Chapter 2 presents design considerations, a model of the diode-grid, and computer simulations of the reflection phase-shifter for electronic beam-steering. The approach is based on a transmission-line equivalent circuit. In designing the grid structures, together with the substrate, dielectric slabs, filters and mirrors, a computer-aided design program was developed to provide an interactive environment for the user and to form a basis for comparing theoretical and experimental results. The software documentation is given in the appendix.

Chapter 3 describes the design of a truncated hyperabrupt doping distribution for making a Schottky-barrier varactor diode. Essential parts of the diode fabrication are discussed. Zah's self-aligning process as described by Zah *et al.* [22] is used to fabricate the aluminum Schottky diodes. Monolithic diode-grids have been fabricated on 2 cm by 3 cm gallium-arsenide wafers with 2000 varactor diodes. Although the diode fabrication yield as high as 98 % has been achieved, the remaining bad diodes, which tend to be short circuits, usually render the grid useless. Therefore, a liquid-crystal detection technique was developed to identify the shorted diodes, which are subsequently removed by an ultrasonic probe.

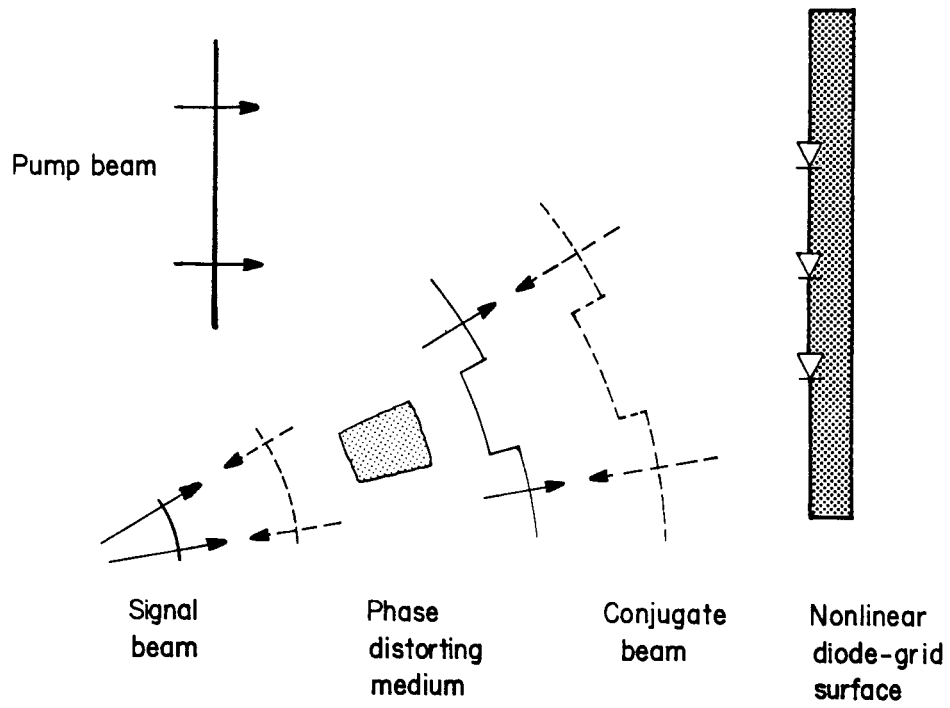


Figure 1.7. Phase-conjugating diode-grid.

The diode series resistance is calculated from the current-voltage measurement at DC, and the doping distribution is extracted from the capacitance-voltage measurement at 1 MHz. Detailed procedures are given in the appendix.

Chapter 4 begins with a survey of possible existing experimental methods for testing the diode-grid and concludes that none was practical for testing our diode-grid samples because the sample sizes are typically small and irregular in a laboratory environment. Therefore, a novel small aperture quasi-optical reflectometer was developed to meet this need. It uses a wave-front division interference technique to measure the reflection coefficient of a surface. The technique was first applied to measure the reflection coefficient of bismuth on quartz. The purpose was to get an indication of its validity and limitations. Then it was used for testing the diode-grids. Results measured at 90 GHz are curve-fitted with a theory based on a transmission-line equivalent circuit model. The best-fit grid parameters are compared with parameters measured at low frequency.

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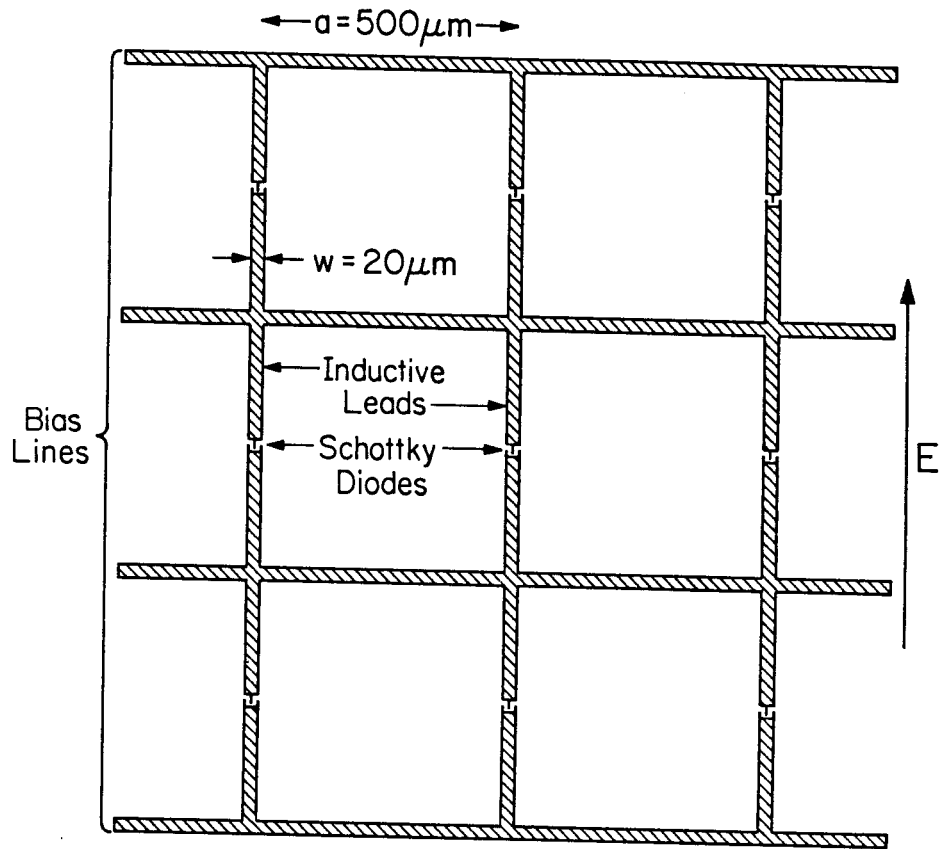
Chapter 2

Design and Analysis of Diode-Grid Phase Shifter

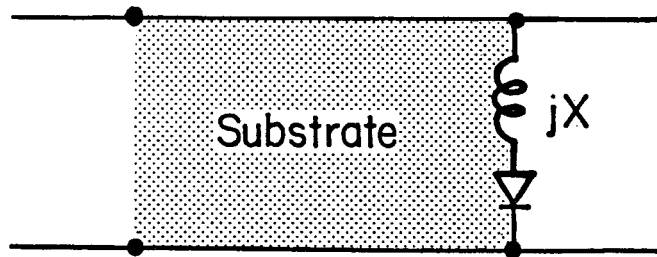
Many types of periodic grids have been used in infrared and millimeter-wave applications, including the Jerusalem-cross for band-reject filters [1], discs for artificial dielectrics [2], inductive wires for polarizers [3], inductive and capacitive strips for polarization independent beam splitters [4] and multiplexers [5], and metal meshes for output couplers for lasers [6]. The periodic grid that we use in designing our diode-grid phase shifter is a square mesh of metal strips on a gallium-arsenide substrate as shown in figure 2.1a. The vertical strips add inductance to cancel the diode capacitance. The horizontal strips provide the bias but should not otherwise affect the circuit. Design considerations include the grid period, angle of incidence, and dielectric constant. The design approach is based on an equivalent circuit model and the transmission-line theory. In designing the diode-grid phase shifter for electronic beam-steering, we developed a computer-aided design program to provide an interactive environment for the designer and to form a basis for comparing theoretical and experimental results.

2.1 Diode-Grid Model

We model the diode-grid with an equivalent circuit based on the transmission-line theory because it is relatively easy to incorporate both the diode model and supporting substrate into the analysis. Figure 2.1b shows a simple model of the diode-grid on a dielectric slab. The grid is represented by an inductor in series with a diode, and the substrate is represented by a section of transmission line with a characteristic impedance equal to the wave impedance in the dielectric. The inductance-per-unit length of the metal strip for normal incidence is given



(a)



(b)

Figure 2.1. (a) Grid dimensions for a 90 GHz programmable phase shifter. The incident electric field is assumed to be vertically polarized. (b) Transmission-line model of an inductive grid, loaded with diodes, and supported by a substrate.

by a quasi-static approximation,

$$L = \frac{\mu_o}{2\pi} \ln \left[\csc \left(\frac{\pi w}{2a} \right) \right], \quad (2.1)$$

where w is the strip width, a is the grid period, and μ_o is the magnetic permeability. MacFarlane [7] derived this formula based on conformal mapping of an inductive grating in a parallel plane metal waveguide. It does not take into account the angle of incidence, the polarization, the effect of the dielectric interface, the parasitic capacitance across the diode, or the effect of the horizontal cross strip. These effects have been considered in the literature [8,9]. Since they amount to a correction of less than 10 %, they have been neglected in our initial designs of the diode-grid. This enables us to see the effects of design changes faster and therefore to get a quicker turn-around time in doing the design of the diode-grid phase shifter.

The grid period, a , should be somewhat smaller than a substrate wavelength to avoid exciting substrate modes. We can decide how much smaller by considering figure 2.2, which shows the spatial frequencies of the grid. The spatial frequencies for the incident radiation lie within a circle of radius $1/\lambda_o$ centered at the origin. This radiation excites currents, which, because of the periodic nature of the grid, have spatial frequencies that lie within similar circles centered on the reciprocal lattice points of the grid. The reciprocal lattice is a square lattice, with a period of $1/a$. The spatial frequencies for the substrate modes lie within the doughnut-shaped regions that are also centered on the reciprocal lattice points. The inner radius of the doughnuts is $1/\lambda_o$ and the outer radius is n/λ_o , where n is the refractive index of the substrate. To avoid exciting substrate modes, the small circles should not intersect the doughnuts. This means the grid period

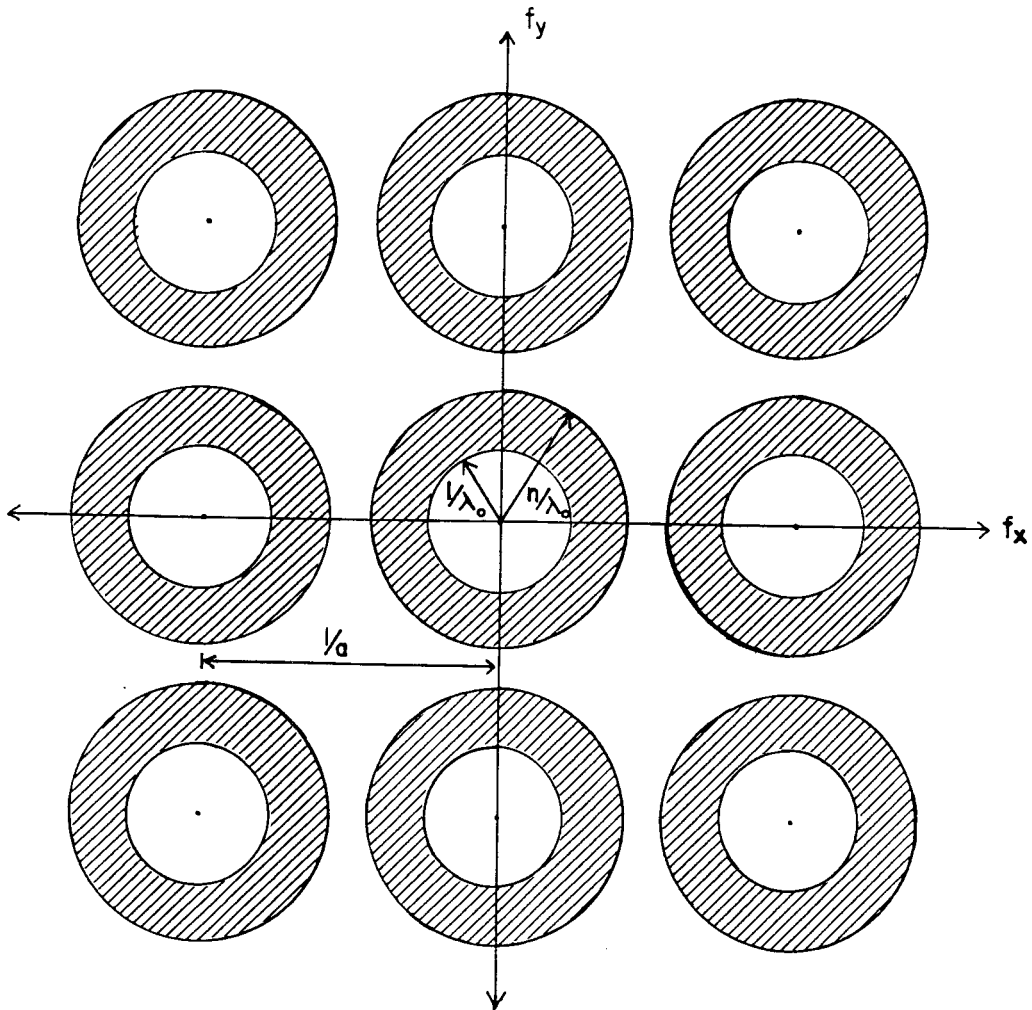


Figure 2.2. Reciprocal lattice of a square grid for considering the excitation of substrate modes.

should satisfy

$$a < \frac{\lambda_0}{(n+1)}. \quad (2.2)$$

For gallium-arsenide, which has a refractive index of 3.6, the grid period should be less than $0.22\lambda_0$.

2.2 Diode-Grid Phase Shifter

Figure 2.3 shows the diode-grid phase shifter design. It consists of a fused-quartz cover, two diode-grids, and a metal mirror. The circuit is analogous to Garver's microwave phase shifter [10]. The quartz layer acts as a protective cover as well as an impedance transformer. The metal mirror prevents radiation from escaping and serves as a heat sink and mechanical support. Another inherent feature of this design is that the mirror also shorts out the second harmonic at the diode-grids. This reduces conversion losses to the second harmonic; therefore, it is more attractive for high power-operation.

Figure 2.4a shows the transmission-line model. The mirror is electrically an open-circuit, because it is a quarter-wavelength behind the back diode-grid. At the front diode-grid, the back grid appears electrically as a parallel load, but with the impedance inverted (figure 2.4b). The total normalized reactance X_t is the parallel combination of jX and $1/jX$, or

$$X_t = \frac{X}{(1 - X^2)}. \quad (2.3)$$

X_t ranges from $-\infty$ as X approaches -1 , to $+\infty$ as X approaches $+1$. This allows a full 360° phase shift as the normalized grid reactance goes from -1 to $+1$. The grid reactance, X , is normalized relative to the characteristic impedance of the substrate. In gallium-arsenide this corresponds to a grid reactance sweep between $-107\ \Omega$ to $+107\ \Omega$. For the grid dimensions in figure 2.1, the inductive

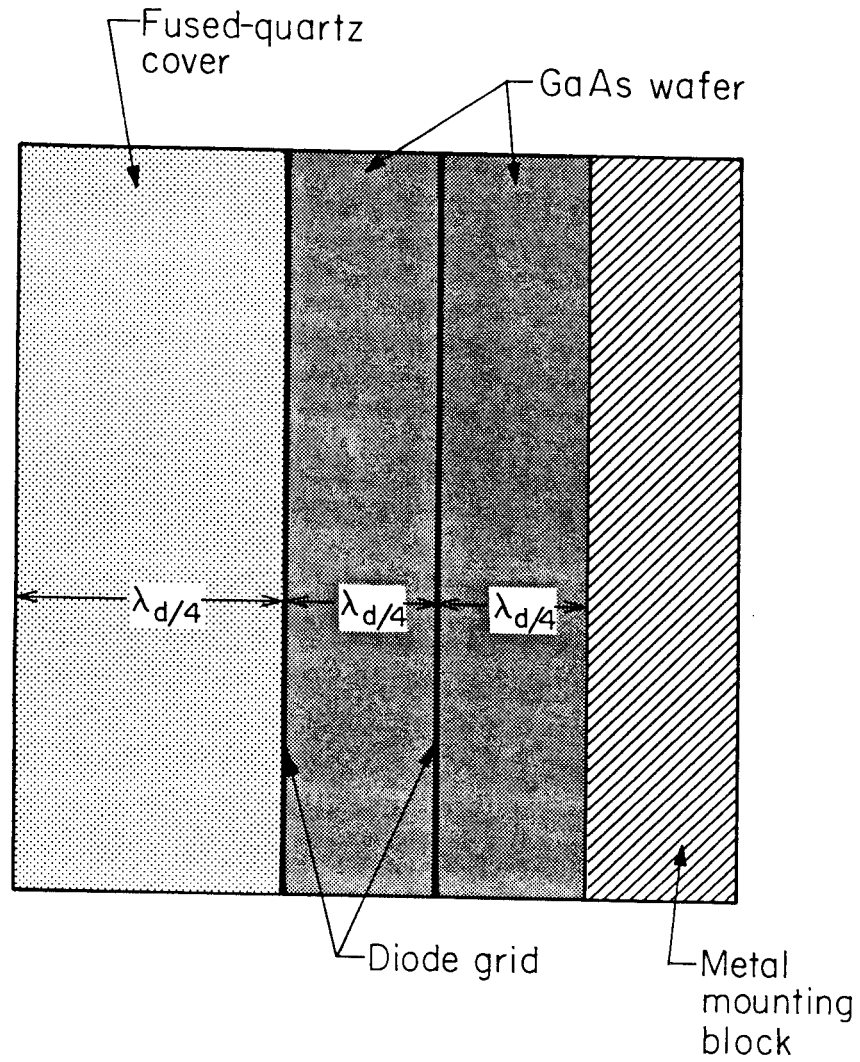
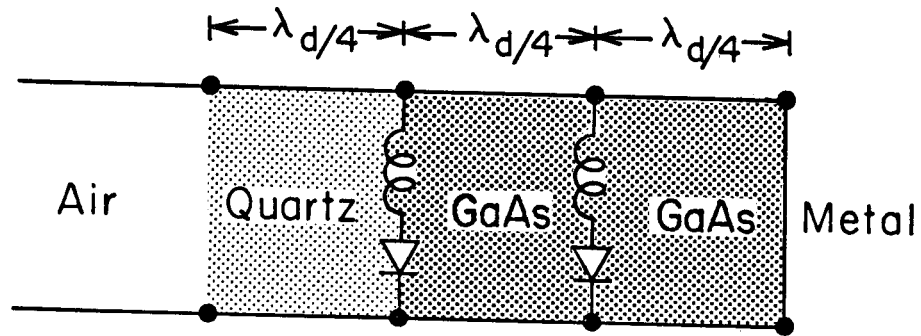
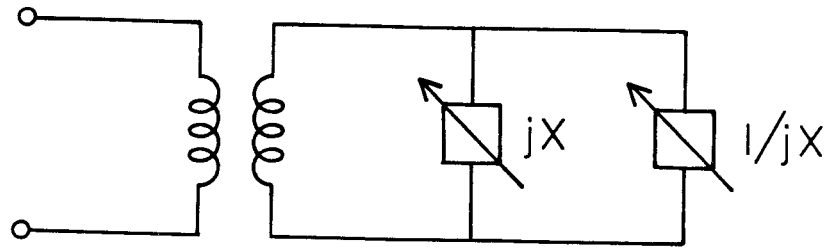


Figure 2.3. Side view of the programmable diode-grid phase shifter for electronic beam steering.



(a)



(b)

Figure 2.4. (a) Transmission-line model of the diode-grid phase shifter. (b) An idealized equivalent circuit for the diode-grid phase shifter.

reactance due to the strip is 160Ω . This means that the diode capacitance should vary from 7 fF to 35 fF. This type of capacitance ratio has been demonstrated with the hyperabrupt junction Schottky varactor diode [11].

The design allows a spatial phase variation in one dimension only. Two-dimensional phase variation can be achieved by biasing the diodes individually rather than row by row, or by cascading two such phase shifters. The design also does not control sidelobes because there is no adjustment for amplitudes. However, it should be possible to reduce sidelobes by tapering the radiation feeding the array with an externally designed collimating lens.

2.3 Computer-Aided Design and Analysis – TRAP

Since calculations of reflection modulus and phase of multi-layered media are tedious and time consuming, we have developed a computer-aided design program that provides an interactive environment for the user to design his circuits and to compare the theoretical and experimental results. TRAP (transmission, reflection, absorption, and phase) was developed to analyze the square grid, together with the substrate, dielectric slabs, filters, and mirror. It is an interactive graphics program written in Turbo Pascal for the IBM personal computer. The user types a descriptive command line via the line editor in TRAP. Commands may include lossy dielectrics, lumped elements, and a mirror. The angle of incidence, polarization, wavelength, and layer thicknesses can be varied linearly. The calculated results are displayed as the computations are made. Three real-time keyboard commands are available to stop, speed up, or slow down the simulation. On the average it takes about 30 seconds per layer to complete a plot on an IBM-XT. The programmed optimization routine is based on a multi-dimensional simplex algorithm [12]. It allows the user to fit a model based on transmission-line theory to the measured reflectance and phase of reflection from a multi-layered

medium. TRAP calculates the transmittance, absorptance, reflection coefficient of a layered medium by generalizing Berning's algorithm [13] to include the effects due to periodic grids at the interfaces. This algorithm is numerically more efficient than the conventional cascade matrix approach. Equivalent circuit models are derived from physical dimensions of thin screens such as the square grid. One model is based on MacFarlane's quasi-static formula of a strip [7], and the other model is based on a modification of Eisenhart and Khan's theory of a post in a waveguide [9]. In addition the circuit model of a Jerusalem-cross based on Arnaud and Pelow [1] is also available.

The solution for plane wave propagation in a multi-layered medium is a well known boundary value problem in wave analysis. However, in electromagnetic engineering, it is more desirable to make an analogy between the plane wave solutions and the waves along a transmission-line. The electric and magnetic fields are analogous to the voltage and current in a transmission line, and the ratio is called the impedance. The analogy is useful because it allows the designer to take advantage of existing impedance matching techniques in the field of transmission line design and analysis. To exploit this analogy fully, the concept of admittance, the ratio of magnetic field to electric field, is used because the multi-layered media is plane-parallel, which means its equivalent circuit is parallel. Therefore, it is more desirable to use admittance because parallel admittances add. The method of calculation for wave reflection from multi-layered media is discussed next to establish the notation.

Figure 2.5 shows a schematic of a multi-layered medium with thin structures such as a lossy film or a periodic grid at the interface between two layers. The waves are incident from the left. The boundaries are labeled from 0 to N , with 0 being the interface of the incident medium and N being the interface of the final medium. The angle of incidence is θ_0 , and the complex refractive index

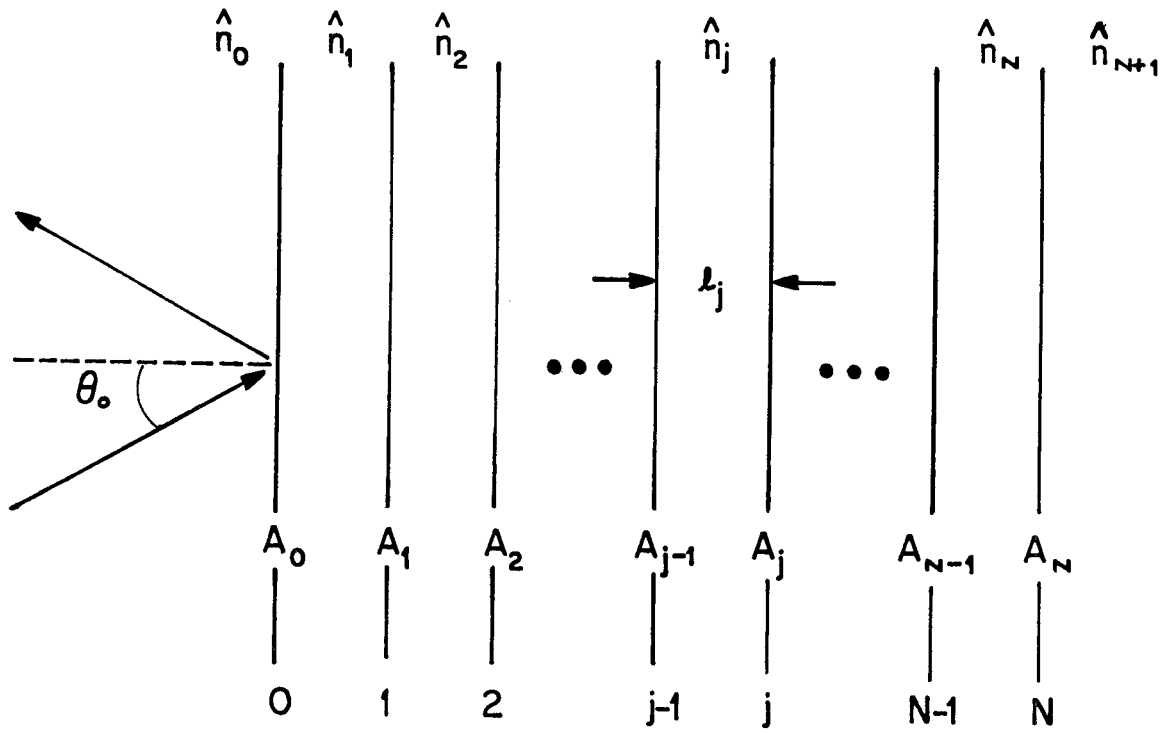


Figure 2.5. Schematic diagram of a multi-layered medium.

of the j^{th} layer is $\hat{n}_j = n_j + ik_j$, where n_j is the refractive index and k_j is the absorption index of the j^{th} layer. The physical thickness of the j^{th} layer is l_j , and its electrical length is $\phi_j = 2\pi\hat{n}_j l_j$. All admittances are normalized to the characteristic admittance in the vacuum. The normalized admittance for a thin structure at the j^{th} interface is y_j . The calculation of the reflection coefficient, transmittance, and absorptance begins from the final medium on the right and works its way back toward the incident medium on the left. The normalized admittance of the final medium is initialized to $Y_N = \hat{n}_{N+1} + y_N$. The normalized admittance looking from the j^{th} interface toward the right is given by the recursive relation,

$$Y_{j-1} = a_{j-1} + ib_{j-1} = \hat{n}_{jp} \frac{Y_j \cos \phi_j + i\hat{n}_{jp} \sin \phi_j}{\hat{n}_{jp} \cos \phi_j + iY_j \sin \phi_j} + y_{j-1}, \quad (2.4a)$$

where

$$\hat{n}_{jp} = \begin{cases} \hat{n}_j \cos \theta_j & \text{for TE polarization} \\ \hat{n}_j / \cos \theta_j & \text{for TM polarization} \end{cases}, \quad (2.4b)$$

and θ_j satisfies Snell's Law of Refraction,

$$\hat{n}_o \sin \theta_o = \hat{n}_j \sin \theta_j. \quad (2.4c)$$

The reflection coefficient at the input surface of the multi-layered medium is calculated from

$$\rho = \frac{\hat{n}_o - Y_o}{\hat{n}_o + Y_o}. \quad (2.5)$$

The transmittance at the output surface is given by

$$T = (1 - |\rho|^2) \prod_{j=1}^N \psi_j, \quad (2.6a)$$

where ψ_j is the ratio of time average of the magnitude of Poynting's vector at the j^{th} and $(j - 1)^{\text{th}}$ interfaces and is given by the formula,

$$\psi_j = \frac{a_j}{a_{j-1} |\cos \phi_j + iY_j \sin \theta_j / \hat{n}_j|^2}. \quad (2.6b)$$

Using the Law of the Conservation of Energy, the total absorptance in the layered medium is given as,

$$A = 1 - T - |\rho|^2. \quad (2.7)$$

2.4 Simulated Performance of Diode-Grid Phase Shifter

Figure 2.6 shows a simulation of the diode-grid phase shifter. The assumed metal parameters are based on the skin-effect formulas for gold. Dielectric properties are taken from Afsar and Button's data [14]. The result indicates that the phase of reflection varies linearly from -180° to $+180^\circ$ as the grid reactance sweeps from a normalized reactance of -1 to $+1$ (-107Ω to $+107 \Omega$ for the gallium-arsenide substrate). The reflection efficiency varies from a low of 0.49 to a high of 0.57, with an average loss of 2.7 dB. Of this loss, all but a tenth of a dB is due to the series resistance of the diode (assumed to be 10Ω).

Figure 2.7 shows a family of diode-grid phase shifter performances for normal incidence in air. Gallium-arsenide is assumed for the diode-grid substrate. The grid reactance is assumed to vary from -107Ω to $+107 \Omega$, and resistance is assumed to vary from 10Ω to 50Ω). The refractive index of the quarter-wavelength dielectric cover is assumed to vary from 2.26 to 2.34. These indices satisfy the condition,

$$2 \left(\frac{Z_c}{Z_s} \right)^4 (R^2 + Z_s^2) = Z_i^2, \quad (2.8)$$

where the Z_s is the effective characteristic wave impedance of the diode-grid substrate, Z_c is the effective characteristic wave impedance of the quarter-wave

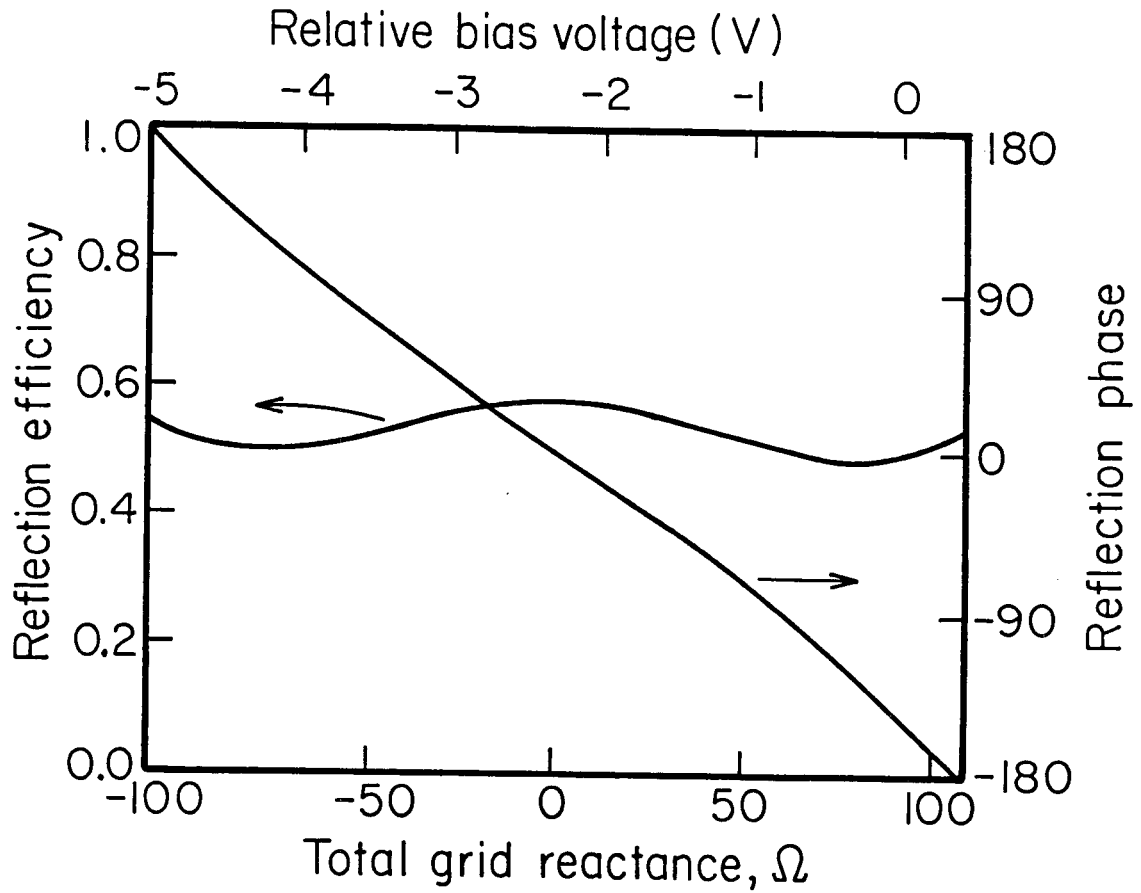


Figure 2.6. Simulated results of the programmable diode-grid phase shifter. The initial quarter-wave transformer layer is fused quartz, the angle of incidence in the air is 45° , and the polarization is *TE*.

dielectric cover, Z_i is the effective characteristic wave impedance of the incident medium, and R is the series resistance of the diode-grid. It is interesting to note that the use of these refractive indices equalizes the reflection losses by having the impedance looking into the diode-grid phase shifter to revolve around the center of the Smith chart. Also, the required refractive indices are slightly larger than the refractive index usually required for an anti-reflection coating. Although the required refractive indices are not too practical from the material point of view, the same effect of equalizing the reflection loss can be obtained by adjusting the angle of incidence in air. When this is done for TE polarized waves, the angle of incidence is about 36° for crystal-quartz, and about 52° for fused quartz. From Figure 2.7 we can see that the maximum and minimum reflection losses occurs around 0° and $\pm 110^\circ$, respectively. Figure 2.8 shows these reflection losses as a function of $R/\Delta X$, the ratio of the real part to the total change in the imaginary part of the diode-grid impedance. They vary almost linearly in $R/\Delta X$ from 0.05 to 0.5. The slope for the maximum loss curve is 29.5, and for the minimum loss curve, 24.2.

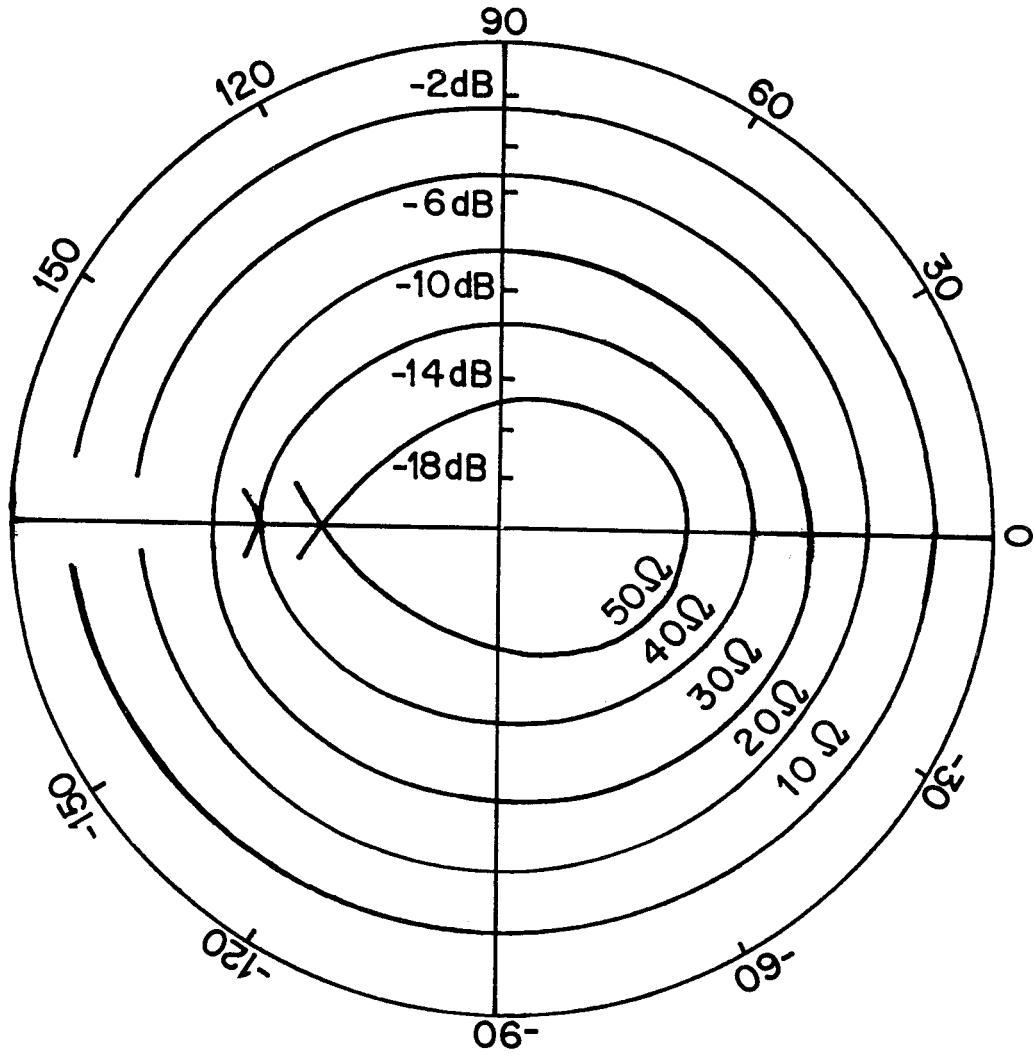


Figure 2.7. Reflection loss of the diode-grid phase shifter

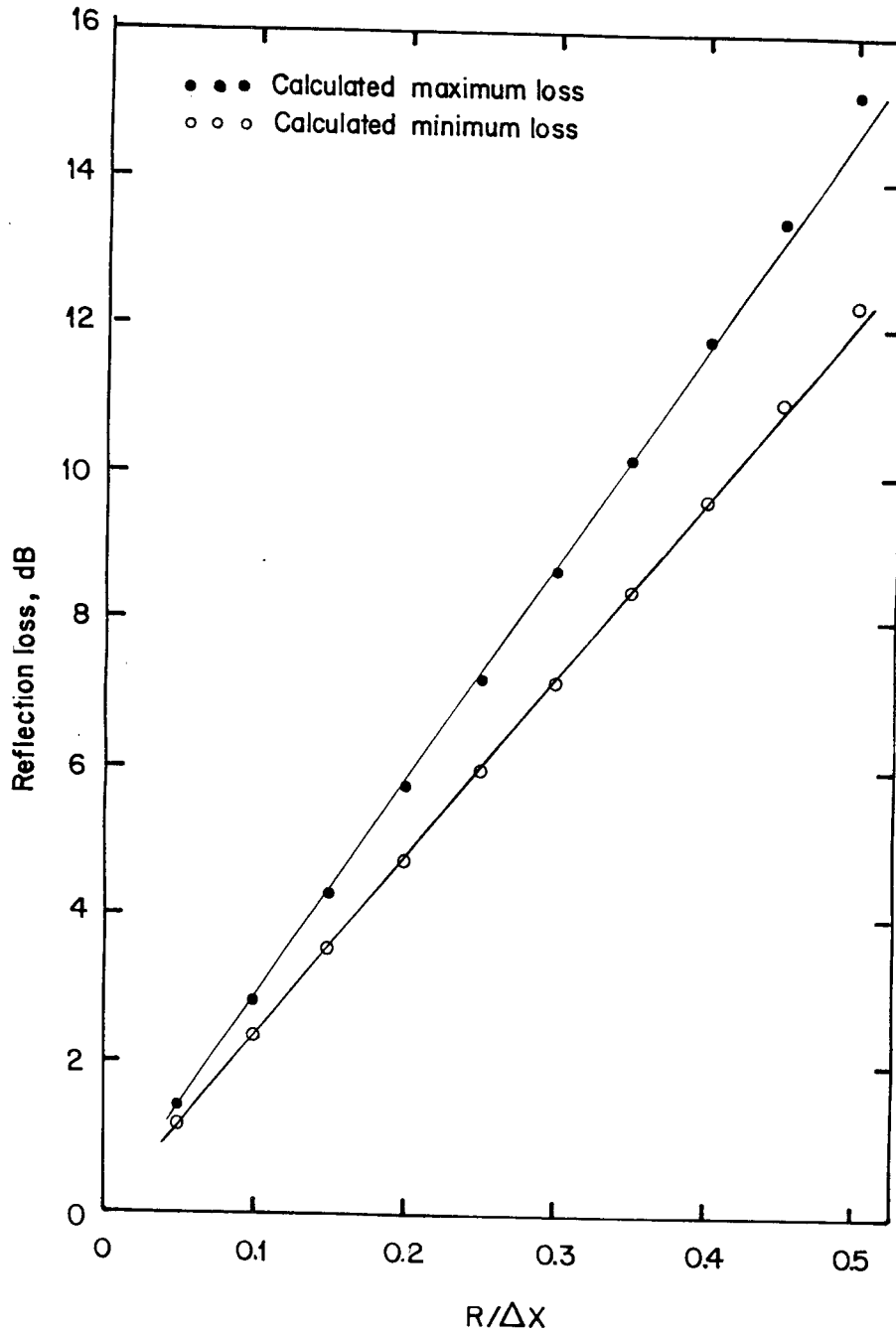


Figure 2.8. Reflectance loss of the diode-grid phase shifter as a function of the ratio of diode-grid series resistance to reactance.

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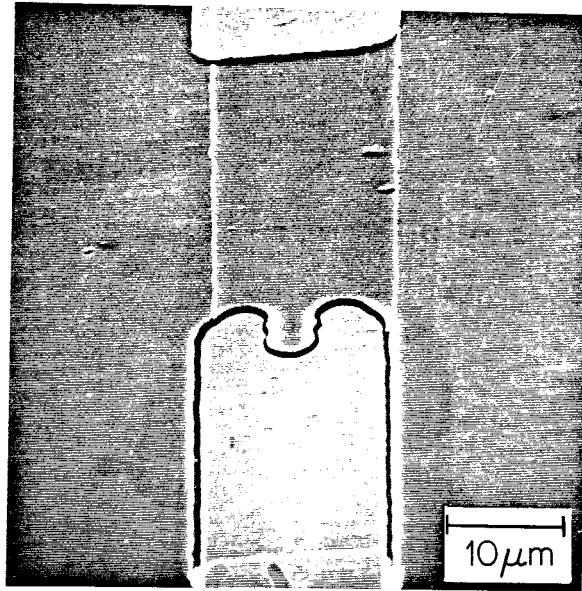
Chapter 3

Fabrication of Diode-Grids

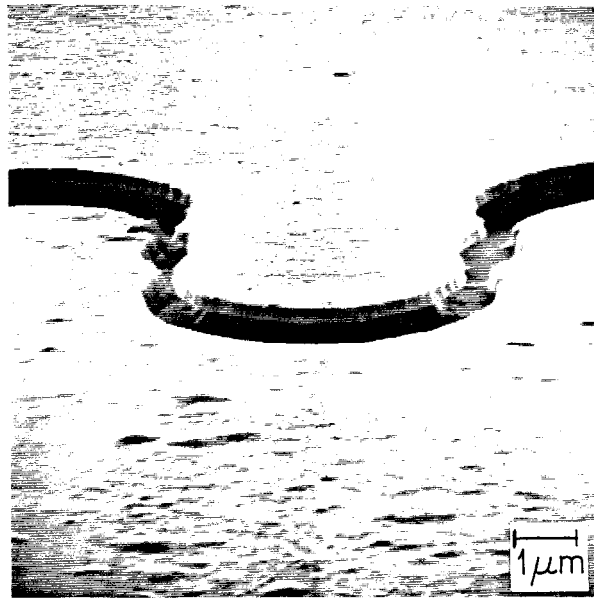
Monolithic diode-grids have been fabricated on 2 cm by 3 cm gallium-arsenide (GaAs) wafers with 2000 aluminum Schottky-barrier varactor diodes. The self-aligning technique, which Zah had developed in our group [1], is used to fabricate the diodes. The best fabrication yield for individual diodes in an array was 98 %. A liquid crystal detection technique was developed to identify the diodes that are shorted in the grid, and subsequently an ultrasonic probe is used to remove the bad diodes. The design of the varactor diode with a truncated-hyperabrupt doping profile is discussed. The doping profile is measured with a mercury probe. The diode series resistance is calculated from the current-voltage (IV) characteristics measured at DC, and the capacitance parameters are calculated from the capacitance-voltage (CV) characteristics measured at 1 MHz. The detailed procedures are given in the appendix.

3.1 Design of a Hyperabrupt Schottky Varactor Diode

Figure 3.1 shows SEM photographs of the Schottky-barrier varactor diode on GaAs. The diode consists of a Schottky contact with a shape of a strip, which is surrounded by an ohmic contact. The width of the strip is defined by a self-aligning technique, and its length is defined by proton bombardment. This is the key of Zah's process, which enables us to make a small planar diode for millimeter-wave applications. The area of the strip is about $18 \mu\text{m}^2$; its small rectangular geometry gives a low spreading resistance since the periphery-to-area ratio is high. The metal for the Schottky contact is aluminum (Al), and the metal for the ohmic contact is gold-germanium/nickel/gold (AuGe/Ni/Au). The metalization extends from the diode and becomes part of inductive lead of the



(a)



(b)

Figure 3.1. SEM photographs of a planar Schottky-barrier varactor diode. (a) Diode is located at the tip of the strip. (b) Close-up view of diode.

diode-grid. The other part of the inductive lead is gold. For a large capacitance variation, the varactor is designed with a hyperabrupt doping profile; that is, the net doping concentration of the epitaxy decreases with the distance from the metal semiconductor interface.

The three most important circuit parameters of a varactor are the breakdown voltage V_b , the zero bias capacitance C_o , and the series resistance R_s . They affect the amount of power the varactor will handle, the level of impedance the varactor will present, and the amount of power the varactor will dissipate in a circuit. These parameters can be calculated and optimized when the doping concentration as a function of the distance from the junction is specified. Norwood and Shatz [2] analyzed an ideal hyperabrupt doping profile that is described mathematically by an m^{th} power law. In practice, a truncated-hyperabrupt doping profile must be used. Figure 3.2 shows the truncated-hyperabrupt doping profile. The doping concentration is given by

$$N_d = \begin{cases} N_o & 0 \leq x \leq x_o \\ N_o \left(\frac{x}{x_o}\right)^m & x_o \leq x \leq T_{epi} \end{cases}, \quad (3.1)$$

where x_o is the zero bias depletion width, N_o is the doping concentration at the surface, T_{epi} is the epitaxial thickness, and m is the doping profile exponent. The method of design is based on Lundien *et al.*'s [3] design algorithm of an exponentially retrograded doping profile. In the *depletion approximation*, the one-dimensional Poisson equation,

$$\frac{d^2 \phi}{dx^2} = -\frac{q}{\epsilon_s} N_d, \quad (3.2)$$

is integrated, where ϕ is the electric potential, q is the electronic charge, and ϵ_s is the dielectric permittivity of the semiconductor. The boundary conditions are

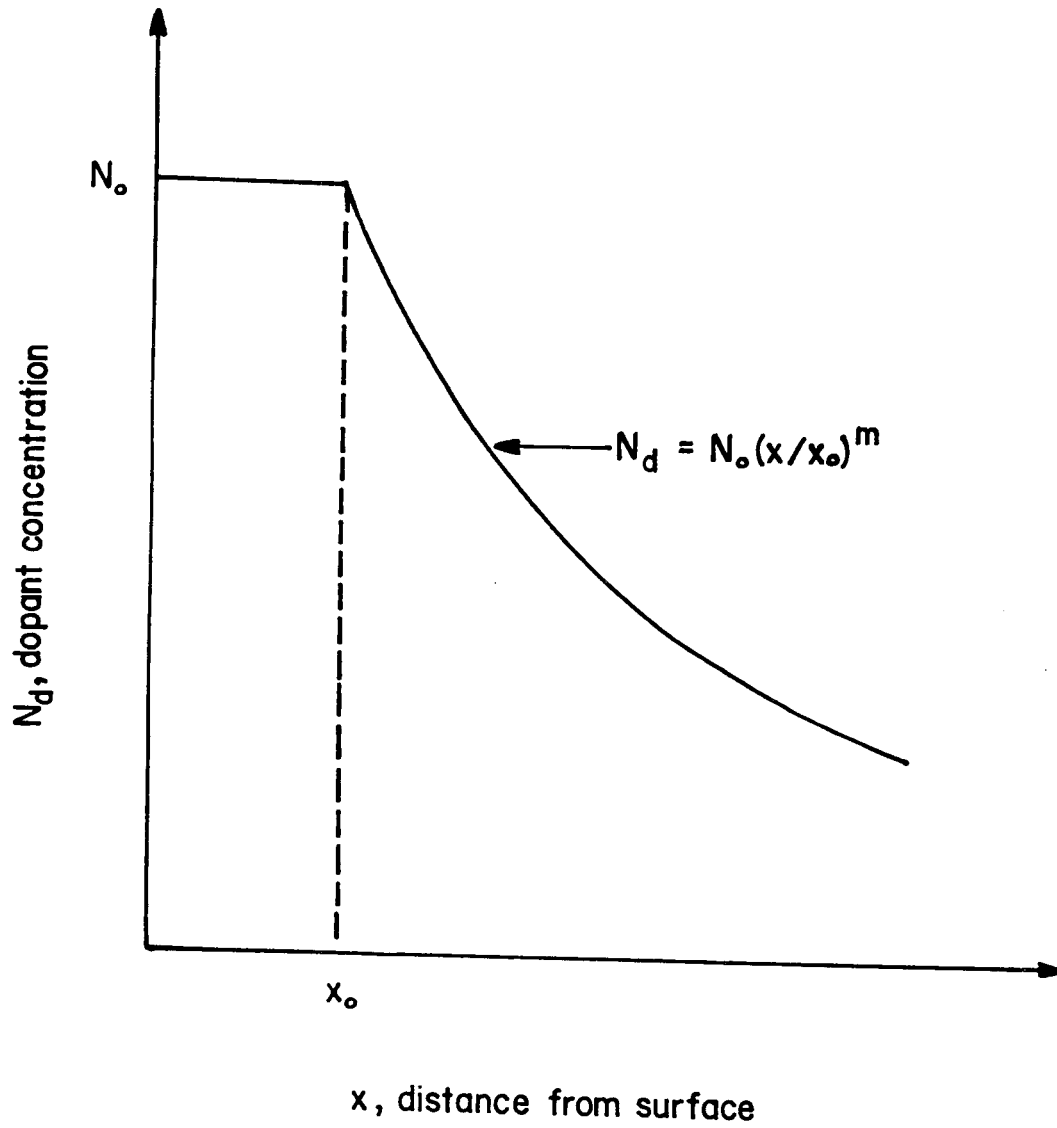


Figure 3.2. A truncated-hyperabrupt impurity doping distribution designed for a varactor diode.

$\phi(x=0) = 0$ and $\phi(x=W_{dep}) = \phi_j - V$, where ϕ_j is the junction potential, W_{dep} is the depletion width of the space-charge region, and V is the applied voltage. This leads to a complicated algebraic relation, $V = F(W_{dep})$, which is used to compute the corresponding CV relation

$$C = \epsilon_s \frac{A}{W_{dep}}, \quad (3.3)$$

where A is area of the varactor. The calculated CV characteristic is checked for self-consistency by the following expression [4],

$$N_d = \frac{\left(\frac{-C^3}{q\epsilon_s A^2}\right)}{\left(\frac{dC}{dV}\right)}. \quad (3.4)$$

The profile computed from the above is compared with the original profile. The resistance due to the undepleted epitaxial layer is calculated from,

$$R_{epi} = \int_{W_{dep}}^{T_{epi}} \frac{dx}{q\mu_n N_d A}, \quad (3.5)$$

where μ_n is the electron mobility, and T_{epi} is the epitaxial thickness. An empirical expression for the electron mobility is used

$$\mu_n = \frac{10^4}{1 + \sqrt{\frac{N_d}{10^{17}}}}, \quad (3.6)$$

where N_d is in units of cm^{-3} and μ_n is in units of $\text{cm}^2(\text{Vs})^{-1}$ [5]. The breakdown voltage is computed as the applied voltage at which the ionization integral becomes unity,

$$I = \int_0^{W_{epi}} A \exp \left[- \left(\frac{b}{E(x)} \right)^2 \right] dx = 1, \quad (3.7)$$

where $E(x)$ is the electric field in units of $\text{V}(\text{cm})^{-1}$, $A = 3.5 \times 10^5 \text{ cm}^{-1}$, and $b = 6.85 \times 10^5 \text{ V}(\text{cm})^{-1}$ [6]. The integrals are integrated numerically with an algorithm based on Simpson's rule [7]. The electric field is obtained from the one-dimensional Poisson equation. The boundary conditions are imposed by matching the fields at $x = x_o$ and by setting the field to zero at $x = W_{epi}$. The expressions obtained are checked with those obtained from Gauss's law.

A simple graphical procedure is used as a guide to design the truncated-hyperabrupt profile for a varactor. The design parameters are the surface concentration (N_o), the doping profile exponent (m), the zero bias depletion width (x_o), and the epitaxial thickness. A figure of merit for a varactor is the dynamic cutoff frequency, which is defined by Penfield and Rafus [8] to be,

$$f_c = \frac{S_{max} - S_{min}}{2\pi R_s}, \quad (3.8)$$

where S_{max} is the reciprocal of minimum capacitance, S_{min} is the reciprocal of the maximum capacitance, and R_s is the series resistance of the varactor. Other quantities including series resistance, capacitance tuning ratio, and avalanche integral are also calculated to indicate design margins and tradeoffs.

Figure 3.3 shows a contour plot of dynamic cutoff frequency as a function of surface concentration and doping profile exponent. An approximated junction potential for Al on GaAs is 0.94 V from Eglash *et al.* [9]. The maximum capacitance is taken to be the zero bias capacitance, which is assumed to be 30 fF because this gives the desired reactance at 90 GHz for our experiments. The epitaxial thickness is $0.65 \mu\text{m}$. This thickness is chosen because it is thick enough to give a capacitance ratio of 5 and thin enough for proton isolation. The calculation also assumes a parasitic resistance of 7Ω and a parasitic capacitance of 3 fF. The solid line indicates that the ionization integral is unity ($I = 1$) and divides

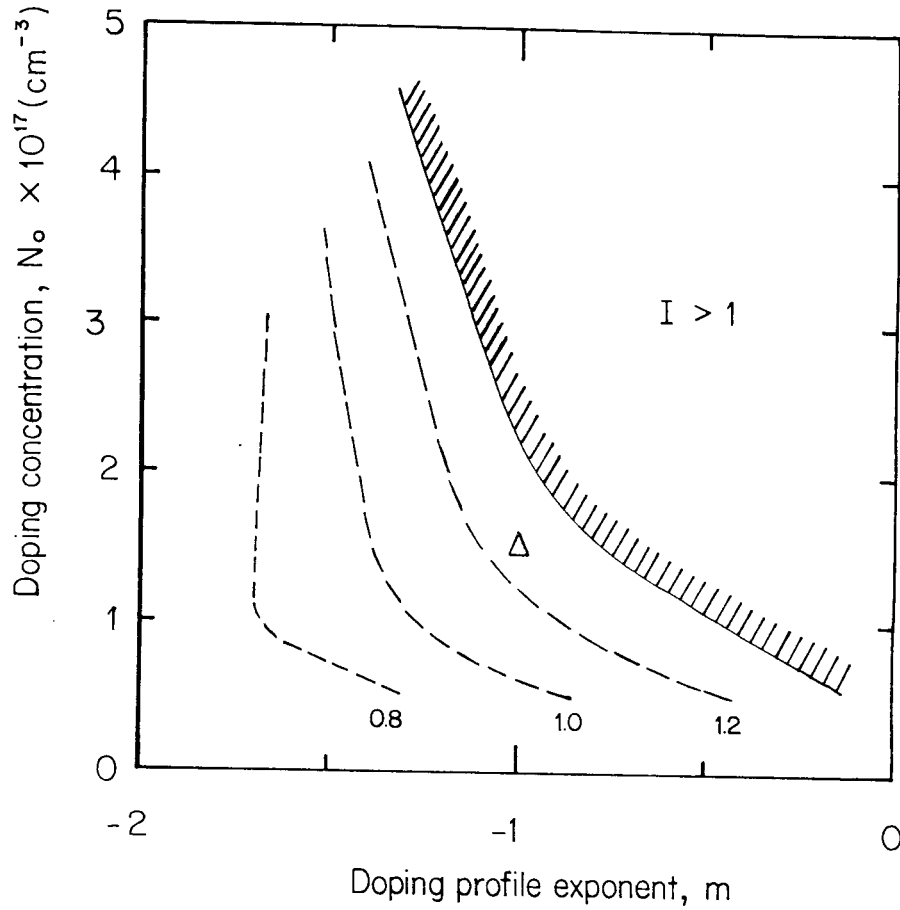


Figure 3.3. A contour plot of dynamic cutoff frequency in units of THz as a function of surface concentration and doping profile exponent in the region where the ionization integral is less than unity ($I < 1$). The solid line corresponds to the ($I = 1$) and the shaded region corresponds to the ($I > 1$).

the space into safe and unsafe regions of operation. Safe means that the entire epitaxial layer can be depleted without avalanche breakdown or with ($I < 1$). Qualitatively speaking, the closer N_o and m approach the solid line, the greater the chance of reaching avalanche breakdown. The bigger N_o is, the bigger the capacitance ratio, which is good for phase-shifting, while ($-0.5 < m < -0.3$) is more favorable for second harmonic conversion efficiency [10]. The final design of the truncated-hyperabrupt doping profile varactor is $N_o = 1.5 \times 10^{17} \text{ cm}^{-3}$, $m = 1.0$, $x_o = 0.1 \mu\text{m}$, and $T_{epi} = 0.65 \mu\text{m}$. An extra $0.05 \mu\text{m}$ is added as safety margin for back depletion from the n^+ layer. The thickness of the n^+ layer is $1.8 \mu\text{m}$. This gives a total epitaxial thickness of $2.5 \mu\text{m}$, which is close to the limit of the proton isolation capability available to us. The doping concentration for the n^+ layer is designed to be $3 \times 10^{18} \text{ cm}^{-3}$.

A mercury probe is used to measure the CV characteristic and the doping profile [10]. Figure 3.4 shows the measured CV characteristic. The ratio of the capacitance at zero bias to the capacitance at breakdown is about 4.5 and this is close to what the simulation predicts. The breakdown voltage is about 9 V, and this corresponds to what the simulation predicts when the ionization integral is about 0.4. This comparison is based on averaging the calculated results at $m = -1.1$ and $m = -0.9$. Figure 3.5 shows the corresponding measured doping profile and compares it to the profiles that were designed and measured with a *Polaron* profiler. Basically, a back-to-back Schottky diode is formed on the surface of the wafer with a small and a large mercury dot, which are held there by a vacuum. Then the small signal capacitance of this diode structure is measured as a function of the DC bias. The advantage of this technique is that it is non-destructive. Occasionally, the doping profile is available from a *Polaron* profiler for comparison. This provides a more complete measure because it includes the n^+ layer; however, this is a destructive technique since it requires etching away

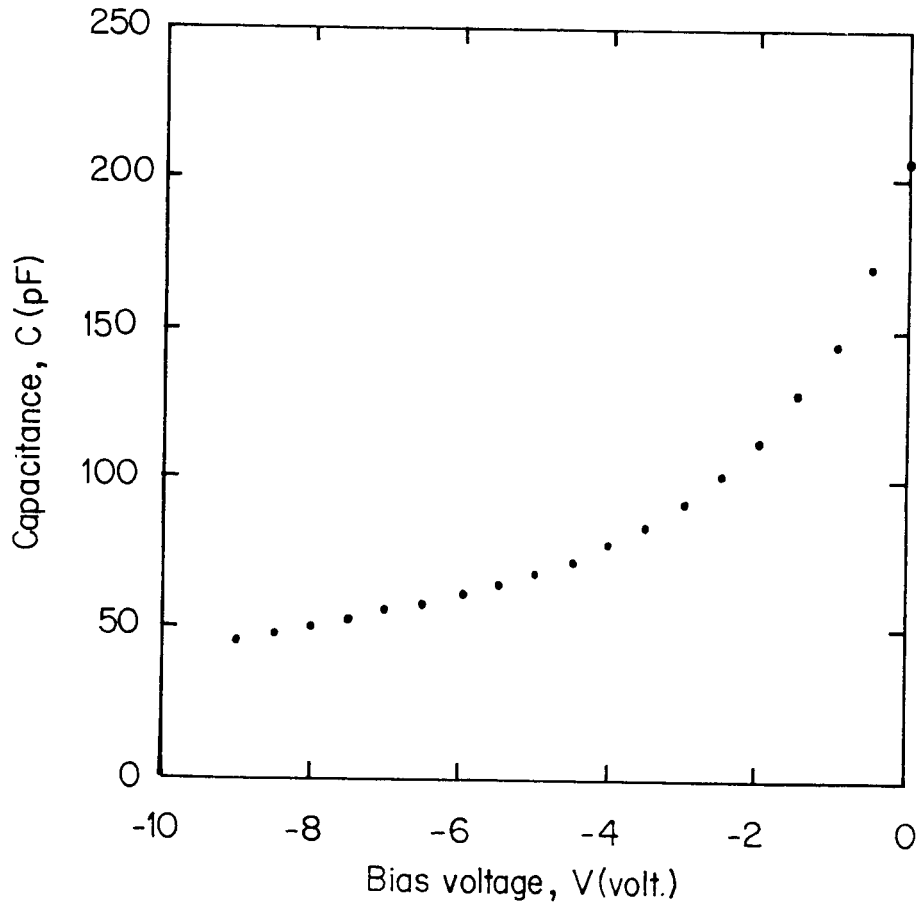


Figure 3.4. Measured capacitance-voltage characteristic at 1 MHz from a GaAs wafer with a truncated-hyperabrupt doping profile.

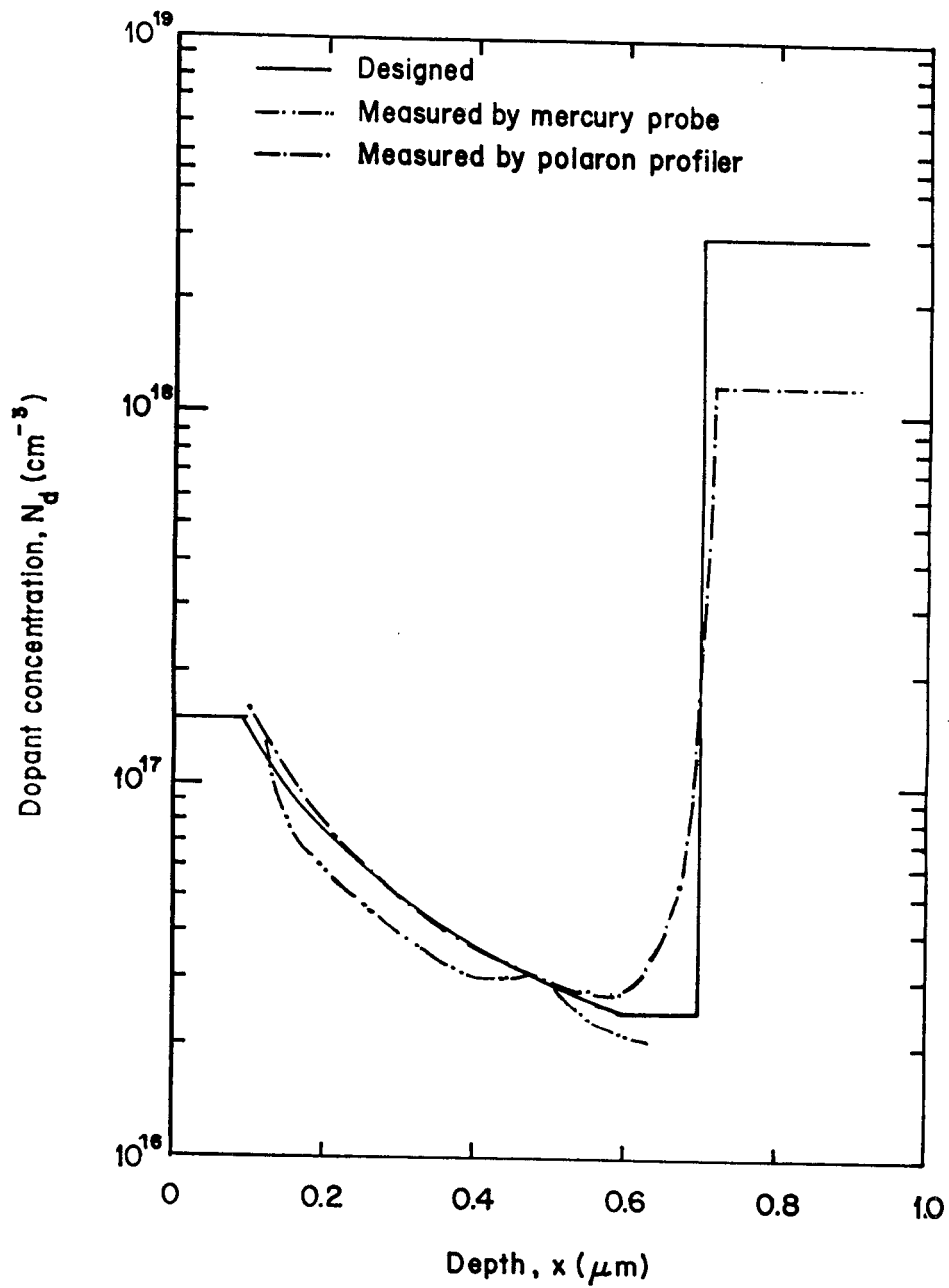


Figure 3.5. A comparison of designed doping profile with the measured doping profiles from a mercury probe and a *Polaron* profiler.

the substrate in determining the depth. The doping profile is calculated from the measured CV characteristic according to Equation (4). This is run by an IBM-PC that controls an HP4280A C-meter for CV measurements. The CV program is given in the appendix.

3.2 Fabrication Processes

Having determined the CV characteristic and doping profile of the wafer, we evaporated a 2000 Å thick layer of aluminum. Although *in-situ* molecular beam epitaxy (MBE) aluminum is superior [1,12], it is not as readily available. Figure 3.6 summarizes all the different layers of the starting material for fabricating the diode-grid. The aluminum is on an 0.7 μm layer of n-type GaAs with a hyperabrupt doping profile. The n-type GaAs is on an 1.8 μm layer of n⁺ GaAs with a doping concentration of $1 \times 10^{18} \text{ cm}^{-3}$. Howard Chen, of Professor Yariv's group at Caltech, and Kjell Stolt of TRW grew the epitaxial layers on chrome-doped semi-insulating GaAs with MBE for us. Wafers as large as 2 cm by 3 cm have been used to fabricate the diode-grids. The diodes are fabricated with a self-aligning process that Zah developed [1].

3.2.1 Self-Aligning Schottky Contact

Figure 3.7 shows how the self-aligning process works. A photoresist is patterned to protect the aluminum Schottky contact during wet etching and to serve as a lift-off mask when the metalization for the ohmic contact is evaporated. The structure is formed by first etching the aluminum until it cuts under the photoresist and then etching the n-layer until it reaches the n⁺ layer and cuts under the photoresist. When AuGe/Ni/Au is evaporated over this structure, a small gap is created between the Schottky metal and the ohmic metal; hence, the Schottky contact is self-aligned to the ohmic contact.

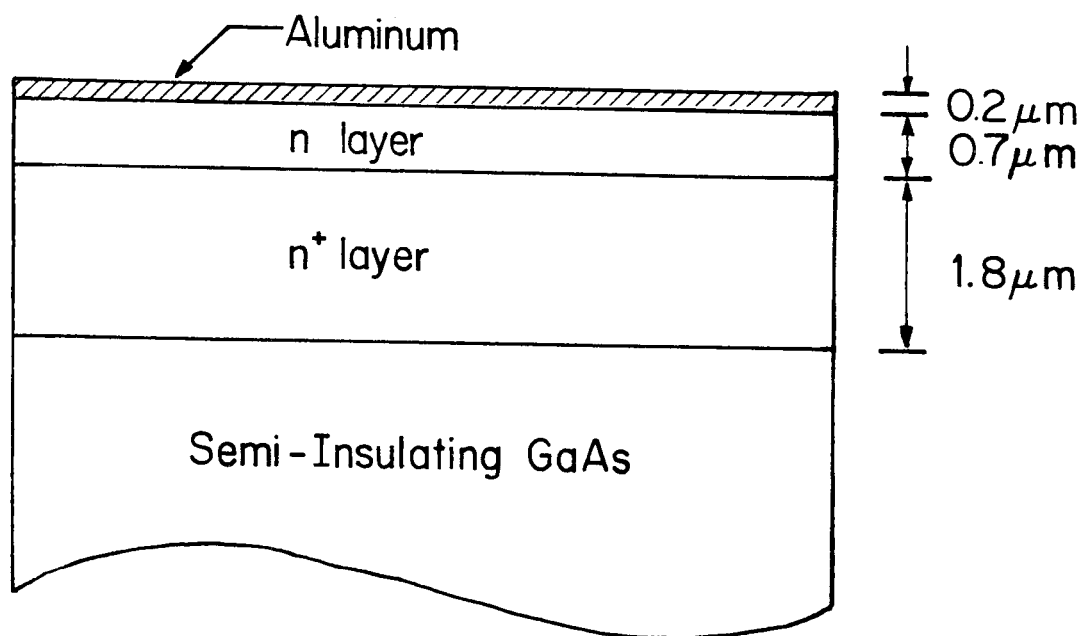


Figure 3.6 A side view of the starting material with $2000\ \text{\AA}$ of evaporated aluminum on the n-type GaAs epitaxy.

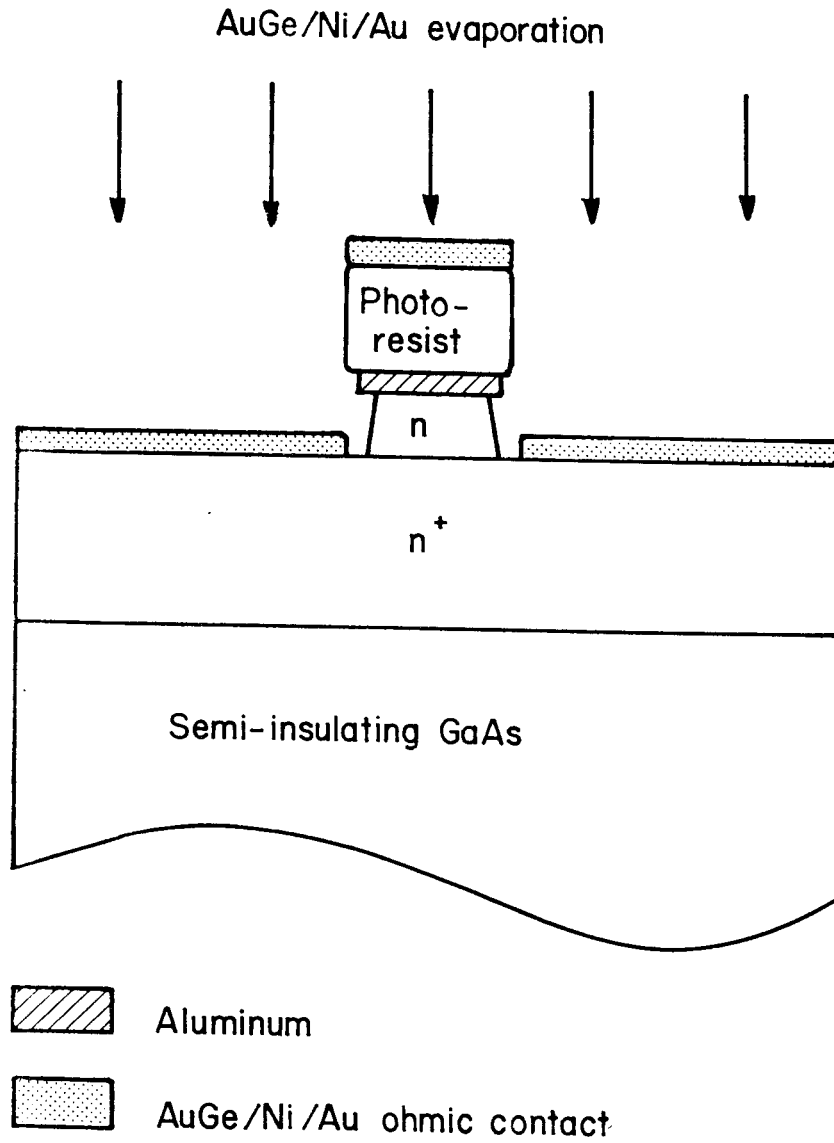
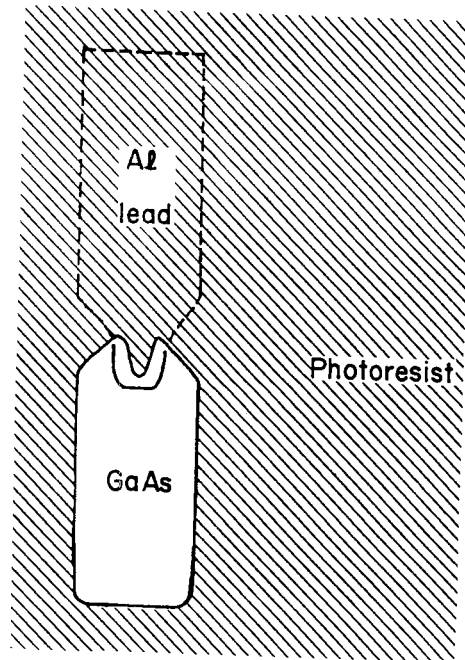


Figure 3.7. Zah's self-aligning process for defining the width of the Schottky contact.

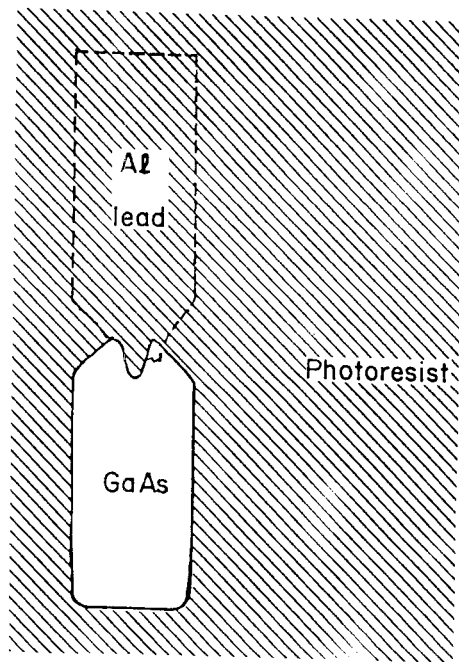
In the past, the lead for the Schottky contact was patterned first and then the width of Schottky contact and the ohmic contact were defined by the self-aligning process as described above. Also *in-situ* MBE aluminum was used. In my experience of using evaporated aluminum, I find it better to etch the width of the Schottky contact and the ohmic contact first and then pattern the lead for the Schottky contact. This has the following advantages. Since a small amount of aluminum is removed in defining the contacts, there is still a significant amount of aluminum left over. If over-etching occurs, one can repeat this step several times until the desired result is obtained. More importantly, etching becomes easier to control because etching tended to be less uniform, when the lead for the Schottky contact was etched before the width of the Schottky contact and the ohmic contact were etched. This is probably due to bubbles formed and trapped at the corner of the photoresist pattern. Figure 3.8 illustrates this problem. Having patterned the lead for the Schottky contact, we then developed the photoresist mask for self-aligning the diode contacts via etching. This exposes a small piece of aluminum to be etched away. Figure 3.8a shows the top view of this. When the wafer is dipped into aluminum etchant, it was found that about 20 % of the diodes tended to be incompletely etched. Typically a small patch of aluminum is left behind as shown in figure 3.8b. If the wafer is etched again, then those diodes that were etched completely will be over-etched. If the wafer is not etched again, then those diodes with a small patch of aluminum cause an electrical short when the ohmic metalization is evaporated. This problem is corrected by simply reversing these two steps.

3.2.2 Ohmic Contact

The technique of using AuGe/Ni/Au to form an ohmic contact on n-type GaAs in a furnace is followed [13]. The thicknesses for the metalizations are 700 Å



(a)



(b)

Figure 3.8. Schematic for illustrating a fabrication problem. (a) Before etching the width of the diode. (b) After etching the width of the diode, which has a small patch of aluminum due to nonuniform etching.

of AuGe (88 % Au and 12 % Ge by weight), 300 Å of Ni and 2000 Å of Au. They are evaporated consecutively without breaking the vacuum. The metalizations for the bonding pads are also evaporated at the same time. This is because the alloy process makes the bonding pads adhere to the substrate better and roughens the surface of bonding pads so that they are easier for ultrasonic wire bonding. The alloying process is done in a furnace at 460 °C for 10 minutes with flowing forming-gas. As the AuGe alloy begins to melt, gallium diffuses into the metal [14]. Germanium diffuses into the crystal lattice and dopes the GaAs. Nickel enhances this diffusion and keeps the metal 'wet' onto the surface from segregating in lumps, and gold serves as a capping layer to increase the conductivity.

Making a good ohmic contact is important in achieving a low series resistance for the varactor diode. Factors that influence the quality of an ohmic contact are well documented [14,15]. One of the most important factor that influences the quality of an ohmic contact is the alloying temperature. Other parameters including the type of metalization and its composition and thickness, ambient gas, alloying time etc. can easily be reproduced based on published literature. There is a wide variation in the temperature used by various laboratories, because the equipment and the way in which alloy temperature is measured are not the same; therefore, it is necessary to calibrate the temperature of our furnace controller. The basic technique of using a linear resistor array to measure the contact resistance is used [14]. Figure 3.9 is a photograph of the actual array fabricated on a GaAs wafer. The ohmic contacts are AuGe/Ni/Au alloyed on a mesa of n^+ GaAs epitaxy. An HP3478A multimeter with a 4-wire measurement capability is used to measure the resistance between the ohmic contacts. The measured resistance as a function of distance gives the contact resistivity. The furnace controller is calibrated by measuring the contact resistivity of the

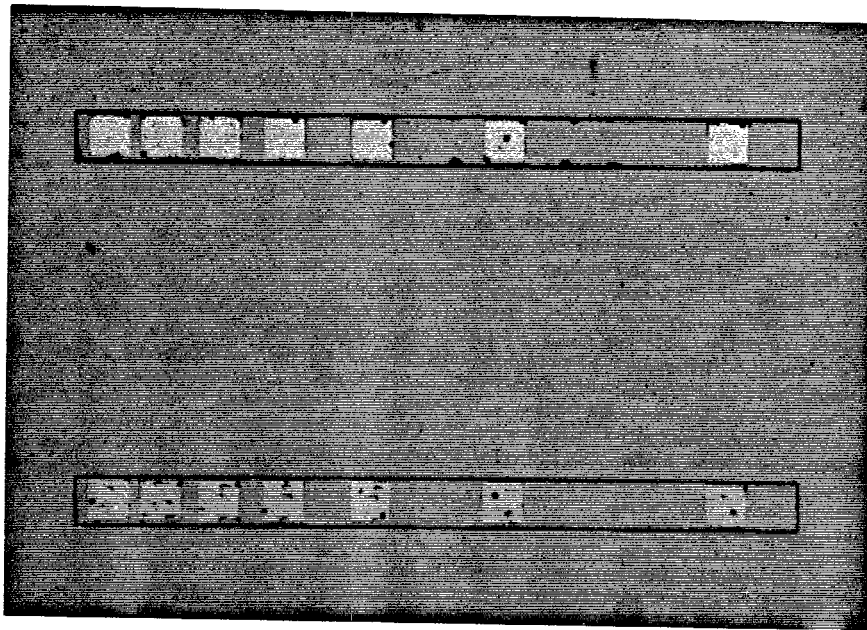


Figure 3.9. Photograph of the resistor array used in measuring contact resistance. Square ohmic contacts are separated by increasing distances. Each resistor array is isolated by mesa etch.

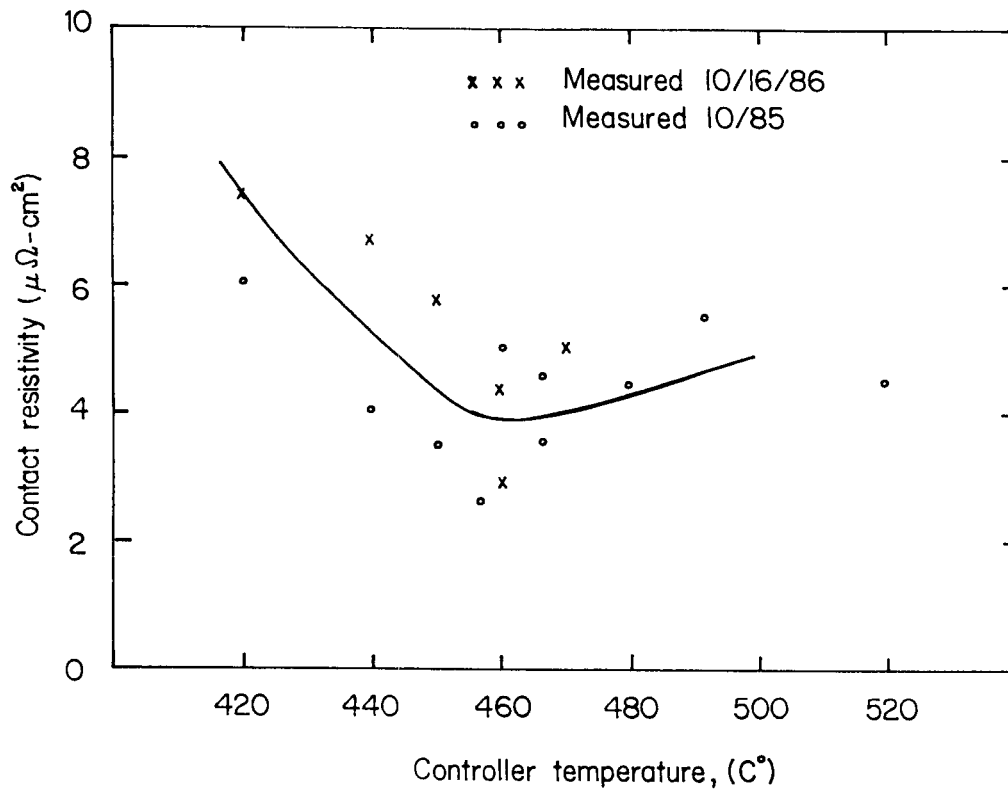


Figure 3.10. Temperature calibration curve of the furnace controller used in alloying ohmic contacts.

ohmic contacts alloyed at various temperatures. Figure 3.10 shows the temperature calibration curve for our furnace controller. A minimum contact resistivity of $4 \mu\Omega(\text{cm})^2$ was measured at an alloying temperature of 460°C . The actual temperature was estimated to be 430°C .

3.2.3 Proton Isolation

Figure 3.11 shows the proton isolation process used in defining the length of the diode. Figure 3.12 shows a $7 \mu\text{m}$ thick photoresist patterned to protect the diode from the protons. The implanted protons convert the n-type semiconductor into a high resistivity dielectric by creating deep levels that trap free carriers [15]. Two consecutive proton bombardments are used in order to completely isolate the epitaxial layers. The implantation parameters are 1.) dose= $4 \times 10^{14} \text{ cm}^{-2}$ and energy= 330 keV , and 2.) dose= $4 \times 10^{14} \text{ cm}^{-2}$ and energy= 200 keV . This was done for us by Frank So and Ali Ghaffari in Dr. Nicolet's group at Caltech and Bob Rush at Huges. The temperature reached during implantation is high enough to harden the photoresist, so an oxygen plasma is used to remove the photoresist.

3.2.4 Low Frequency Varactor Parameters

After proton isolation, low frequency parameters of the varactor are measured. A number of varactors are sampled through out the wafer in order to assess the amount of nonuniformity, and to find an average and a standard deviation for the nonuniformity. Figure 3.13 shows a contour plot of measured series resistance as a function of position on the wafer. The measured series resistance is based on an algorithm that curve-fits the measured IV characteristic with the following equation [1]

$$I = I_s \exp[(V - IR_s)/nV_T] - 1, \quad (3.9)$$

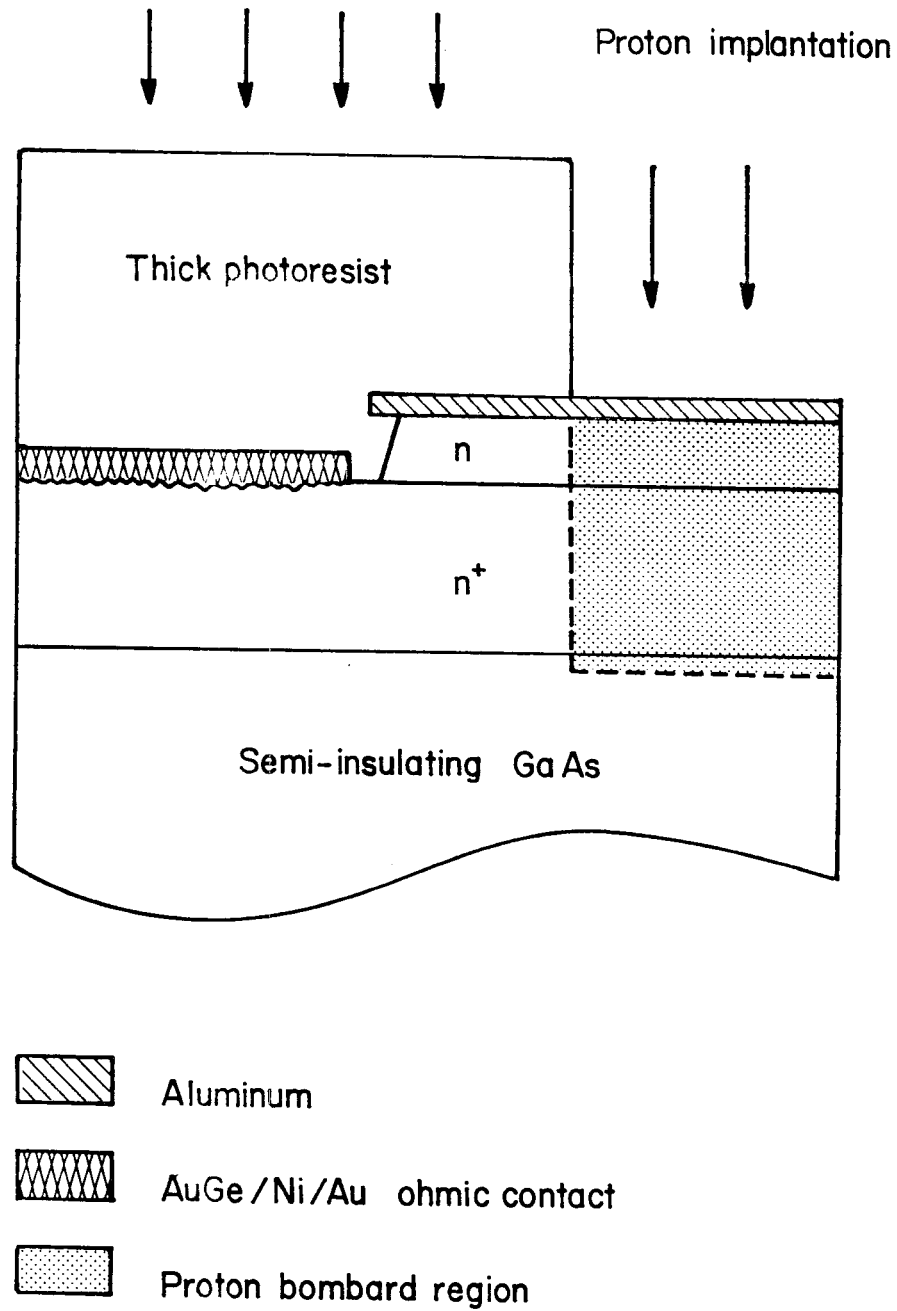


Figure 3.11. Schematic diagram of proton bombardment for defining the length of the Schottky contact.

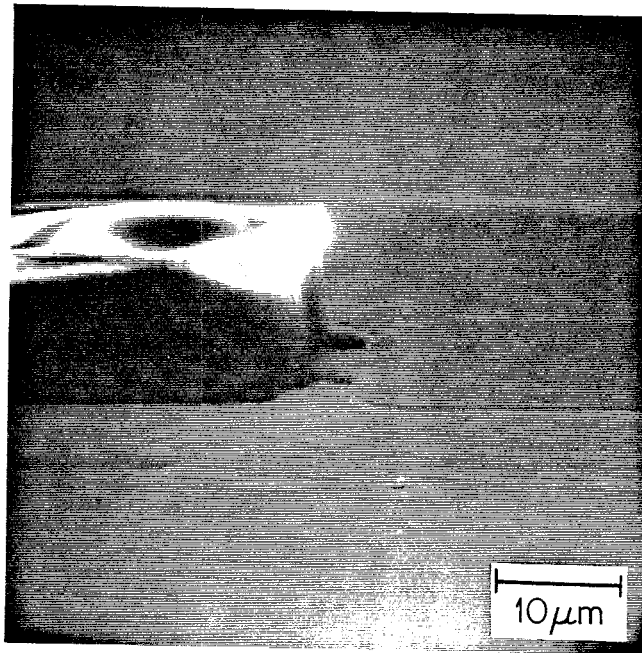


Figure 3.12. SEM photograph of a $7\ \mu\text{m}$ thick photoresist mask for protecting the Schottky contact in defining its length.

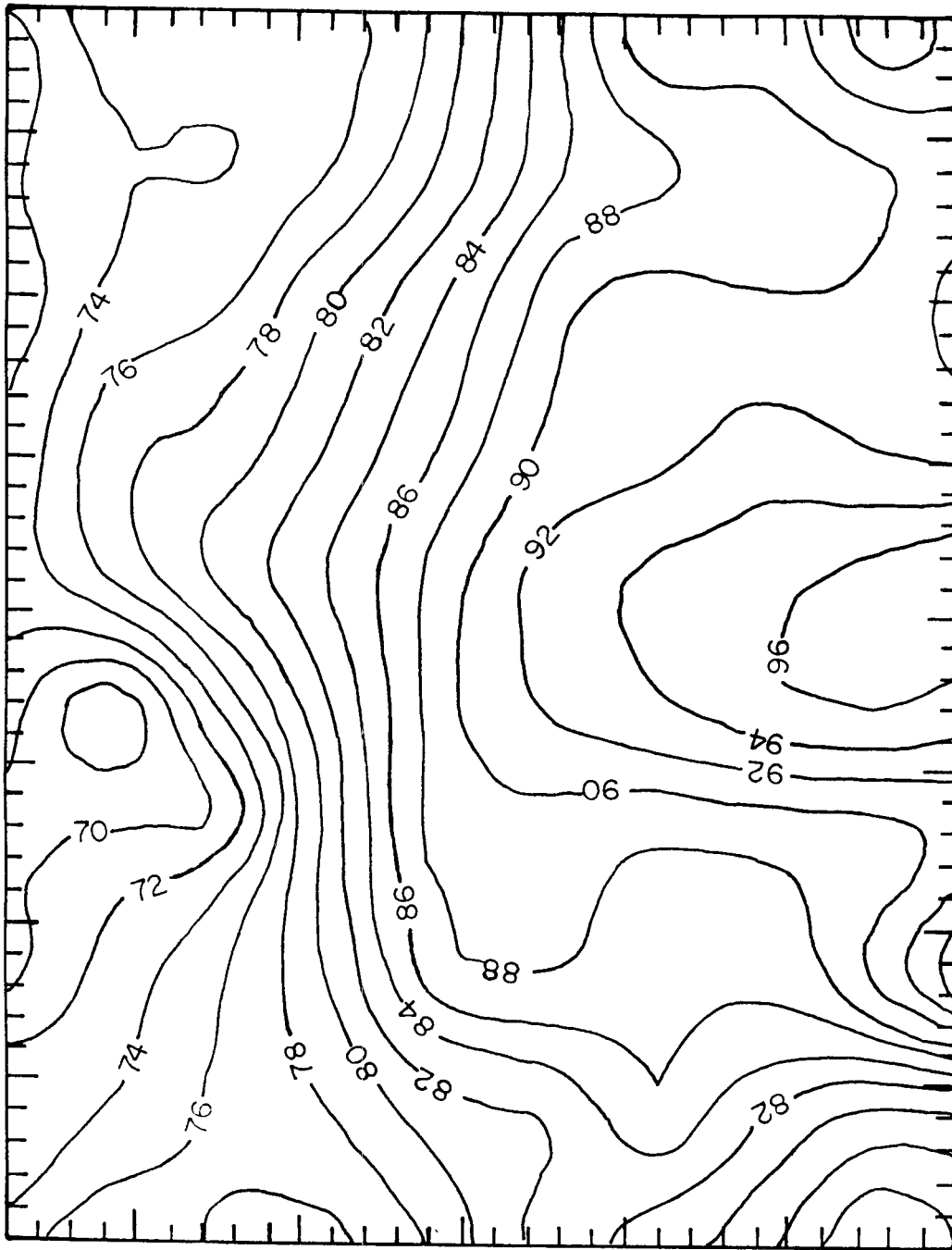


Figure 3.13. A contour map of the diode series resistance in units of ohms.

Tick marks correspond to diode positions on the wafer.

where I_s is the reverse saturation current, R_s is the series resistance, n is the ideality factor, q is the electronic charge, k is the Boltzman constant, and T is the temperature. Figure 3.14 shows a contour plot of zero bias capacitance measured at 1 MHz as a function of position for the same wafer. The diode parameters vary considerably. This is mainly due to mask variation and misalignment during fabrication. The measured diode series resistance is 78Ω with a standard deviation of 19Ω , zero bias capacitance is 30 fF with a standard deviation of 10 fF , and breakdown voltage is 5.1 V with a standard deviation of 1.9 V . This is based on sampling 95 out of 2000 possible diodes. The diodes have a soft breakdown characteristic. This is probably due to contamination because aluminum is evaporated in a oil diffusion-pumped system at 3×10^{-6} Torr. Similar observations were reported in the literature [9,17,18]. Attempts to use refractory metals such as titanium and molybdenum were made, but no significant improvements have been obtained. The series resistance is quite high because the n^+ concentration is only about $1 \times 10^{18} \text{ cm}^{-3}$. The low breakdown voltage limits the capacitance variation from 14.5 fF at -5.1 V to 52.1 fF at $+0.4 \text{ V}$. This corresponds to a capacitance ratio of 3.7. These measurements are computerized. An IBM personal computer is used to control an HP4145B semiconductor parameter analyzer to make the IV measurement and an HP4280A C-meter to make the CV measurement. Software documentations are given in the appendix.

3.2.5 Liquid Crystal Detection

A layer of 2000 \AA thick gold and a layer of 100 \AA thick chrome are evaporated to define the periodic grid that connects the varactors row by row. The chrome acts as an adhesion layer between gold and GaAs. Despite the fact that the yield of the number of devices with a diode characteristic is quite high, the remaining bad diodes, which tend to be electrical shorts, render the entire diode-

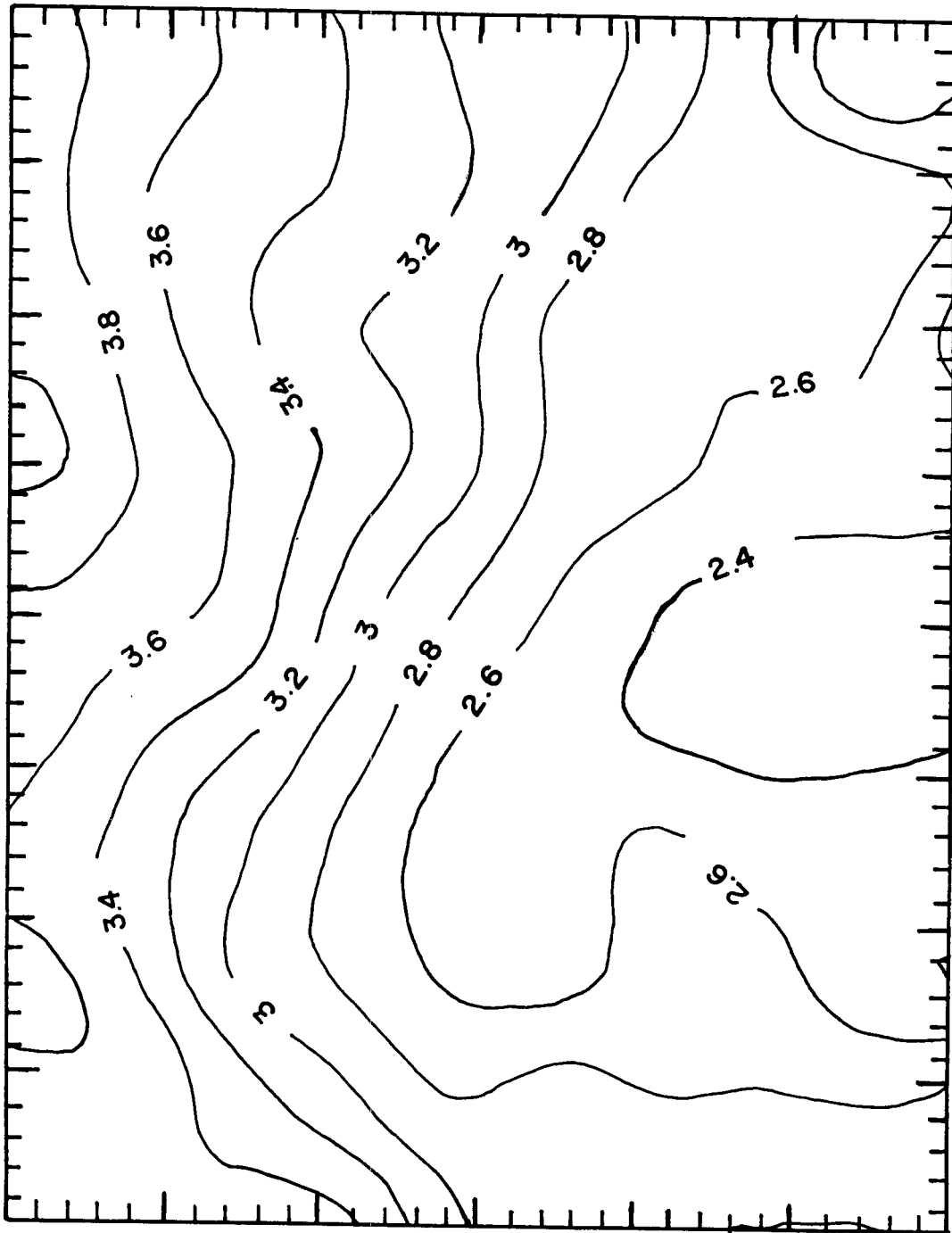


Figure 3.14. A contour map of the diode zero bias capacitance in units of 10^{-14} farads. Tick marks correspond to diode positions on the wafer.

grid almost functionless, because they are connected in parallel. To overcome this, a liquid crystal detection technique is developed to identify the shorted diodes [19]. Figure 3.15 is a photograph of a shorted diode that was found using this method. A layer of liquid crystal for 28-30 °C range is spun onto the wafer. Then current is injected into the rows that are shorted. Because the shorted diodes draw most of the current and therefore dissipate most of the heat, the color of the liquid crystal changes from red to blue as the temperature rises within the vicinity of the shorted diode. Once the short is found, an ultrasonic probe is used to remove the defective diode.

3.3 Test Fixture

Figure 3.16 shows a section of the diode-grid fabricated on a GaAs wafer. Figure 3.17 shows the diode-grid, glued on a glass slide with photoresist at the edge of a pc-board. This is convenient because the photoresist can be dissolved in acetone quite easily. There are a total of 50 DC bias lines available. Two edge connectors are used to feed the bias lines from the variable bias controller, which consists of an array of variable resistors driven by programmable constant current sources. Electronic relays are used to provide low-frequency modulation of the DC bias for situations that require a reference signal.

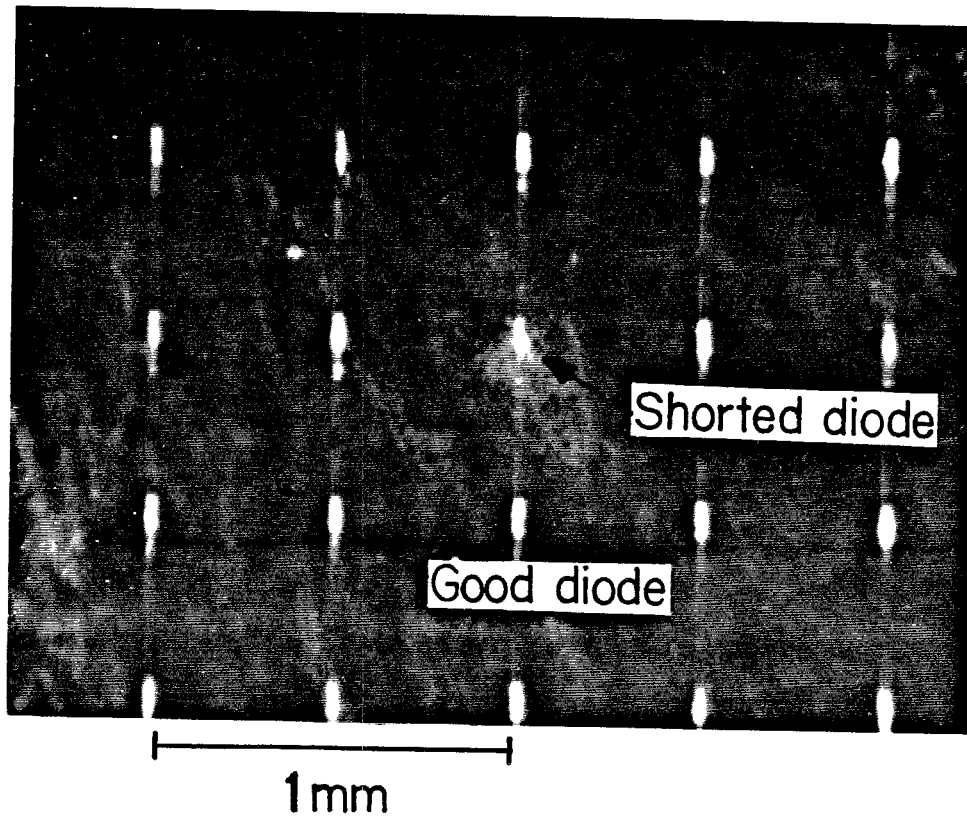


Figure 3.15. Photograph of a shorted diode found by a liquid crystal detection technique.

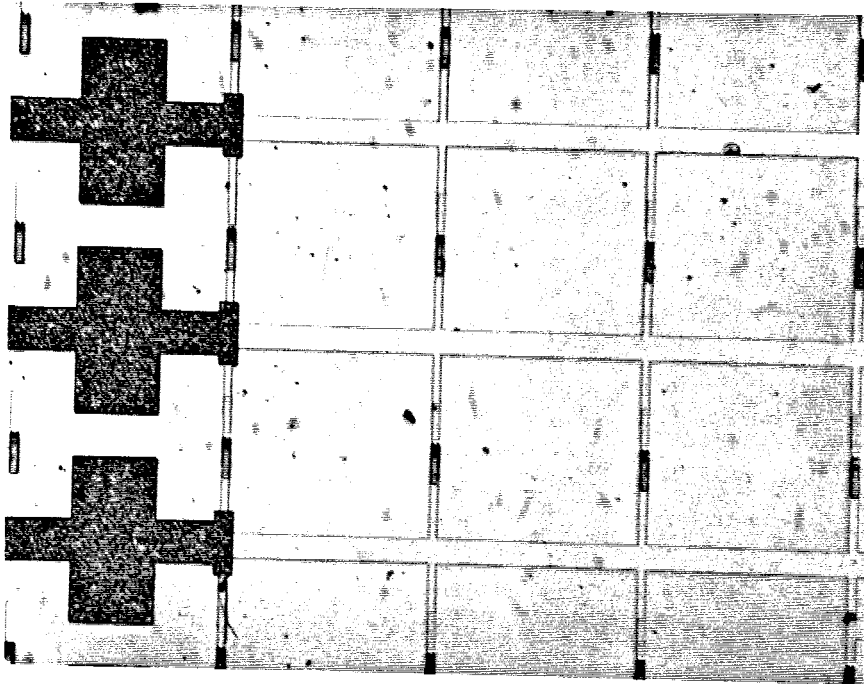


Figure 3.16. Photograph of a diode-grid with AuGe/Ni/Au bonding pads.

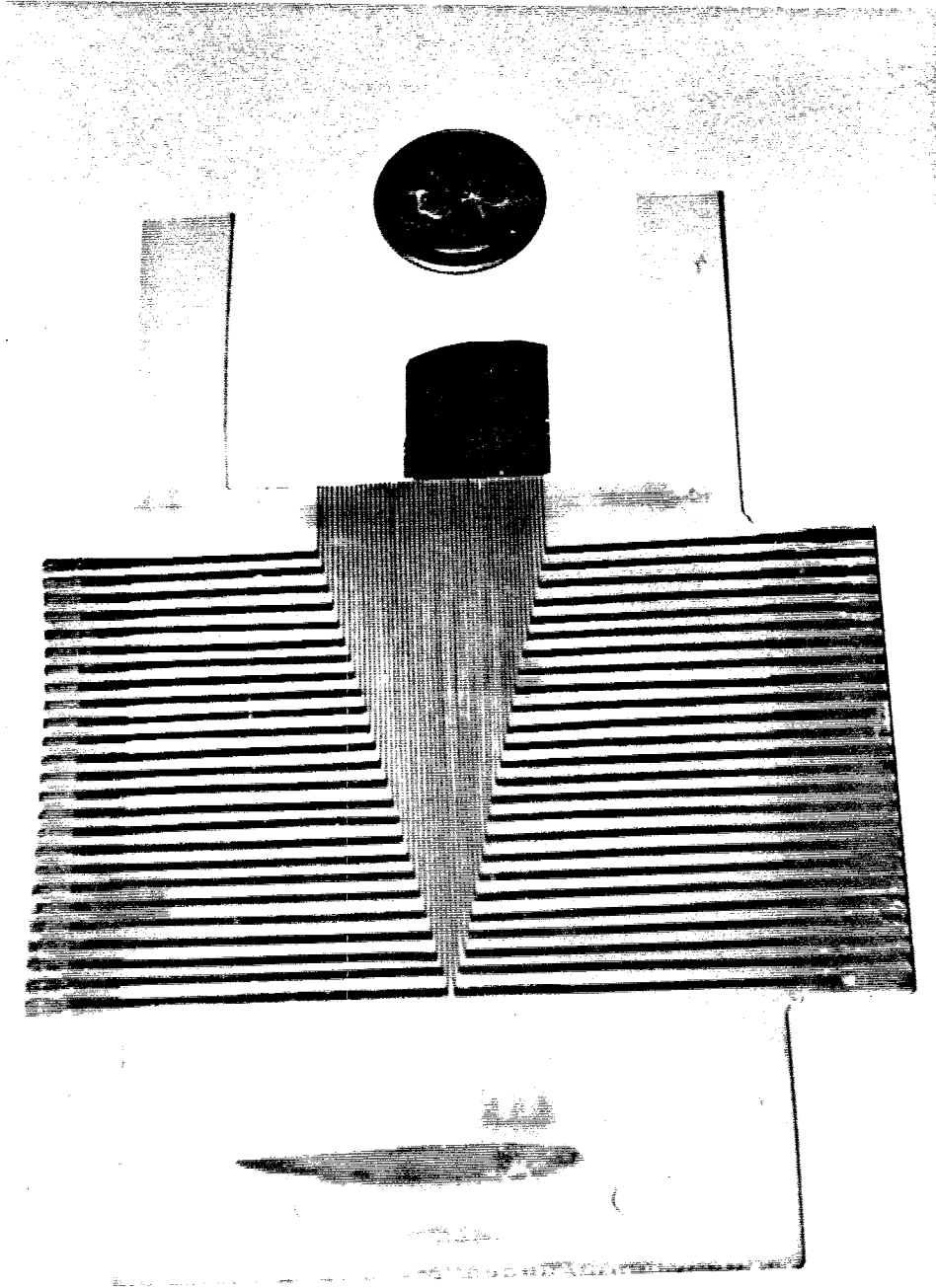


Figure 3.17. Photograph of a diode-grid mounted on a pc-board with 50 DC bias lines available.

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Chapter 4

Diode-Grid Phase Shifter Measurements

In proposing a new design, the experimental procedure for testing the assumed model is also necessary. In the process of developing the experimental procedure, what is available in the laboratory often plays a role in deciding the experimental method. This chapter begins with a survey of possible existing methods of measurement for testing the diode-grids, and concludes that they are not suitable in our work. This is mainly due to the fact that wafers available to do our experiments were usually small and irregular in shape. Consequently, a small aperture reflectometer that uses a wave-front division interference technique was developed to measure the reflection coefficient of the diode-grid. The validity of this method is illustrated by reflection measurements of thin-film bismuth on fused quartz and its limitations are indicated by reflection measurements of fused quartz. The experimental procedure consists of curve-fitting the measured RF reflection coefficient with an equivalent circuit model based on transmission-line theory and comparing the best-fitted parameters with the corresponding parameters measured at low frequency.

4.1 Survey of Possible Experimental Methods

One of the possible methods of testing the diode-grid is based on simulation of a planar periodic array in waveguide [1]. Such simulation permits the use of a few elements in a waveguide to represent a large number of elements in an infinite periodic array. In the waveguide simulator, the array impedance can be measured. However, precision waveguide machining and sample mounting are required to duplicate the details of the array. Furthermore, a scaled model is probably required as the frequency approaches 100 GHz, and it is more desirable

if the measurement can be made directly on the diode-grid itself. Therefore, methods based on quasi-optical techniques become more attractive.

In principle, various quasi-optical methods for measuring the complex dielectric constant of a material can be extended to test the diode-grid. They have demonstrated remarkable accuracy in dielectric measurements at millimeter wavelengths. The semi-confocal open-resonator [2] and quasi-optical network analyzer [3,4] produce results with uncertainty of about 1% in the real part of dielectric permittivity and 10% in the loss tangent. The Mach-Zehnder interferometer [5], and Michelson interferometer [6] provide five or six significant figures for refractive index and 1% accuracy in loss tangent.

In the semi-confocal resonator method, a sample free of any attachments is mounted on the planar mirror side of the resonator, and a change in the Q of the resonator and the resonant frequency are used to determine the complex dielectric permittivity. Jones [2] successfully demonstrated precise dielectric measurements at 35 GHz. The sample should be plane-parallel with a diameter ≥ 5 cm. His sample ranged from 7 cm to 7.5 cm. The minimum spot size of the beam determines how small a sample can be used, and at 100 GHz samples with a diameter larger than 3 cm are required. This was too large for us.

In the other three approaches, various optical configurations for wavefront interference are used to assess the dielectric permittivity. Their general principle of operation is interesting and will simply be illustrated with the Michelson interferometer as shown in figure 4.1. An incident wave is divided into two parts by the beam splitter. One part of this wave goes to a scanning mirror and becomes the reference wave. The other part goes to the sample, reflects off the sample, and becomes the signal wave. Then the two waves recombine at the beam splitter and go to the detector. The measured intensity of the interfering signals allows one to find the reflection coefficient of the sample. To calibrate the system, the

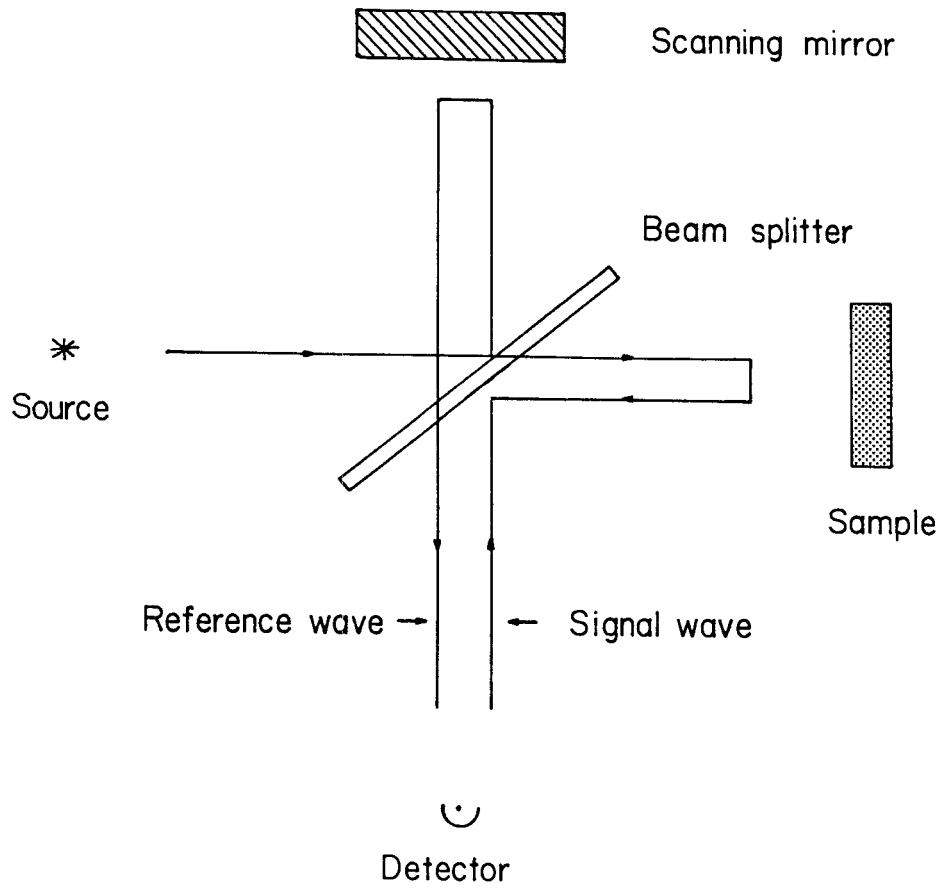


Figure 4.1. Schematic representation of a Michelson interferometer.

microwave technique of using an open, a short, and a matched-load can be used. Placing a planar mirror at certain position along the horizontal arm of the interferometer allows one to define a reflection coefficient of -1 . Translating the mirror by a quarter-wavelength allows one to define a reflection coefficient of $+1$. And removing the mirror allows one to define a zero reflection coefficient.

Figure 4.2 shows an actual setup of the Michelson interferometer. Teflon lenses were included to collimate and focus the beam onto the diode-grid since the grids were typically small (2 cm by 2 cm). Using results published by Harvey [7], we designed an artificial quarter-wave matching layer in the form of grooves to match the surfaces of the dielectric lens. Also, absorbing materials were strategically placed to minimize any stray reflections. Despite these efforts, the measured results were inconsistent with transmission-line theory because the calibration procedure was not sufficient to calibrate out those extraneous reflections coming from the dielectric lens and absorbers in the neighborhood of the diode-grid. Although more complete calibration procedures are available [6], they are very sophisticated for practical use. In addition, these methods require samples with large lateral dimensions. For example, in the multiport reflectometer [3], Stumper used samples 7 cm to 8 cm in diameter to make reflection measurements at 392 GHz. These are the factors that make the small aperture reflectometer attractive for samples that are small and irregular in shape.

4.2 Small Aperture Reflectometer

Figure 4.3 shows a small aperture reflectometer developed to measure the reflection coefficient of the diode-grid. The idea is to use an absorbing screen with a hole in the center to divide an incident wave-front into two parts. The wave that reflects off the absorber is the reference, while the other part reflecting off the sample is the signal. The interference of these two reflected waves is

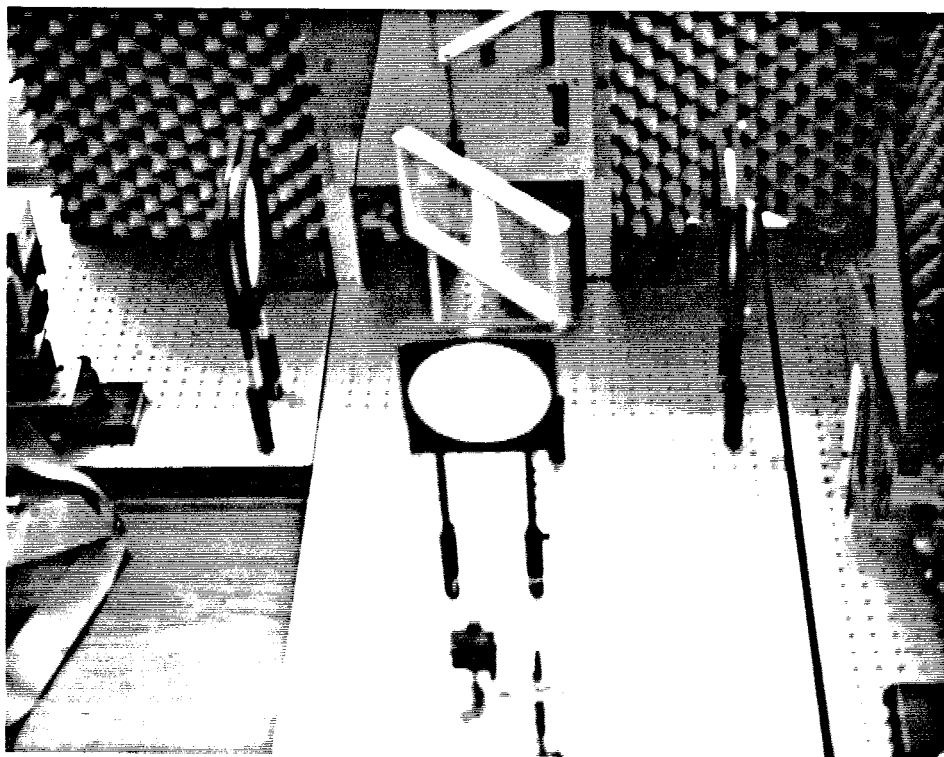


Figure 4.2. Photograph of a Michelson Interferometer for millimeter waves.

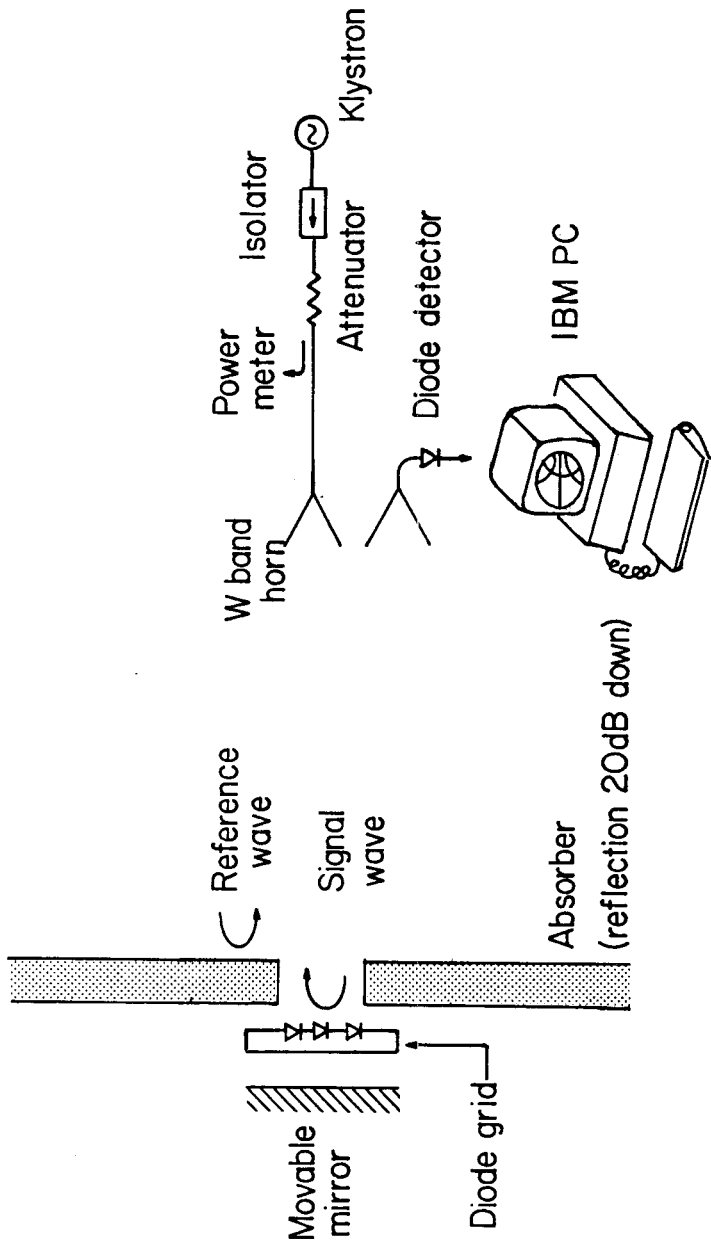
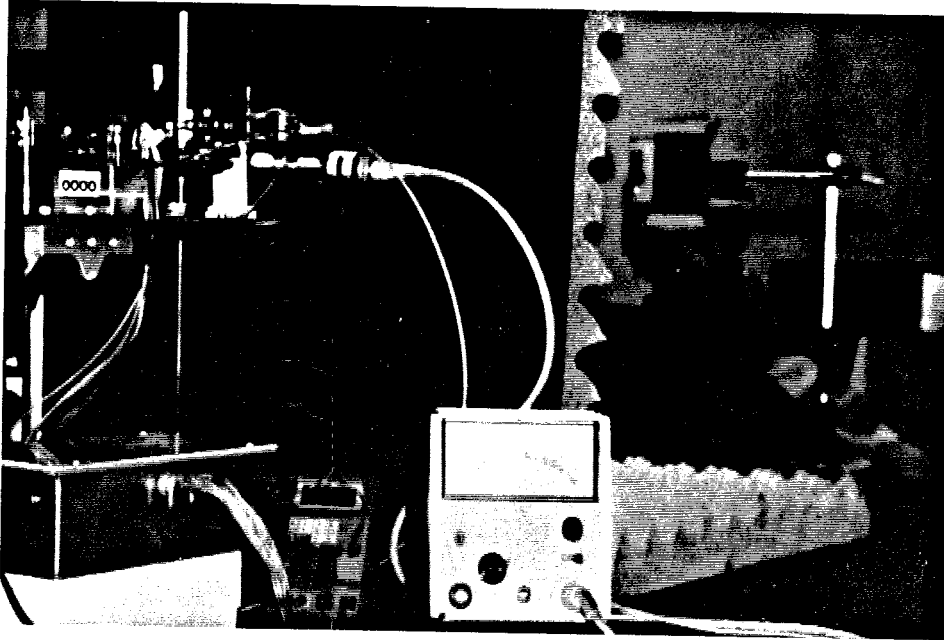


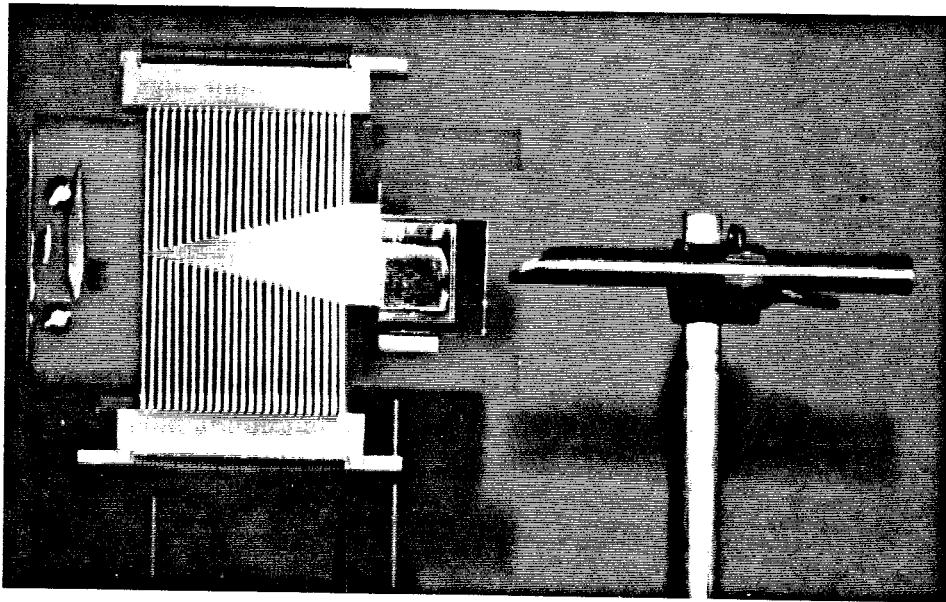
Figure 4.3. Schematic representation of a small aperture reflectometer.

measured as the sample is translated relative to the absorber. An absorbing screen is chosen to approximately balance the energy in the two reflected waves so that the measured intensity has sufficient contrast. The phase and amplitude of the reflection coefficient of the sample can be found by least-square fitting each interference pattern or by a simple four-point method [8]. The scanning mirror serves as a tuning parameter as well as a standard load for calibration. The system alignment and calibration can be done quite quickly, and the measurement can be computerized.

Figure 4.4 shows an actual setup for the small aperture reflectometer. Initially a Varian klystron source with 100 mW of output power was used, but later it was found that a Hughes Gunn-diode source with 10 mW of output power was sufficient to do the measurement. The input power is sampled by a 10-dB directional coupler and monitored by an HP432A power meter thru a Hughes thermistor head. The frequency is measured by a Hughes wave-meter before and is checked after the experiment to determine the appropriate step size and to ensure that no significant frequency shift occurs. The measured input powers are used to normalize the measured intensities. The area of the transmitting horn is 4 cm^2 . The intensity of the interference pattern is measured by a receiving horn that feeds into a zero bias detector. The output is a DC signal, which is monitored by a HP 3478A multimeter. An IBM-PC is used to control the equipment. It uses a Capital Equipment interface board to communicate with HP equipment and a Data Translation A/D and D/A converter to control the stepper motors, which have a resolution of $1 \mu\text{m}$. Figure 4.5a shows the translation stage. Figure 4.5b shows the absorbing screen that is placed next to the diode-grid, which is located at a distance of 82 cm away from the horns. This corresponds to six times the far field condition. To accommodate different sizes and shapes of diode-grid, a small replaceable template of absorber is used in conjunction with a bigger

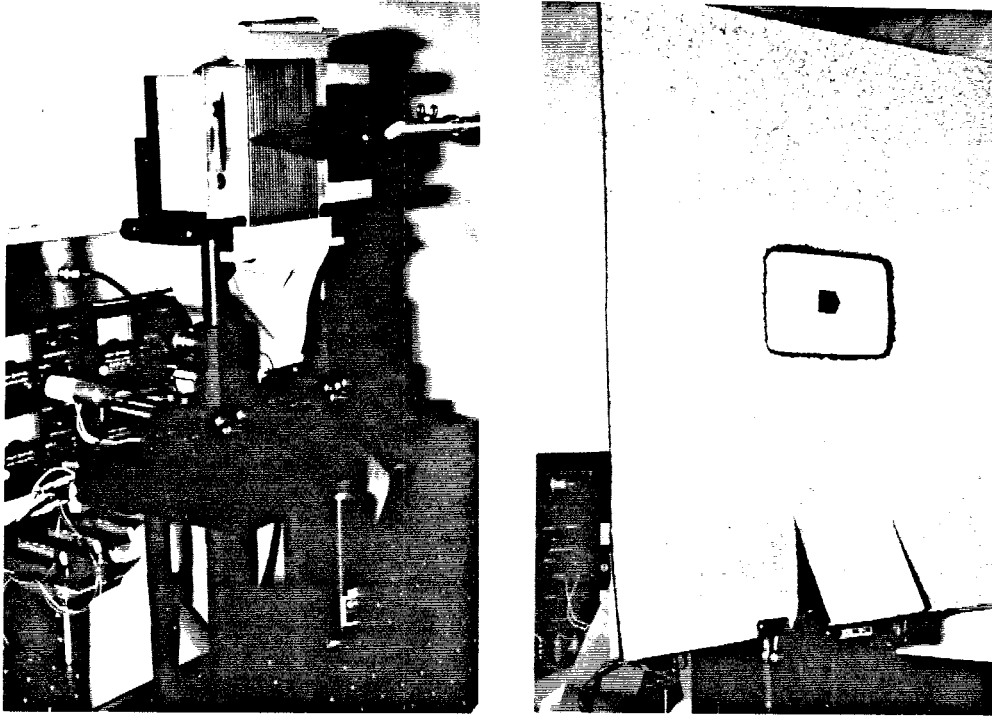


(a)



(b)

Figure 4.4. Photographs of the small aperture reflectometer setup. (a) Overall view. (b) Close-up view of the test fixture and miniature microscope.



(a)

(b)

Figure 4.5. (a) Sideview of the computer-controlled translation stage. (b) The absorbing screen that is used to divide an incident wavefront in the small aperture reflectometer.

absorbing screen. Both the diode-grid and the scanning mirror are aligned with a Helium-Neon laser. The mirror is made by evaporating $2\ \mu\text{m}$ of gold on glass. The initial distance between the sample and the scanning mirror is measured by a miniature microscope with a resolution of $25\ \mu\text{m}$.

The effect of interference can be demonstrated by plotting the power detected by the diode detector as a function of the mirror position. Figure 4.6 shows that the received power varies sinusoidally with the mirror position. The maxima correspond to constructive interference, while the minima correspond to destructive interference. Physically, more energy is deflected at the peaks and less energy is deflected at the valleys into the field of view of the detector. Since the reflection coefficient of the sample is proportional to the complex amplitude of the interference pattern, one way to find the amplitude and phase is simply to fit a sinusoidal curve through the measured data. This is shown in figure 4.6 with a solid line. However, the curve-fitting process is time-consuming.

Figure 4.7 shows pictorially another method in which both the amplitude and the phase of a sinusoid can be calculated. This is called the four point method, and it is based on simple phasor trigonometry. It was developed by Wyant [8] in optical interferometry for three-dimensional sensing. When the intensity of the sinusoid is sampled four times consecutively at 90° intervals, both the amplitude and phase of the sinusoid can be calculated by the law of tangents and the Pythagorean theorem. The 90° phase shifts can be introduced in the form of optical path delay by translating the sample relative to the screen at intervals of one-eighth of a wavelength. Both the curve-fitting method and the four-point method have been used, and they agree to within 2% for amplitude and 3° for phase. The four-point method is preferred since it is faster. Also, it is interesting to note that the use of the four-point method in the small aperture reflectometer makes the system analogous to a six-port network analyzer. However, in the small

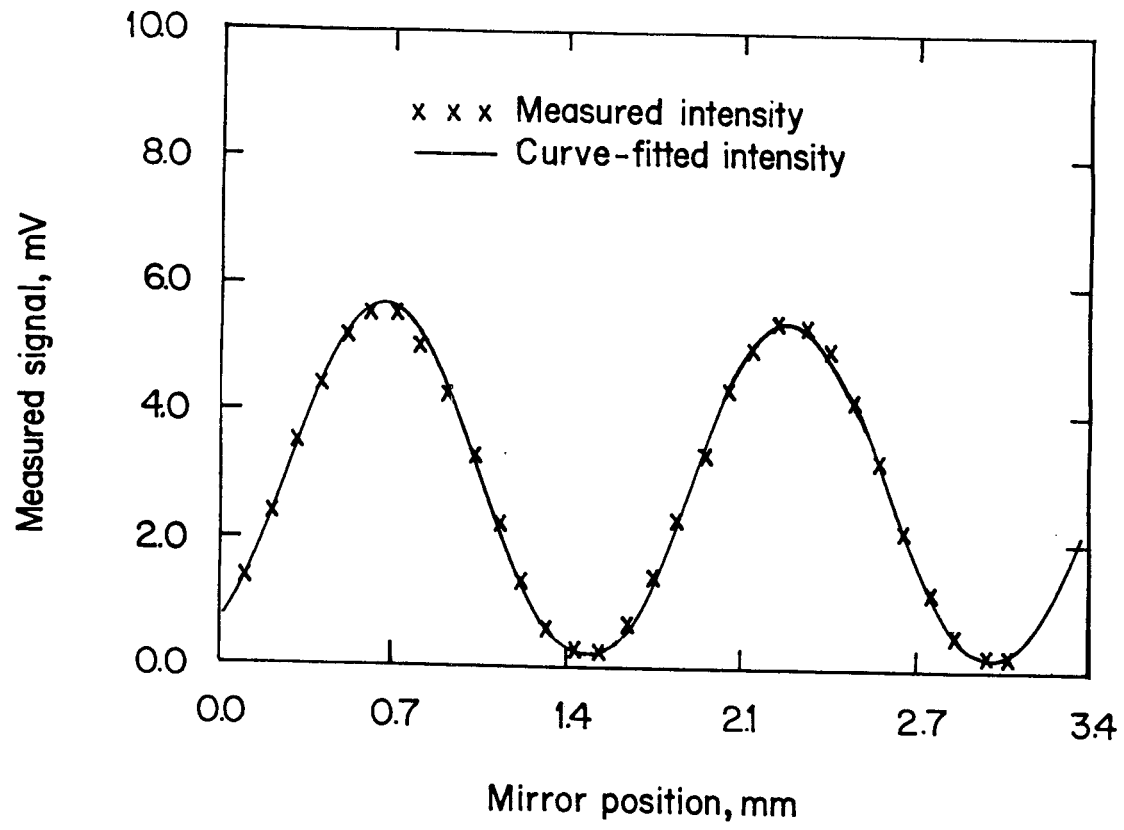
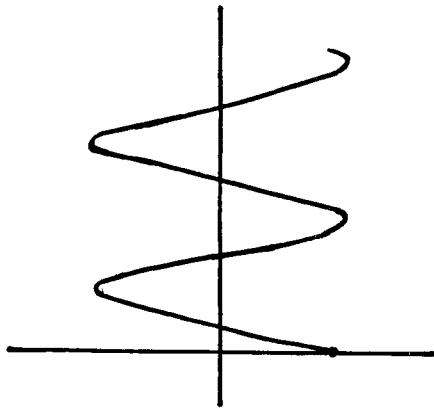
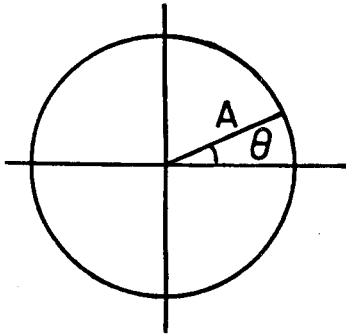


Figure 4.6. An interference pattern measured at 93 GHz.



Measured sinusoid



Phasor representation

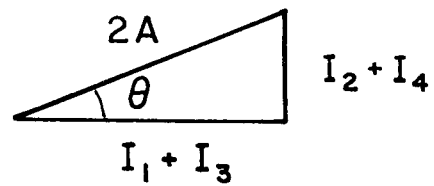
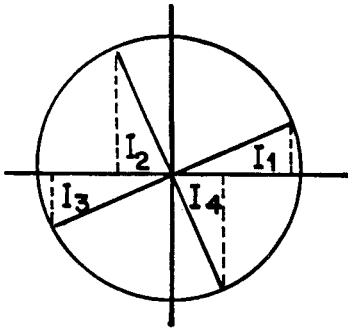
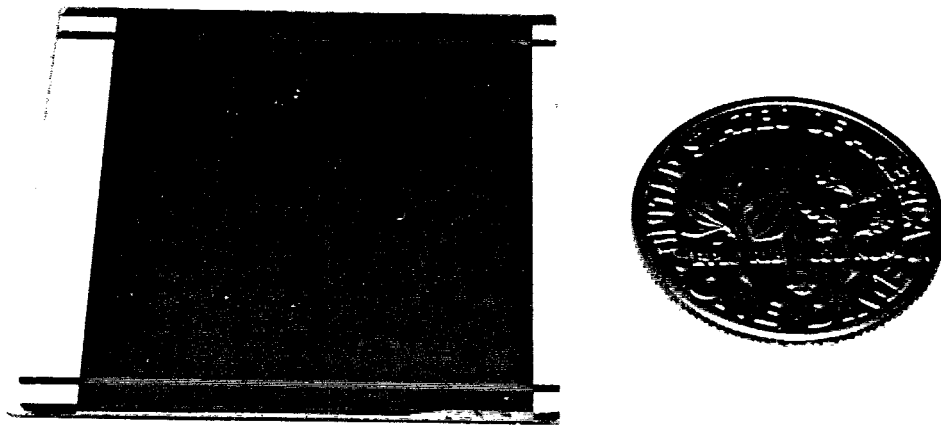


Figure 4.7. A pictorial illustration of the four-point method.

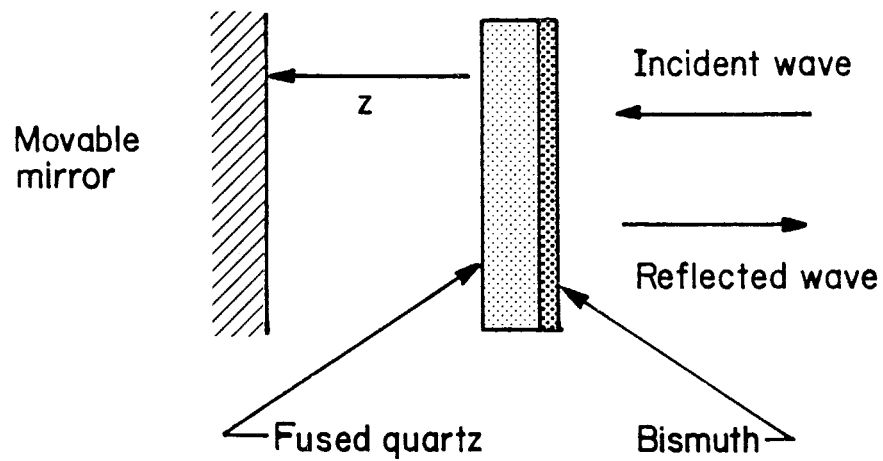
aperture reflectometer only one detector is used, while in the six-port network analyzer four detectors are required.

Reflection Measurements of Bismuth on Fused Quartz

The validity of the small aperture reflectometer can be illustrated with the reflection coefficient measurement of thin-film bismuth since we can control precisely the way in which the sample is prepared. Figure 4.8a shows a one-square inch thin film bismuth deposited on a 3 cm square fused-quartz plate by electron-beam evaporation in a diffusion pumped vacuum system. The bismuth thickness is 608 Å. Using the four-point probe procedure, we measured the sheet resistance the bismuth film via four gold deposited strips at the edge of fused quartz to be 92.2Ω . The fused quartz material is Dynasil # 4000. It is plane-parallel to within $5 \mu\text{m}$. Figure 4.8b shows the configuration in which the reflectance and phase of reflection were measured at 93 GHz. Figure 4.9 shows the result of the reflection measurement or the tuning curve. The reflectance reaches a maximum of 100% at a mirror position of $720 \mu\text{m}$. This is primarily to due the effect of the mirror, which basically presents an electrical short at the plane of the bismuth film. At a mirror position of $1590 \mu\text{m}$, the effect of the mirror is equivalent to an open circuit at the plane of the bismuth film; therefore, the reflectance reaches a minimum. The value of this minimum is determined by the sheet resistance. Theoretical curves are plotted using the measured bismuth sheet resistance (92.2Ω), fused quartz thickness ($434 \mu\text{m}$), index of refraction (1.96 from Afsar and Button [6], initial mirror position ($203 \mu\text{m}$), and a best-fitted length parameter due to phase calibration ($2067 \mu\text{m}$). The phase-calibration length is the distance between the input surface of the sample (bismuth film) and a reference plane at which the measured phase of reflection is 180° for the mirror. The phase-calibration length was measured to be $2019 \mu\text{m}$, which disagrees with the best-fitted value by $48 \mu\text{m}$



(a)



(b)

Figure 4.8. (a) Photograph of thin-film bismuth on fused quartz. (b) Reflection measurement configuration of bismuth at 93 GHz.

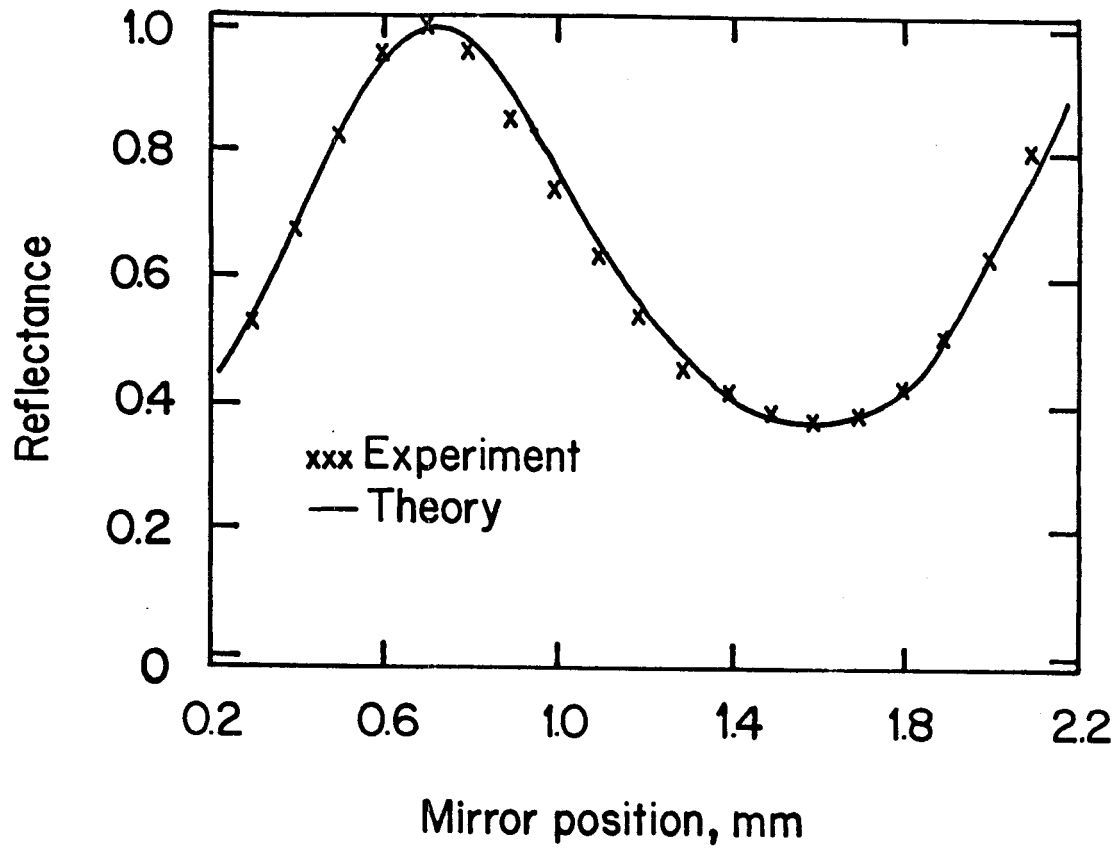


Figure 4.9a. Measured reflectance of thin-film bismuth on fused quartz at 93 GHz.

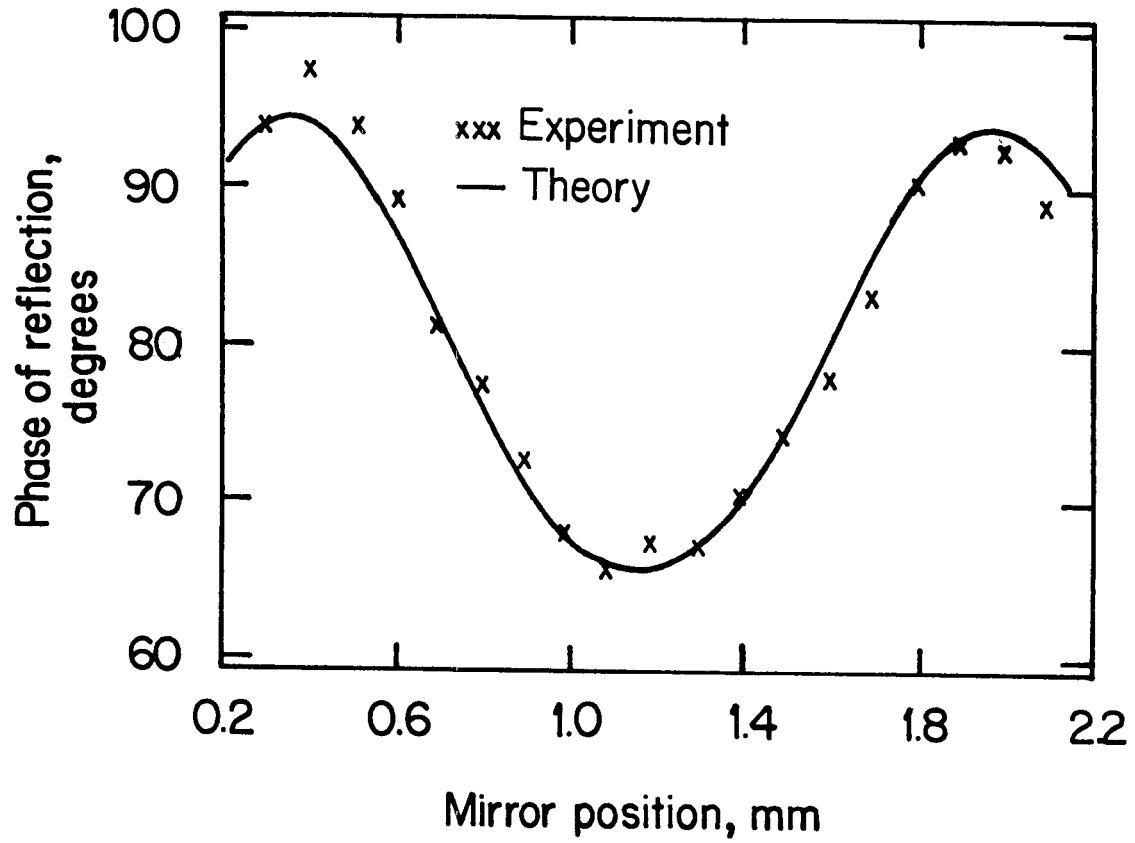


Figure 4.9b. Measured phase of reflection of thin-film bismuth on fused quartz at 93 GHz.

in physical length or 10° in electrical length. This is about as accurate as we can measure the phase-calibration length since, a miniature microscope with a resolution of $25\ \mu\text{m}$ is used to measure distances from the side of the sample. When the sheet resistance of thin-film bismuth is also treated as a fitting parameter, the best-fitted value is $91.6\ \Omega$. This agrees quite well with the measured sheet resistance ($92.2\ \Omega$) at DC.

4.4 Reflection Measurements of Fused Quartz

The limitation of the small aperture reflectometer is observed when it is used to measure low loss materials such as fused quartz. Figure 4.10 shows the configuration in which the reflection measurement was made at 89 GHz. The reflection coefficient is measured as the mirror is tuned. Figure 4.11a shows the measured reflectance. The calculated reflectance is plotted using the measured fused quartz thickness ($434\ \mu\text{m}$), initial mirror position ($203\ \mu\text{m}$), and refractive index for fused quartz (1.96 from Afsar and Button [6]). The calculation does not agree well with the measured reflectance when the absorption index for fused quartz (0.0005 from Afsar and Button [6]) is used. This is shown with a solid line. In order to get a better agreement, an absorption index of 0.02 must be used. This is shown in figure 4.11a with a dashed line. The dips in the measured reflectance are probably due to power leaking laterally. Figure 4.11b shows the measured phase, calibrated according to the measured phase calibration length ($1105\ \mu\text{m}$). It is interesting to note that the calculated phase of reflection agrees quite well with the measured phase. An explanation for this is the following. If power escapes as described above and the amount of power loss is relatively constant during a reflection measurement scan, then the measured phase is relatively unaffected because the four-point method, which takes the ratio of a difference of four intensity measurements to calculate the phase, effectively calibrates the

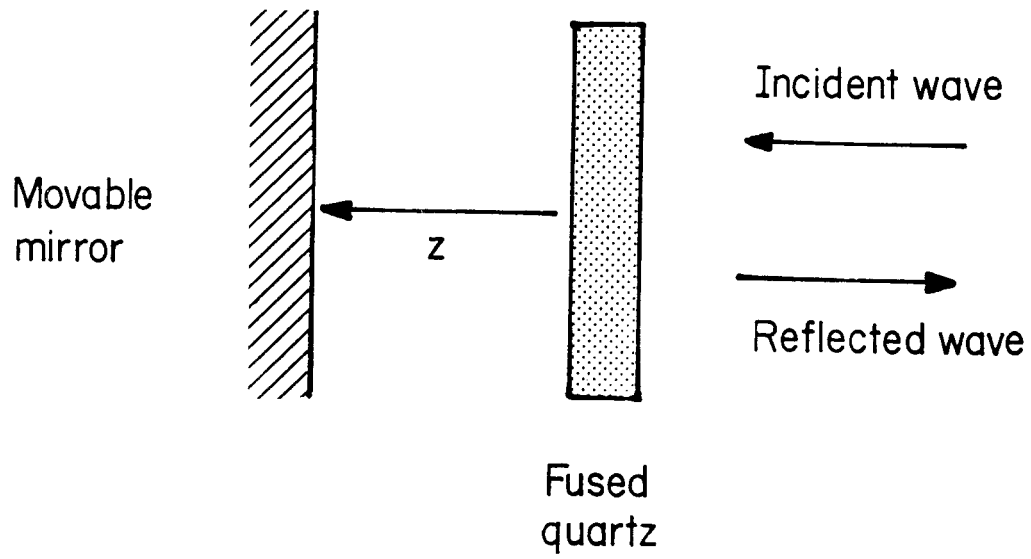


Figure 4.10. Reflection measurement configuration of fused quartz.

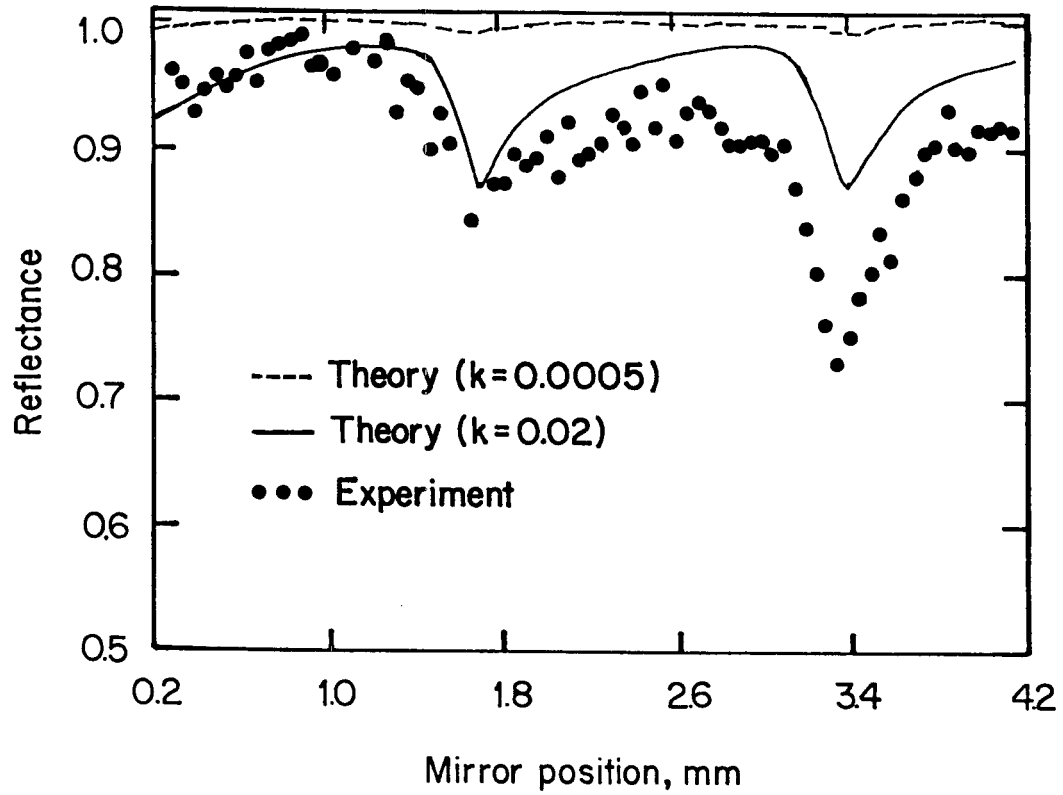


Figure 4.11a. Measured reflectance of fused quartz at 89 GHz.

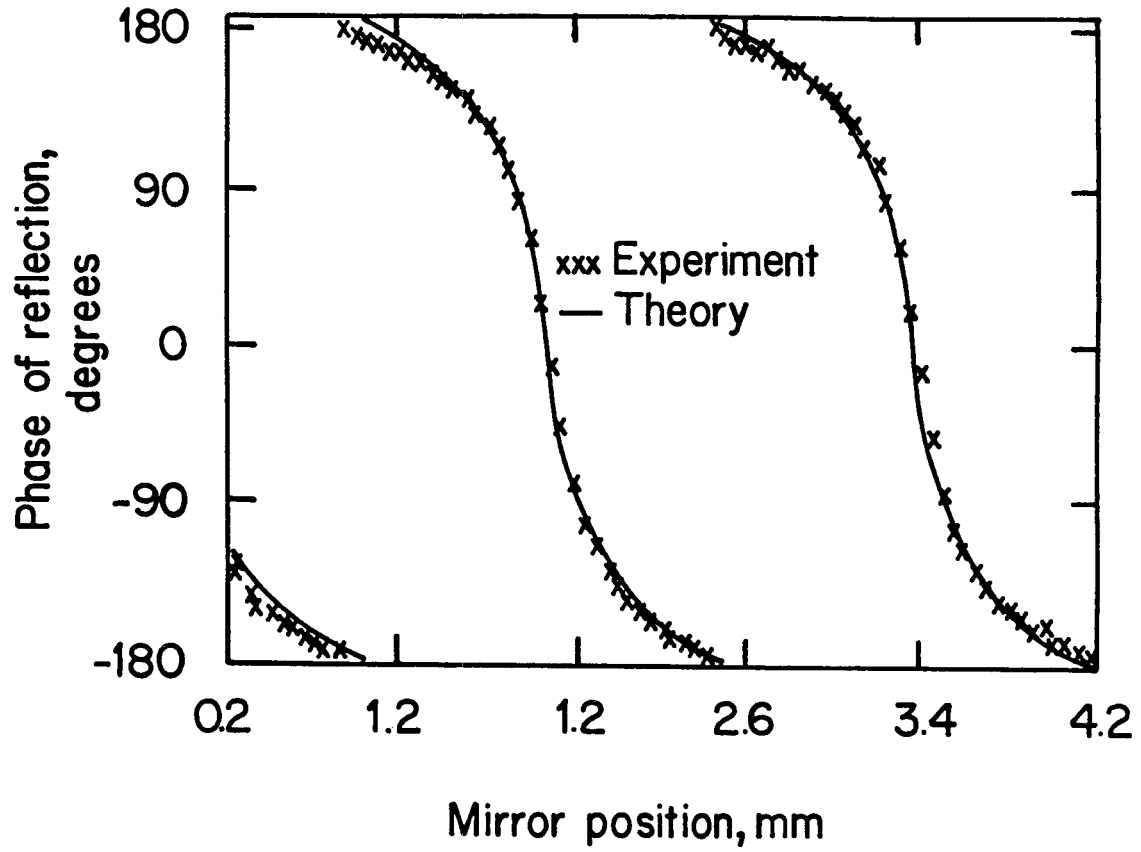


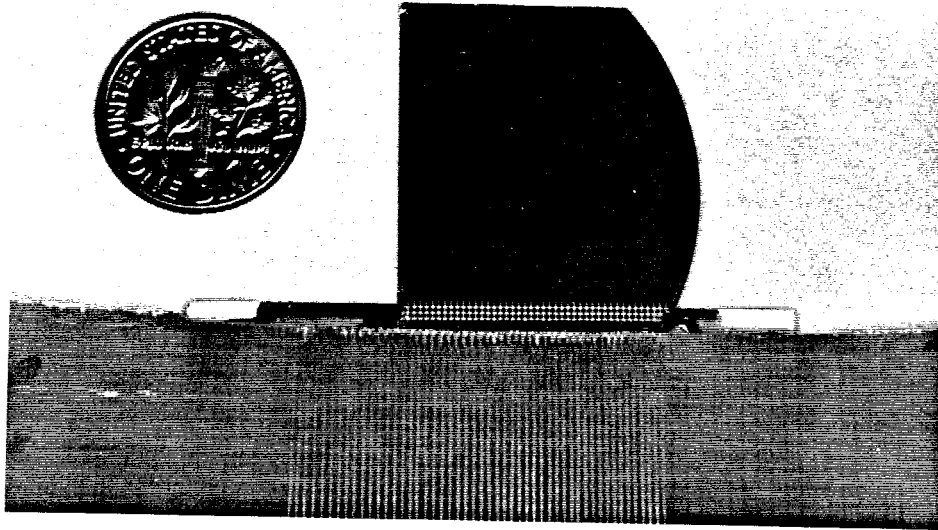
Figure 4.11b. Measured phase of reflection of fused quartz at 89 GHz.

loss of power out of the calculation. This also indicates that power is most likely leaking out from the etalon formed between the quartz and the mirror. Interestingly, no dips occurred in the previous reflectance measurement of thin film bismuth on fused quartz. An explanation for this is that the thin-film bismuth, being lossy, damps out most of the power during situations that favor lateral power leakage from the etalon.

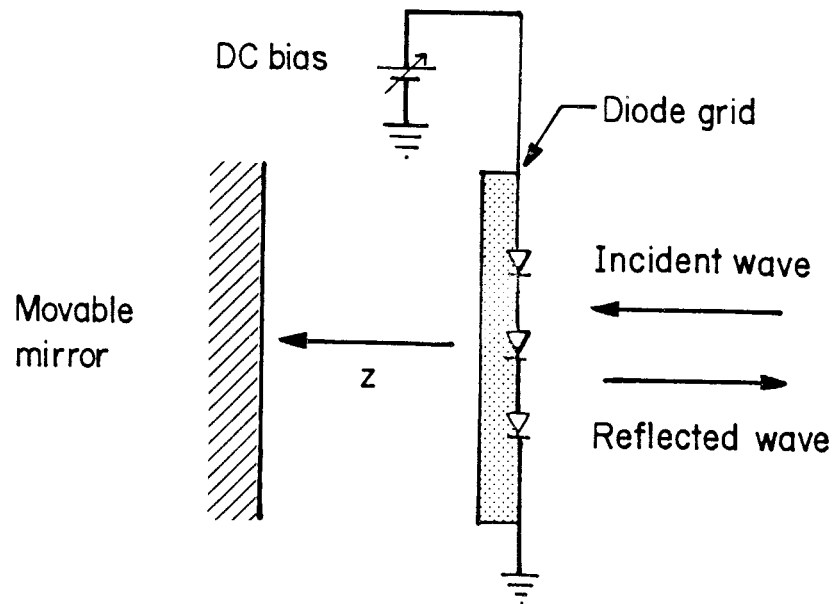
4.5 Reflection Measurements of Diode-Grids

Comparisons between theory and experiment up to this point have been reasonably good. This is because the quartz plate is plane-parallel to within $5\ \mu\text{m}$ and has a uniform layer of bismuth. On the other hand, a diode-grid has both thickness variation and nonuniform device parameters. Typically, the wafer thickness varies between $10\ \mu\text{m}$ and $30\ \mu\text{m}$. This is due to the manual lapping procedure. A total of 10 % of the diodes are expected to be open-circuited. This is due to over-etching during diode fabrication and removal of bad diodes that are shorted electrically or have a low breakdown voltage during diode testing. They tend to scatter randomly throughout the wafer. Nonuniformity of diode characteristics are also expected. They scatter less randomly throughout the wafer. This is mainly due to material properties and alignment during fabrication. These factors are not readily amenable to analysis and make a comparison between theory and experiment difficult. This is why sample preparation plays such a vital role in quasi-optical dielectric measurements [2-6]. Although our calculation does not take these variations into account, they should represent some sort of average and provide useful information for the designer.

A family of tuning curves were measured in order to see the effect of DC bias and millimeter frequency on the diode-grid circuit parameters. Figure 4.12a shows a diode-grid mounted on a pc-board that provides external DC bias to



(a)



(b)

Figure 4.12. (a) Photograph of a diode-grid mounted on a pc-board. (b) Reflection measurement configuration of a diode-grid.

the diodes. A network of variable resistors driven by constant current sources is used to provide adjustable floating voltages to each row of the diode grid. The physical dimensions of this wafer are 2 cm in width, 3 cm in length and $376\ \mu\text{m}$ in thickness. Approximately 91 % of the varactors are functional and the rest are open-circuited. Figure 4.12b shows the configuration in which the tuning curves were made. Using a multi-dimensional simplex optimization algorithm [9], we curve-fitted the measured results with an equivalent circuit model based on a transmission-line theory. The error function was defined to be the absolute value of the difference between the measured and the calculated complex reflection coefficient.

Figure 4.13 shows one of these measured tuning curves at 94 GHz. This particular curve was measured with zero bias on the diode-grid. Four fitting parameters were used. The first parameter, phase calibration length, represents the thickness of a layer of air inserted in front of the diode-grid to account for the inaccuracy in phase-calibration. It allows the calculated phase of reflection to be adjusted by a constant offset. The second parameter, initial mirror position, represents the thickness of another layer of air inserted between the diode-grid and the mirror to account for the inaccuracy in measuring their initial separation. This has the effect of translating both the calculated reflectance and phase of reflection horizontally. The last two fitting parameters are the real and imaginary part of the diode-grid impedance. On this basis, theoretical curves were plotted using the best-fitted phase-calibration length ($792\ \mu\text{m}$), initial mirror position ($1663\ \mu\text{m}$), diode-grid impedance ($58 + j94\ \Omega$), the measured wafer thickness ($376\ \mu\text{m}$), and the index of refraction of GaAs (3.6 from Afsar and Button [6]). The measured phase-calibration length was $727\ \mu\text{m}$ and the initial mirror position was $1702\ \mu\text{m}$. The measured diode series resistance was $78\ \Omega$ with a standard deviation of $19\ \Omega$ and the zero bias capacitance was $30\ \text{fF}$ with

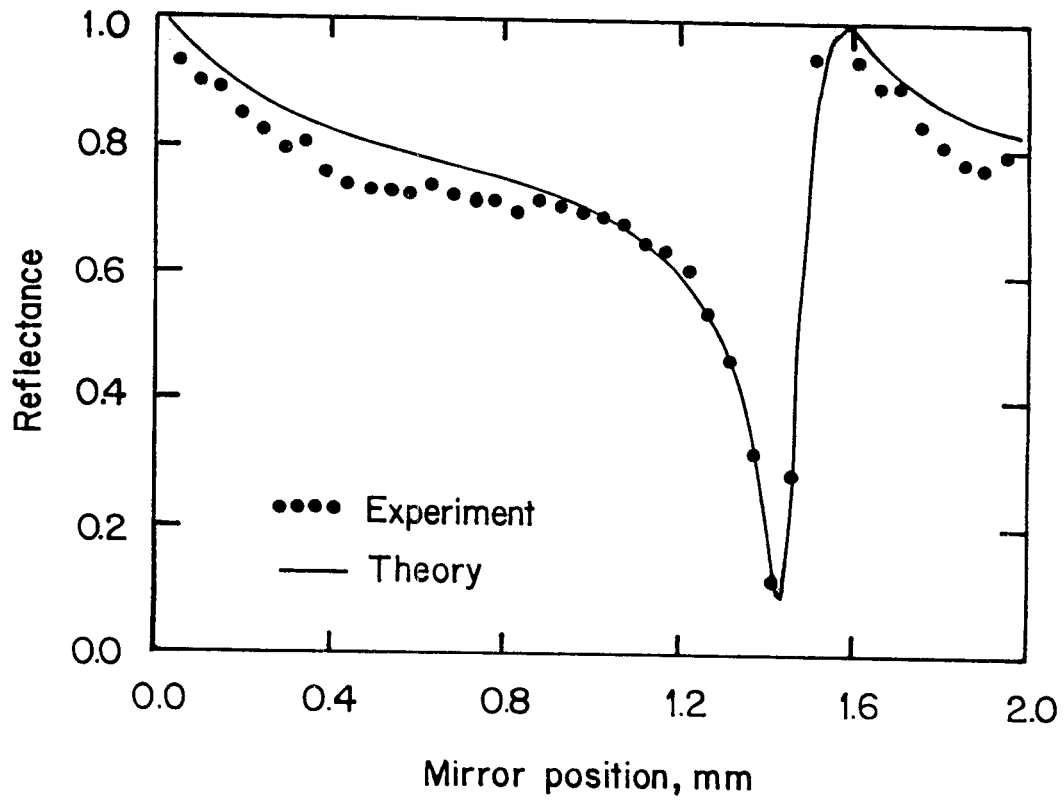


Figure 4.13a. Measured reflectance tuning curve of a diode-grid at 94 GHz.

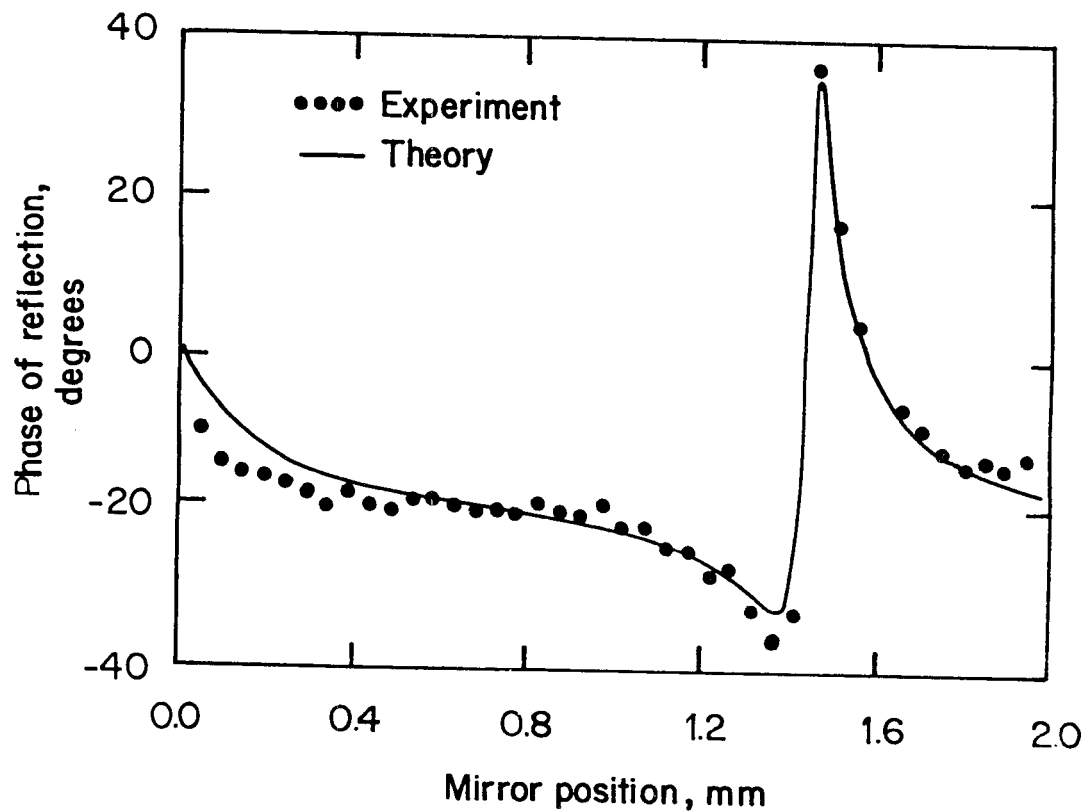


Figure 4.13b. Measured phase of reflection tuning curve of a diode-grid at 94 GHz.

a standard deviation of 10 fF. This is based on sampling 95 out of 2000 possible diodes. From the measured grid period ($504 \mu\text{m}$), strip width ($18 \mu\text{m}$), and strip length ($450 \mu\text{m}$), the strip inductance calculated from the quasi-static formula is 0.26 nH, which corresponds to an inductive reactance of 153Ω at 94 GHz. Subtracting the best-fitted diode-grid reactance from the calculated inductive reactance, we get 59Ω for the capacitive reactance due to the varactor. This corresponds to 29 fF, which agrees with the measured zero-bias capacitance (30 ± 10 fF) at 1 MHz. Following this procedure of measuring diode capacitance at Rf frequency, figure 4.14a compares the capacitance-voltage (CV) measured characteristic at 94 GHz with the CV characteristic measured at 1 MHz. Figure 4.14b shows the corresponding series resistance measured at 94 GHz. The decreasing trend of the series resistance as a function of reverse bias is expected, since the resistance associated with the undepleted region of the diode decreases as the reverse bias increases; however, the amount of the decrement seems a little high.

To investigate the phase shift capability of this diode-grid, the measured phase and amplitude of these tuning curves for a particular mirror position is plotted as a function of bias voltage. The largest phase shift occurs at a mirror position of 3.23 mm. Figure 4.15 shows a comparison between experiment and theory based on transmission-line. In calculating the reflectance and phase of reflection, the assumed diode-grid parameters are based on average values of the corresponding parameters measured from the tuning curves. The average series resistance is 49Ω , the minimum and maximum diode-grid reactance are 60Ω and 105Ω , respectively, and the average diode-grid to mirror separation is 3.16 mm. However, a phase calibration length of $795 \mu\text{m}$ is used. The calibration length averaged from the measured tuning curves is $761 \mu\text{m}$, which corresponds to an 8° vertical shift. Phase shift performance is about 40° and average reflection loss is 6-dB.

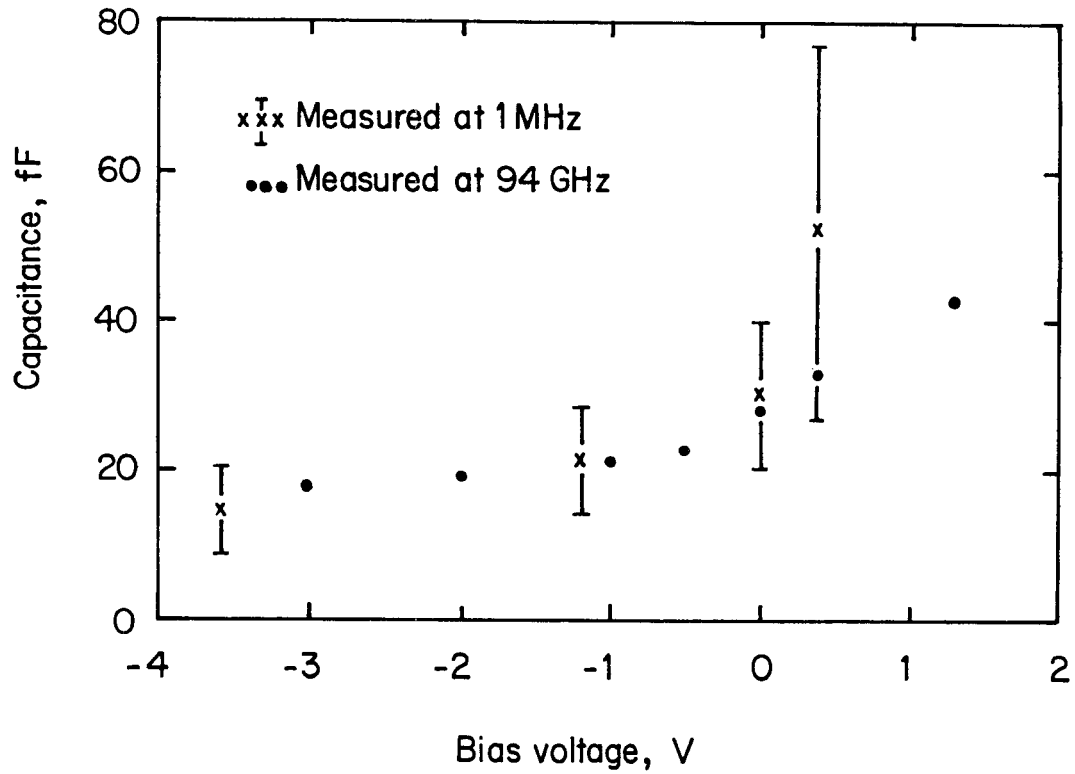


Figure 4.14a. A comparison between the measured capacitance-voltage (CV) characteristic of a diode-grid at 94 GHz and the measured CV characteristic at 1 MHz. Bars represent one standard deviation.

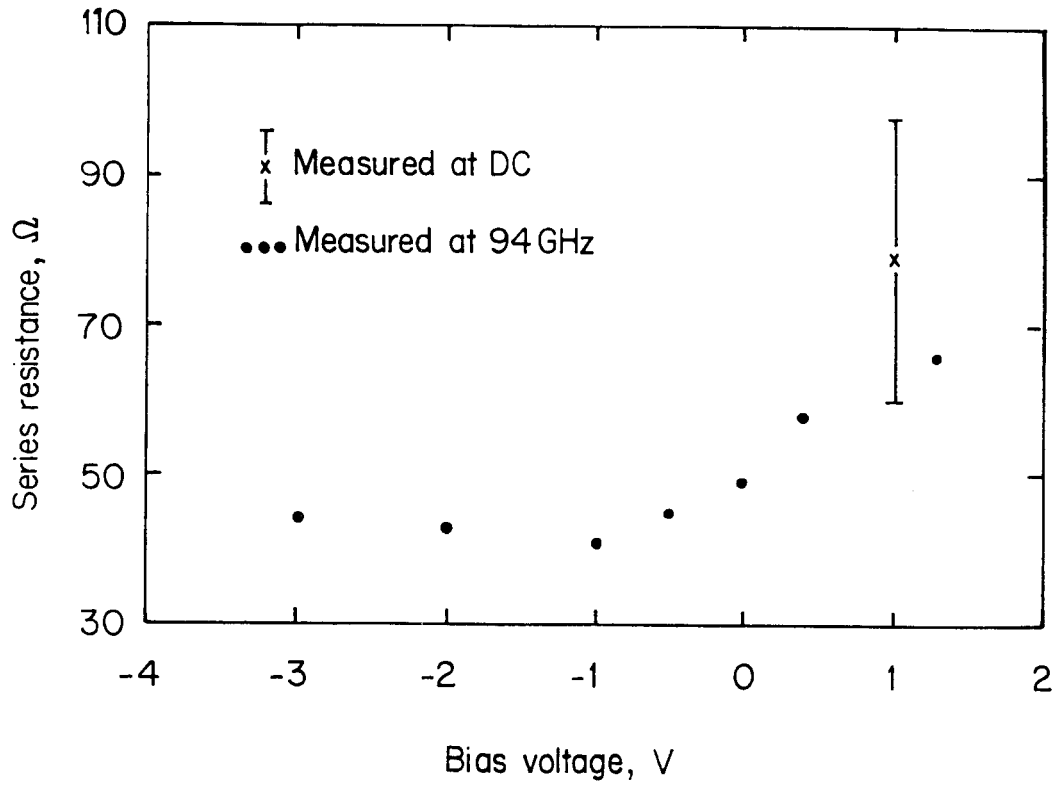


Figure 4.14b. A comparison between the measured resistance-voltage characteristic of the diode-grid at 94 GHz and the measured series resistance at DC. Bar represents one standard deviation.

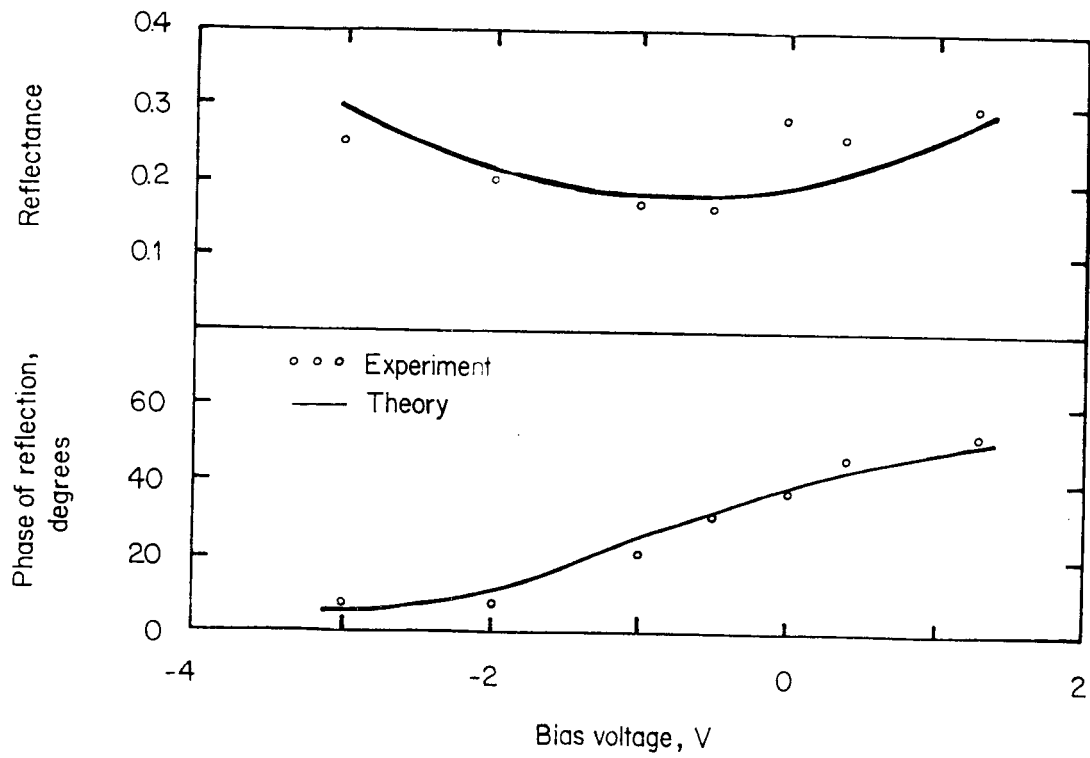


Figure 4.15. A comparison of the measured and the calculated phase shift performance as a function of bias voltage at 94 GHz.

In exploring the frequency dependence of the diode-grid, tuning curves of the same diode-grid were measured at several frequencies with zero bias on the grid. This was done in Professor Luhmann's laboratory at UCLA, where backward wave oscillators were available. Figure 4.16 shows the result of these measurements. The circles are the measured diode-grid impedance at frequencies as shown. The solid line shows the corresponding calculated diode-grid impedance, assuming a strip inductance of 0.26 nH, a diode series resistance of 78Ω , and a zero bias diode capacitance of 30 fF. The agreement is reasonably good at 90 GHz, although it deteriorates quickly as the frequency approaches 130 GHz. This is expected since the effective dielectric wavelength is approaching the grid period (0.5 mm).

Figure 4.17 shows another configuration used in measuring phase shift performance of a diode-grid. A different diode-grid was used in this measurement. The largest phase shift occurred when the tuning mirror was placed 1.49 mm away from the diode-grid. A 70° phase shift and an average of 6.5-dB reflection loss was obtained. Figure 4.18 shows a comparison between the measured phase shift performance and calculation based on transmission-line theory. This comparison is complicated by the fact that the wafer thickness varies between $210 \mu\text{m}$ to $230 \mu\text{m}$, 4 out of 35 rows of the diode-grid were shorted during the measurement, and the parasitic capacitance and series resistance of the diodes cannot be measured at low-frequency. The diode series resistance could not be measured accurately at DC because the surface of this wafer has many ripples with a feature size of about $1 \mu\text{m}$. This created the problem of maintaining a good contact between the probes and the metal contacts; consequently, the contact resistance became too large (roughly 300Ω) and dominated the actual diode series resistance. However, the diode series resistance was measured from a tuning curve at 93 GHz. The measured values were 26Ω for the series resistance

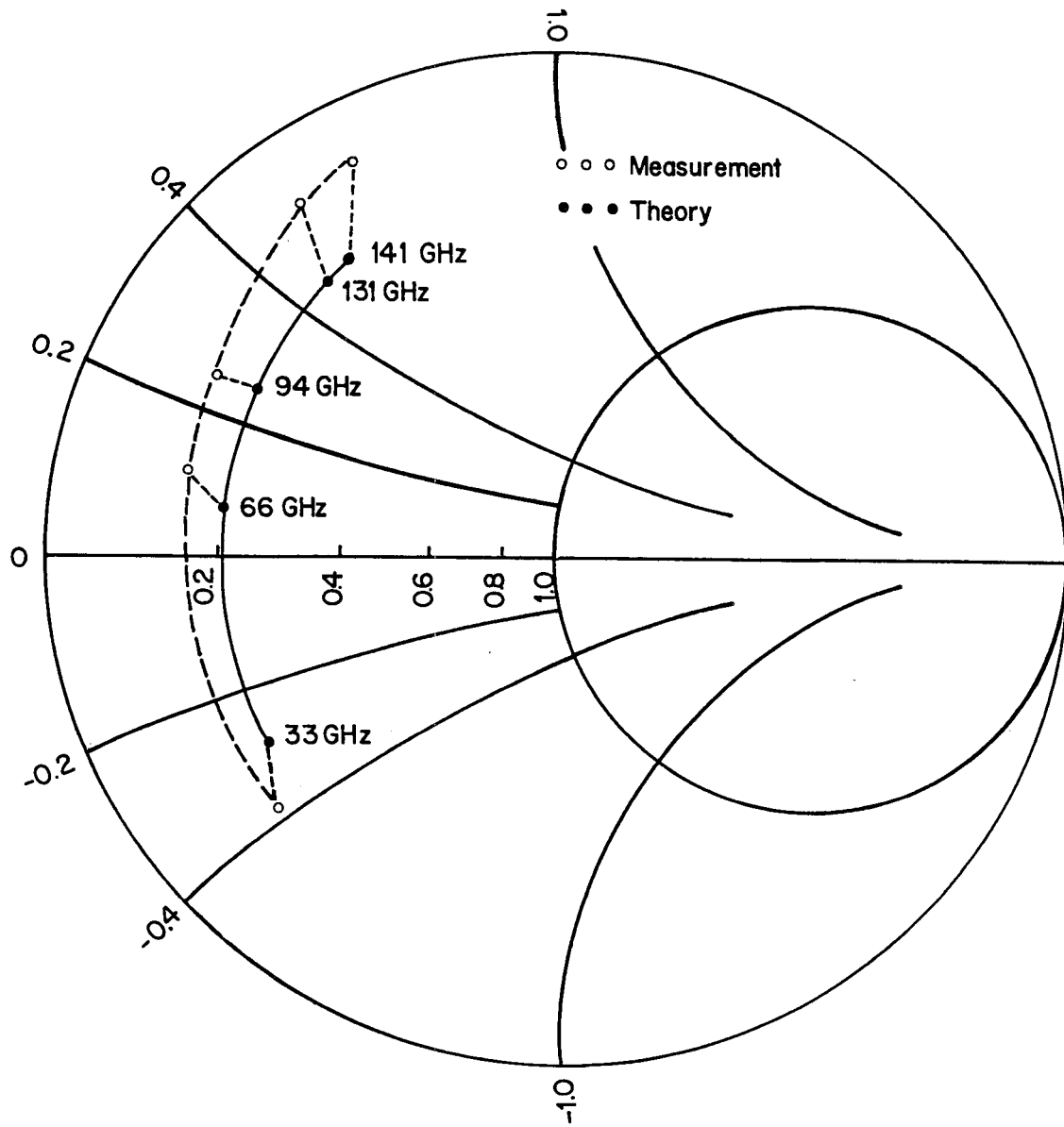
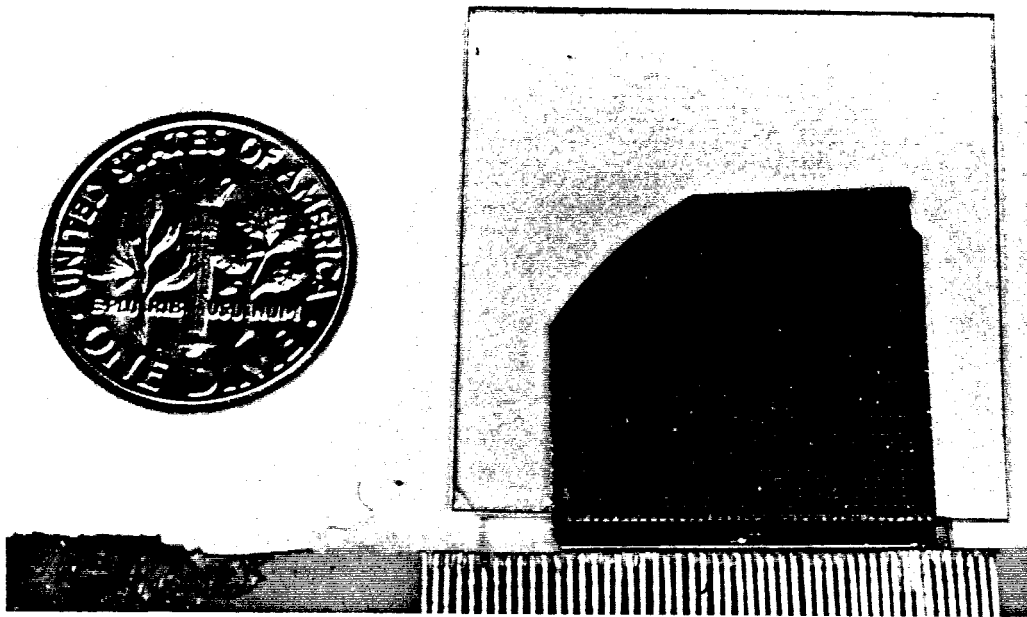
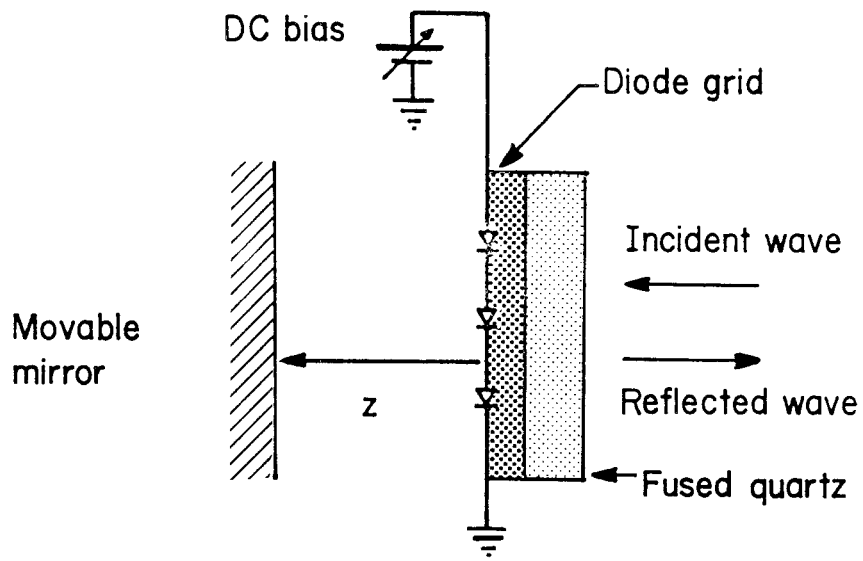


Figure 4.16. A comparison of the measured and the calculated diode-grid impedance as a function of the RF frequencies.



(a)



(b)

Figure 4.17. (a) Photograph of diode-grid on fused quartz. (b) Reflection measurement configuration of diode-grid on fused quartz.

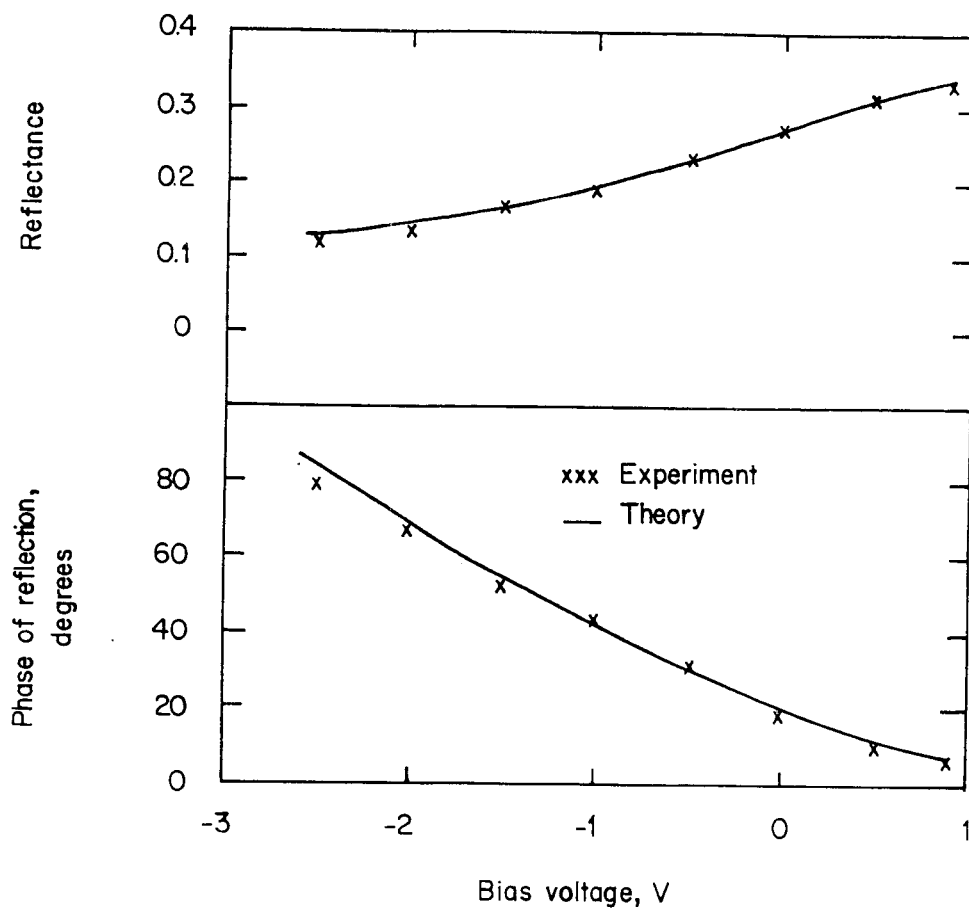


Figure 4.18. A comparison between the measured phase shift performance of a diode-grid in parallel with a fused quartz at 93 GHz.

and 62Ω for the reactance. Theoretical curves were plotted using the measured series resistance (26Ω), the average wafer thickness ($218 \mu\text{m}$), and the calculated grid inductive reactance (153Ω). In addition, we assumed that the initial mirror position was 1.45 mm , the phase calibration length was 1 mm , the published refractive index for fused quartz and GaAs was 1.96 and 3.6 , respectively [6], and the diode capacitance varied from 35 fF at 0.9 V to 18 fF at -2.75 V . The measured initial mirror position was 1.49 mm , the phase calibration length was 0.91 mm , and the average diode capacitance was 27 fF at zero bias and 20 fF at -3 V and had a standard deviation of 15 fF and 13 fF , respectively. Sensitivity analysis indicates that phase response is quite sensitive to wafer thickness and initial mirror position, shifting vertically 1° per micron for each.

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Chapter 5

Discussion and Future Work

In this thesis, several possible applications of diode-grids were proposed. In laying down the groundwork for these applications, a model of the diode-grid was presented and subsequently used in designing a diode-grid phaser shifter. A computer-aided design tool has been developed to provide an interactive graphics environment for doing designs and to form a basis for comparing theoretical and experimental results. A small aperture reflectometer that uses a wave-front interference technique has been developed to measure the reflection coefficient of diode-grids. The measured results have shown the diode-grid model to be reasonably accurate for doing the designs. A fabrication procedure for diode-grids has been demonstrated. The combination of Zah's self-aligning process and the liquid-crystal detection technique made it feasible. Monolithic diode-grids with 2000 Schottky varactors have been fabricated on 6 cm^2 GaAs substrate. A phase shift of 70° with a 6.5-dB loss was measured for a single diode-grid. Further improvements remain to be done.

Although the fabrication process had a reasonable yield, the quality of the diode was not as good. The diode series resistance was too high. The breakdown voltage was too low; therefore, the ratio of the capacitance at zero bias to the capacitance at breakdown was too low. The average series resistance was $26\ \Omega$ for one grid and $78\ \Omega$ the other grid. The breakdown voltage for both diode-grids was about 3 V. This led to a capacitance ratio of about 2. Presently these limit the performance of the diode-grid.

One of the dominant factors of diode series resistance is the product of doping concentration and thickness of the n^+ -layer. For the diode-grid that has a series resistance of $78\ \Omega$, the doping concentration and thickness of the n^+ -layer were

about $1 \times 10^{18} \text{ cm}^{-3}$ and $1.8 \mu\text{m}$, respectively. However, the diode series resistance can be lowered by increasing the doping concentration and thickness of n^+ -layer. Ballamy and Cho [1] demonstrated beam-leaded diodes with series resistances of 4 to 8Ω . They used $1 \times 10^{18} \text{ cm}^{-3}$ for the doping concentration and $6 \mu\text{m}$ for the thickness of the n^+ -layer. Clifton *et al.* [2] fabricated a Schottky diode that had a series resistance of 7Ω . They used $3 \times 10^{18} \text{ cm}^{-3}$ for the doping concentration and $3 \mu\text{m}$ for the thickness of the n^+ -layer. Jarry *et al.* [3] developed a mixer diode with an incredibly low series resistance that was less than 3Ω . They used a doping concentration that was greater than $2 \times 10^{18} \text{ cm}^{-3}$ and 2 to $3 \mu\text{m}$ for thickness of the n^+ -layer. These results indicate that there is an excellent chance for improving the series resistance of our diodes.

Among the factors that influence the diode breakdown voltage, contamination due to oil vapor back-streaming from the diffusion pump into the evaporating chamber was the most probable cause of our low diode breakdown voltage. The reverse breakdown characteristic was soft, and this led to a breakdown voltage of about 3 V at $0.5 \mu\text{A}\mu\text{m}^{-2}$. This is about 25% of Sze's prediction, which is 12 V for avalanche breakdown in a one-sided abrupt junction diode with a doping concentration of $1.5 \times 10^{17} \text{ cm}^{-3}$ [4]. The doping concentration for our diodes was $1.5 \times 10^{17} \text{ cm}^{-3}$ at the GaAs surface and decreased inversely as a function of the depth in the GaAs. The low breakdown voltage can be increased by using *in-situ* MBE aluminum, or by evaporating the metal for the Schottky contact in an oil-free ultra high vacuum system. For example, Cho and Dernier [5] deposited *in-situ* MBE aluminum on the GaAs epitaxy in the MBE growth chamber after the epitaxy was grown and before it was exposed to air. They fabricated Schottky diodes with 15 V breakdown voltage, which was about 60% of Sze's prediction for diodes with a doping concentration of $5 \times 10^{16} \text{ cm}^{-3}$. Sato *et al.* [6] demonstrated that Schottky diodes fabricated in an oil vapor free vacuum had

a higher breakdown voltage than diodes fabricated in an oil diffusion pumped vacuum. They indicated an 3.5 V to 5.3 V improvement for their diodes, which had a doping concentration of $3.5 \times 10^{17} \text{ cm}^{-3}$. The 5.3 V breakdown voltage corresponds to 75 % of Sze's prediction. In addition, Schottky diodes with near theoretical breakdown voltage have been fabricated by other researchers. Clifton *et al.* [2] fabricated diodes with 10 V breakdown voltage. This was about 80 % of Sze's prediction. Immorlica and Wood [7] developed diodes with 13 V breakdown voltage, and this was about 85 % of Sze's prediction. These were abrupt junction Schottky diodes. Their doping concentrations were $1.5 \times 10^{17} \text{ cm}^{-3}$ and $1 \times 10^{17} \text{ cm}^{-3}$, respectively. Their diode geometries were strips, which were similar to our diodes. Furthermore, their diode areas were about $10 \mu\text{m}$, and proton bombardments were used for isolating their diodes. Although their methods of deposition were not given, these results do indicate that there is a good chance for improving the breakdown voltage and therefore the capacitance variation of our diodes.

Based on the reasonable agreement between theory and experiment, we believe our diode-grid model to be sufficiently accurate for doing the designs. Furthermore, works on second harmonic generation using diode-grids are being investigated by Christina Jou in Professor Luhmann's group at UCLA. They also showed a reasonable agreement between theory and experiment. For the diode-grid that had a series resistance of 78Ω , the measured transmittance as a function of the position of the tuning slabs agreed well with the transmission-line model. Also, a second harmonic conversion efficiency of 16 % and an output power of 0.5 W were measured at 66 GHz when a pulsed magnetron at 33 GHz was used to pump the diode-grid that had a series resistance of 26Ω .

These results indicate an exciting future for the diode-grid. In electronic beam-steering, the array design that is based on using two diode-grids appears

to be feasible. Diode-grids for harmonic power generation look promising. Also, the integration of other electronic devices into a periodic grid is beginning to emerge. In our group, Zorana Popović is building a Gunn diode-grid on a Duroid substrate at 10 GHz. In any case, the future of integrating electronic devices into a periodic grid will be very exciting as well as promising.

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Appendix A

Varactor Diode-Grid Fabrication Procedure

The following contains notes on the fabrication of a Schottky-barrier varactor diode-grid on a semi-insulating GaAs wafer. A total of five masks is used. Because a GaAs wafer is quite large and fragile, a holder made of teflon is used to hold the wafer during rinsing and developing. Figure A.1 shows the teflon holder. A teflon tweezer is used for etching. This process evolved from Zah's process [1]. The book by Ralph Williams is an excellent reference on GaAs processing techniques [2]. Howard Chen in Professor Yariv's group and Dr. Kjell Stolt in TRW have been the principal suppliers of the MBE wafers.

Fabrication Procedure

1. Obtain a GaAs wafer with the following layers.

0.2 μm of *in situ* MBE aluminum.

0.7 μm of n layer with a hyperabrupt doping profile.

1.8 μm of n^+ layer with $3 \times 10^{18} \text{ cm}^{-3}$ doping concentration.

- * Some MBE wafers have indium on the back side and some do not. If indium is present, mount the wafer on a lapping block with wax and lap away the indium on the backside of the wafer. A mixture of water and a 5 μm diameter Al_2O_3 lapping powder made by Buehler is often used. Afterwards, acetone can be used to dissolve the wax.

2. Determine the crystal orientation of the wafer by making a strip pattern on a small chip scribed from the wafer. Note: If aluminum is present on the chip, remove the aluminum by etching it in aluminum etchant for GaAs.

- a. Standard lift-off photoresist process

photoresist : AZ 1350J

spin speed : 4000 rpm

prebake : 85 °C for 25 min.

exposure : 25 sec.

development : 30 sec. (agitate in 1:1 diluted developer.)

- b. Hardbake the photoresist at 125 ° C for 10 min.
 - c. Using a 93 % concentrated H_2SO_4 , and a 30 % concentrated H_2O_2 , mix the solution ($\text{H}_2\text{SO}_4:\text{H}_2\text{O}_2:\text{H}_2\text{O}$) with a ratio of 1:8:160. Stir it with a magnetic stirrer in a petri-dish for an hour. The etch rate is about a quarter of a micron per min.
 - d. Etch the wafer and rinse it in 20 beakers of DI water.
 - e. Cleave a slice from the chip and mount it sideways on double-sided tape. Note the etched profile in a microscope. The diode orientation should be in the direction of a 'V-groove' etch or perpendicular to the direction of a 'dovetail' etch. Figure A.2 shows the etched profile.
3. If *in-situ* aluminum is not available, then clean the wafer thoroughly and evaporate aluminum.
- a. Cleaning procedure.
 - acetone ultrasonic bath : 10 min.
 - ethanol ultrasonic bath : 5 min.
 - hot Transene 100 bath : 5 min.
 - cold Transene 100 bath : 30 sec.
 - let it dry by itself
 - b. Etch away the native oxide on the wafer for a minute in a solution of ($\text{H}_2\text{O}:\text{HCl}$) with a 1:1 ratio and rinse it with 20 beakers of DI water.
 - c. Load the wafer into the vacuum immediately. Thermo-paste is used to mount the wafer on a glass slide, which is then mounted onto the sample holder. Evaporate 2000 Å thick of aluminum in a vacuum with pressure

below 3×10^{-6} Torr. If possible, this should be done in an oil-free vacuum system at lower pressure [4,5]. The aluminum source must be cleaned by etching it in organic solvent and aluminum etchant.

4. Generate the self-aligning mask for defining the ohmic contact and the width of the Schottky contact.
 - a. Use the standard lift-off photoresist process. (see 2a.)
 - b. Hardbake photoresist at 125° C for 10 min.
 - c. Etch the aluminum in Transene aluminum etchant - Type D. Typically, it takes about 60 sec. at 58° C.
 - d. Rinse the wafer in DI water and inspect it under the microscope. Look for signs of under-etched diode tips. They tend to form short circuits when AuGe/Ni/Au is evaporated. Typically, several rounds of etching aluminum for 10s and rinsing the wafer are required to get good results. Because the wafer is quite large, some etching nonuniformity is expected. If the situation appears desperate, dissolve the photoresist and start over again since there are "tons" of aluminum still un-etched on the wafer. This is worth the trouble because an MBE wafer is precious.
5. Etch away the n-layer until the n^{+} -layer is exposed.
 - a. Mix the solution ($H_2SO_4:H_2O_2:H_2O$) with a ratio of 1:8:160. Stir it with a magnetic stirrer in a Petridish for an hour. Note: Use the 93 % concentrated H_2SO_4 , and the 30 % concentrated H_2O_2 .
 - b. Etch the wafer and rinse it in 20 beakers of DI water. The etch rate is a quarter of a micron per min. Generally, an extra 15 second is added to over-etch the crystal. This is just a precautionary measure for exposing thoroughly the n^{+} layer throughout the wafer.
6. Evaporate AuGe/Ni/Au with the following thicknesses ($700\text{\AA}/300\text{\AA}/2000\text{\AA}$) at a pressure below 3×10^{-6} torr. The edges of the wafer are taped with

paper so that lift-off process in acetone becomes easier.

7. Remove the thermo-paste on the backside of the wafer with a Q-tip, that is slightly dampened with acetone. Lift off the photoresist in acetone and ethanol baths.
8. Generate the etching mask for defining the lead of the Schottky contact.
 - a. Use the standard lift-off photoresist process. (see 2a.)
 - b. Hardbake the photoresist at 125 °C for 10 min.
 - c. Etch the aluminum (see 4 b.)
 - d. Lift off the photoresist in acetone and ethanol baths.
9. Generate the bonding pad mask with standard lift off photoresist process. Evaporate AuGe/Ni/Au with the following thicknesses (700Å/300Å/2000Å) at pressure below 3×10^{-6} torr. The purpose for this is to make a good bonding pad. The alloying process make the metals partly dissolve into the GaAs and acquire a rough surface texture. This is worthwhile since wire bonding can be very difficult if the bond wires do not like to stick on the bonding pads.
10. Alloy the ohmic contacts and bonding pads at 460 °C for 10 min. in forming gas (15 % of N₂ and 85 % of H₂).
11. Generate proton implantation mask with photoresist.
 - a. Pattern implantation mask.
 - photoresist : AZ 4620
 - spin speed : 4000 rpm
 - prebake : 85 °C for 1 hour
 - exposure : 70 sec. at 20 mJ-cm⁻²
 - development : 4 min. (agitate in 1:1 diluted developer)
 - b. Optional: Some people feel that if the photoresist is flood-exposed here, then it will make the removal of the photoresist easier after ion implanta-

tion. This was not noticeable to me.

- c. If implantation is to be done at Caltech, obtain sample holder from Frank So or Sung Kim in Professor Nicolet's group. Use the phosphorus compound to define the implantation boundary. Mount the wafer onto the sample holder with thermo-paste. If implantation is to be done at Hughes, no sample holder is required.
- d. Implantation parameters:
 - 330 keV at $4 \times 10^{14} \text{ cm}^{-2}$ dose
 - 200 keV at $4 \times 10^{12} \text{ cm}^{-2}$ dose
12. Measure the resistance between the adjacent ohmic contacts to make sure that the desired area of the wafer is completely isolated. If isolation is completed, remove the photoresist in an O_2 plasma. Measure the diode parameters.
13. If the breakdown voltage varies widely across the wafer, then probing every varactor to weed out the weak diodes becomes necessary. This can be done at UCLA, since an analytical probing station is available for fast probing.
14. Measure the diode parameters with the HP4145B parameter analyzer and the CV characteristics with the HP4280A C-meter. See software documentation in the appendix. Here probing is quite tricky because contact resistance between the probe tip and the metal depends highly on the amount of pressure applied. Some practice is necessary to get a reasonably low contact resistance.
15. Generate the periodic grid mask and the bonding pad mask simultaneously with the standard lift-off photoresist process. Evaporate 100 \AA of chrome and 3000 \AA of gold. The lift-off here is critical. Lift-off flags must be avoided because they tend to cause electrical shorts. The cause is due to poor edge definition when the photoresist is patterned.
15. Lap the wafer to the desired thickness.
16. If a row of diode-grid is shorted, use liquid crystals to find shorted diodes

and remove them from the wafer with an ultrasonic probe. It is important to prepare the sample properly. A layer of liquid crystal is spun onto the wafer at 1000 rpm. It is important to shake the bottle thoroughly before using it. This usually gives a nice and uniform layer. Use a Q-tip to wipe off the excess liquids on the bonding pad. Mount the wafer on a resistive heated chuck. Illuminate the wafer with a dual fiber optic lamp at approximately 30° incidence relative to the horizon. Use a curve tracer to inject current into the shorted row. Typically more than 5 mA is required to see any noticeable color change. Use the Signatone 850 ultrasonic cutter to remove the bad diode.

References

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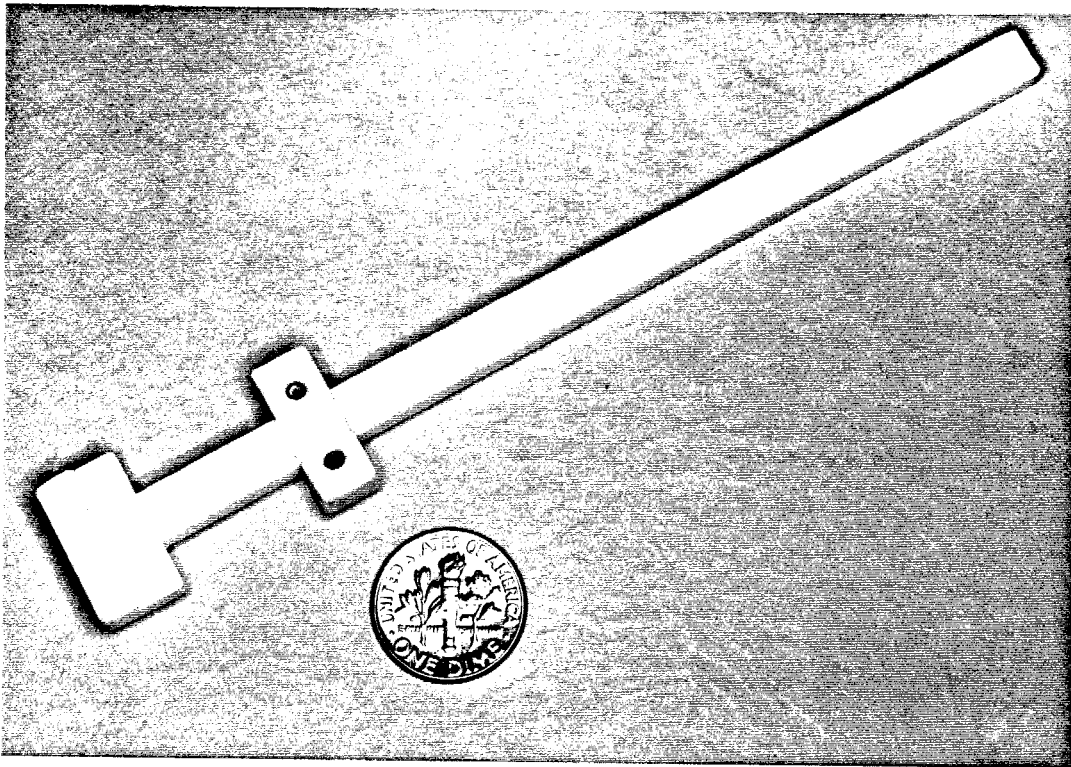
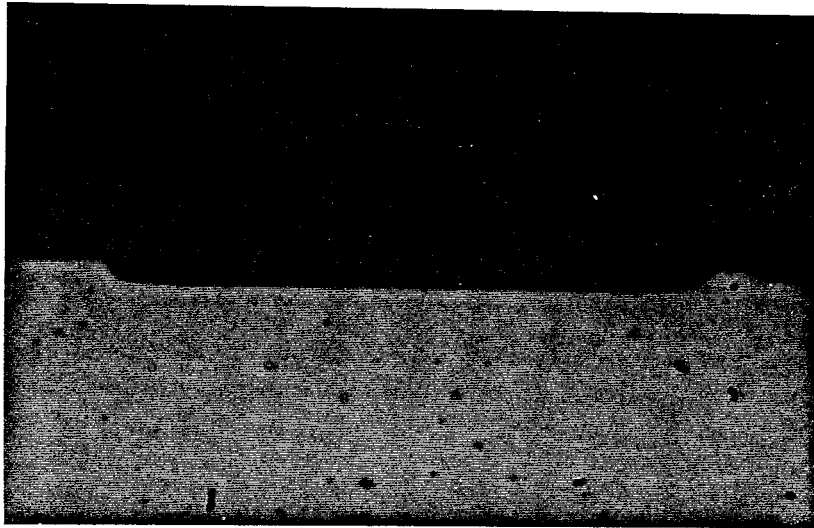
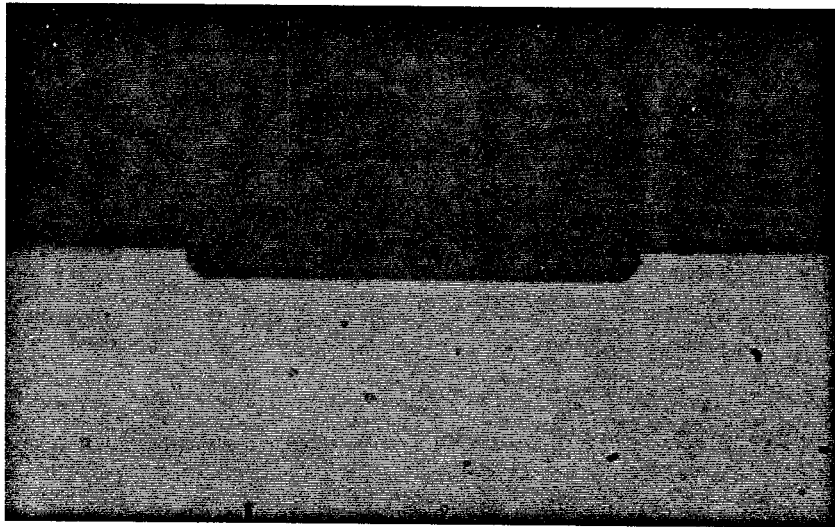


Figure A.1. A teflon holder for holding the GaAs wafer during developing and rinsing.



(a)



(b)

Figure A.2. Crystal orientation. (a) Cross-section profile of a "V-groove". (b) Cross-section profile of a "dove-tail".

Appendix B

Computer Program Listing of TRAP

```
1 program trap(input,output);
2
3
4 { TRAP is an acronym that indicates the calculation of
5 transmittance, reflectance, absorptance, and phase of
6 reflection. Because the calculation of transmittance,
7 absorptance and reflection coefficient for multi-layered
8 media is tedious and time consuming, TRAP was developed to
9 provide an interactive environment for the user to design the
10 circuit and to compare the theoretical and experimental
11 results. It is an interactive graphics program written in
12 Turbo Pascal for an IBM-PC. For the computational algorithm,
13 please see "Thin Films Calculations Using the IBM 650
14 Electronic Calculator," by Jean A. Berning and
15 Peter H. Berning in Journal of The Optical Society of America,
16 Vol. 50, Num. 8, pg. 813, Aug. of 1960. TRAP features a
17 line editor, from which the user can enter a command line
18 that describes the structures of the layered media. Commands
19 include lossy dielectric, grids, lumped elements,
20 and a mirror. The angle of incidence, polarization,
21 wavelength, and layer thicknesses can be varied linearly.
22 Keyboard commands are available to stop, speed up,
23 or slow down the simulation. TRAP also features optimization
24 capability for the user to fit a transmission-line model
25 to the measured reflectance and phase of reflection.
26 The fitting procedure is based on minimizing the absolute
27 value of the complex difference between the calculated and
28 the measured reflection coefficient. For the optimization
29 algorithm, please see Numerical Recipes by W. H. Press et al.,
30 Chap. 10, pp. 274, Cambridge University Press, New York, 1986.
31
32 When the program is run, a main menu is displayed. There
33 are four options including database, simulation, optimization,
34 graphics, and quit. To make a selection, simply press the key
35 of the first letter for an option. For example pressing Q
36 exits the program. Note also that pressing Q in an option exits
37 that option, and pressing the return key repeats that option,
38 although this is not explicitly displayed in each option.
39 The database and the graphics option are menu driven and
40 allow the user to read in a set of reflection coefficient
41 data from an ASCII file and to define the vertical plotting
42 range, respectively. The data file can be viewed or edited in
43 the Turbo editor. The format of this file should be
44 (distance reflectance phase of reflection). It should
45 appear as three columns of numbers. Typically these data are
46 measured from an experiment, but they could be generated
47 for the purpose of design and optimization. The simulation
48 option allows the user to edit a command line describing
49 the layered medium. The following are command definitions.
```

50
 51 Convention : 1.) parameters, r1, r2, r3, ... etc., are real numbers.
 52 2.) i, b, f, ... etc. are definitions.
 53 3.) , and : are delimiters.
 54 4.) a command is usually followed by a set of parameters.
 55
 56 ir1 - incident medium : r1 = refractive index
 57 (default value is 1)
 58 br1,r2 - dielectric boundary: r1 = Re(refractive index)
 59 r2 = Im(refractive index)
 60 ar1,r2 - shunt admittance : r1 = Re(shunt admittance)
 61 r2 = Im(shunt admittance)
 62 fr1,r2 - final medium : r1 = Re(refractive index)
 63 r2 = Im(refractive index)
 64 sr1 - TE polarization : r1 = angle of incidence in
 65 degrees with respect to
 66 the surface normal.
 67 pr1 - TM polarization : r1 = angle of incidence in
 68 degrees with respect to
 69 the surface normal.
 70 wr1 - wavelength of incidence : r1 = wavelength in
 71 arbitrary units.
 72 gr1,r2,r3,r4 - quasi-static model of a square grid :
 73 r1 = length of the period.
 74 r2 = length of the gap.
 75 r3 = length of the post.
 76 r4 = series resistance.
 77 Gr1,r2,r3,r4 - Eisenhart model of a square grid :
 78 r1 = length of the period.
 79 r2 = length of the gap.
 80 r3 = length of the post.
 81 r4 = series resistance.
 82 tr1 - layer thickness : r1 = wavelength in arb. units.
 83 d - plot y-axis in unit of dB. (default is linear)
 84 xri:r2 - plot the variable (x: i,b,a, ... etc.) on x-axis.
 85 r1 = start value of x
 86 r2 = stop value of x
 87

88 Examples: 1.) i1 z0, .11 b3.6 t.233 z0, .11 f1 s0 w1.7:2.7
 89 2.) i1 b3.6 t.233 z.054, .27 b1 t0:2.0 m s0 w3.36
 90 3.) i1 b3.6 t.233 z.054, .1: .3 b1 t1.5 m s1 w3.36
 91

92 The first example is a bandpass filter formed by a pair of
 93 lossless inductive screens. The second example is a zero-bias
 94 diode-grid backed by a mirror that translates from 0 to 2.0 mm.
 95 The third example is a diode-grid with varying reactance and
 96 a stationary mirror tuned to 1.5 mm. These examples had been
 97 programed. To run them, enter the simulation mode and press E.
 98 The description of the multi-layered medium can be edited
 99 using a condensed version of Wordstar commands:

100
 101 control s - left
 102 control d - right

103 control g - delete
 104 control v - change from insert mode to overwrite mode and back.
 105 backspace - deletes left

106

107

108

109

110

111

112

113

114

115

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117

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119

120

121

122

When the layer description is correct, press carriage return to enter it. You can get a screen dump by typing shift PrtSc. The optimization mode allows the user to fit a set of reflection coefficient data to a transmission-line model of the layered medium. The data can be read in from an ASCII file thru the database option. Once the data are entered, enter Q to return to main menu and press O to enter optimization option. The commands for optimization are similar to simulation, except that the command should be in capital letters to signify that the variable is to be optimized. Following the command symbol is the optimization range (r1;r2), where a semicolon is used to separate the minimum (r1) and the maximum (r2).

Example: 4.) i1 b1 T.68;.69 Z.042;.043;.085;.09
 b3.6 t.218 b1 T2.49;2.5 t0:2.0 m s0 w3.36

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The fourth example illustrates an optimization command line for curve-fitting a transmission-line model of a diode-grid with a tuning mirror to the experiment. To run this example, enter the database option, get the measured reflection coefficient from the file "diogrid.pas," return to main menu, enter the optimization mode, press the key E, enter 4, and hit return. The data file is available in the disc on the back cover of this thesis. It was obtained from an actual reflection measurement and is shown in part below. As the optimization advances, the values of each parameter of the multi-layered medium are displayed line by line on the screen. Each line represents a completed computation. The ordering of the parameters in the line starts from the right of the medium to left of the medium, and the associated error is displayed last in the line.

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154

155

type

line = string[250];

complex = ^complex_record;

0.000	0.912	24.704	
0.050	0.903	24.485	
0.100	0.886	24.534	
	.		
	.		
	.		
1.900	0.937	24.039	
1.950	0.944	23.760	
2.000	0.953	23.134	}

```

156     complex_record = record
157     r,i : real;
158     end;
159     complex_matrix = ^complex_matrix_record;
160     complex_matrix_record = record
161     t11,t12,t21,t22 : complex;
162     end;
163     char_set = set of char;
164     glmpnp = array[1..9,1..8] of real;
165     glmp = array[1..9] of real;
166     glnp = array[1..8] of real;
167     rng = array[1..2,1..8] of real;
168
169     const
170     structure : line = '';
171     structuresave : line = '';
172     xmin = 190; xmax = 550; {coordinates of graph corners}
173     ymin = 43; ymax = 163;
174     xd = 3; {plotting interval on x_axis}
175     backspace = #8; enter = #13;
176     edit_set : set of char = [^d,^s,^g,^v, backspace];
177     structure_set : set of char = ['-','.',',','0','1','2','3','4','5',
178         '6','7','8','9',':',';',
179         'w','i','b','t','g','d','s','r','n','o',
180         'z','a','p','f','m','j','q','G','/','.',
181         'B','T','Z','A','I','F','W'];
182     number_set: set of char=['-',',','.',',','0','1','2','3','4','5','6','7','8','9'];
183     delimiter_set : set of char = [',','.',':','w','i','b','t','g','d','s','p',
184         'o','z','a','f','m','j','q','G','/','r',
185         'n',',','B','T','Z','A','I','F','W'];
186     command_set : set of char = ['w','i','b','t','g','d','a','z','r','n',
187         'o','s','p','f','m','j','q','G','/','.',
188         'B','T','Z','A','I','F','W'];
189
190     label pau, main;
191
192     var
193     position, pointer, positionstop : 1..150; {position in structure.}
194     a_heap_pointer, b_heap_pointer : ^integer; {pointer for garbage disposal}
195     x, i : integer; {position along the x-axis, 0 to xmax-xmin. }
196     t : complex_matrix; {complex transmittance matrix}
197     reflectance, transmittance, absorptance, phase : real;
198     error_code : integer; {for val statement}
199     min, max : real; { minimum and maximum values of the x coordinates}
200     c, b : char; {character read at keyboard}
201     dB_plot : boolean; {dB plot if true}
202     yj, rc, pjao, pj, yjm1, cnj, cnje, gj,
203     zj, aj, cno, cnoe, yjmin, yjmld, caj, cnjp : complex;
204     no, sj, ao, nj, l, lj, sjt : real;
205     G_flag, p_flag : string[2];
206     p, d, v, h, QQ, ss, ccc, f1, f2, XX, BB : real;
207     a, g, w, rs, rd, cv, sjsave : real;
208     scan : string[5];

```

```

209 bc, bl, par : complex;
210 m, n, en, positionsave, positionG, positionf : integer;
211 zd, bcv, yjsave, cnosave, ymnp, ymmn, bcsun, blsum, densum : complex;
212 xyz, xyz2 : complex;
213 test_string : line;
214 temp, la, mla, nla, ld, sinc : real;
215 yjsaver, yjsavei, cnosaver, cnosavei : real;
216 intensity, normalization : real;
217 on_off : integer;
218 select : char;
219 filename : string[15];
220 trp : array [1..100 , 1..3] of real;
221 trp1, trp2, trp3 : real;
222 data_transfer : text;
223 data_index, ndata, data_inc : integer;
224 find_error_flag : boolean;
225 ref_err : real;
226 xmi, ylmi, yrmi, xmx, ylmx, yrmx : real;
227 ylrage, ylscale, yrangle, yrscale, xrange, xscale : real;
228 zzzx, zzzy : integer;
229 compon : array[1..20] of real;
230 rtol, ftol : real;
231 nparam, idata, optimization_flag, mvertices, ndim, iter : integer;
232 poly : glmpnp;
233 vert : glnp;
234 face : real;
235 y : glnp;
236 range : rng;
237 range_variable : complex;
238 range_pointer, curve_select : integer;
239
240
241 {GRAPHICS routines}
242
243
244 procedure draw_box (x1,y1,x2,y2,color : integer);
245 begin
246   draw (x1,y1,x1,y2,1);
247   draw (x1,y2,x2,y2,1);
248   draw (x2,y2,x2,y1,1);
249   draw (x2,y1,x1,y1,1);
250 end;
251
252 procedure draw_x_ticks(x1,y1,x_increment,x2 : integer);
253 begin
254   while (x1<=x2) do begin
255     draw (x1,(y1+1),x1,(y1-2),1);
256     x1 := x1 + x_increment;
257   end; {while}
258 end; {draw_x_ticks}
259
260 procedure draw_y_ticks(x1,y1,y_increment,y2 : integer);
261 begin

```

```

262   while (y1<=y2) do begin
263     draw ((x1+2),y1,(x1-4),y1,1);
264     y1 := y1 + y_increment;
265     end; {while}
266 end; {draw_y_ticks}
267
268 procedure draw_graph(x1,y1,x2,y2,
269   x_increment, y_increment_left, y_increment_right :integer);
270 begin
271   draw_box(x1,y1-2,x2,y2+2,1);
272   draw_x_ticks (x1,y1,x_increment,x2);
273   draw_x_ticks (x1,y2,x_increment,x2);
274   draw_y_ticks (x1,y1,y_increment_left,y2);
275   draw_y_ticks (x2,y1,y_increment_right,y2);
276 end; {procedure draw_graph}
277
278 procedure write_x_coordinates(x1,y1,x_increment :integer;
279   x1c,xc_increment,x2c : real; field,fix :integer);
280 begin
281   repeat
282     gotoxy((x1 div 8), (y1 div 8));
283     write (x1c:field:fix);
284     x1c := x1c + xc_increment;
285     x1 := x1 + x_increment;
286     until x1c > x2c;
287 end; {write_x_coordinates}
288
289 procedure write_y_coordinates(x1,y1,y_increment :integer;
290   y1c,yc_increment,y2c : real; field, fix : integer);
291 begin
292   repeat
293     gotoxy((x1 div 8), (y1 div 8));
294     write (y1c:field:fix);
295     y1c := y1c + yc_increment;
296     y1 := y1 + y_increment;
297     until y1c < y2c;
298 end; {write_y_coordinates}
299
300 function dB(x : real) : real; {give the dB difference}
301 begin
302 dB := 10*ln(x)/ln(10);
303 end; {dB}
304
305 procedure clean_box;
306 begin
307   gotoxy(2,2);
308   write(' ');
309   gotoxy(2,3);
310   write(' ');
311 end;
312
313 procedure set_up;
314 begin

```

```

315   if select = 'm' then begin
316       hires; hirescolor(15);
317       draw_box(0,4,639,33,1);
318       gotoxy(2,2);
319       write('MAIN MENU: ');
320       write('s(simulation) d(database) ');
321       write('o(optimization) g(graphics)');
322       if c = 'q' then read(kbd,select);
323       if (select = 's') or (select = 'd') then
324           begin
325               draw_graph(xmin,ymin,xmax,ymax,(xmax-xmin)div 5,
326                   (ymax-ymin)div 5, (ymax-ymin)div 4);
327               if select = 's' then begin
328                   gotoxy(1,7); write('Tran. . . . .');
329                   gotoxy(1,8); write('Refl. . . . .');
330                   gotoxy(1,9); write('Abs. . . . .');
331                   gotoxy(1,10); write('Pha. . . . .');
332               end;
333           end;
334   end;
335   graphwindow(0,0,639,199);
336   case select of
337       's' : begin
338           clean_box;
339           gotoxy(2,2); write('Key: i(nt) b(ndry) t(kns) g(rd) ');
340           writeln('f(nl) w(vlth) m(ir) G(*) s(TE) p(TM) d(B) q(it)');
341           gotoxy(2,3); writeln('Structure:');
342       end;
343       'd' : begin
344           clean_box;
345           gotoxy(2,2); write('DATABASE: enter data_filename ');
346       end;
347       'o' : begin
348           optimization_flag := 1;
349           clean_box;
350           gotoxy(2,2); write('Key: i(nt) b(ndry) t(kns) g(rd) ');
351           writeln('f(nl) w(vlth) m(ir) G(*) s(TE) p(TM) d(B) q(it)');
352           gotoxy(2,3); writeln('Structure:');
353       end;
354   end; {of case}
355
356   end; {set_up}
357
358   procedure write_y_axis;
359   begin
360       write_y_coordinates(xmax+20,ymin+7,(ymax-ymin) div 4,
361           yrmx,(yrmi-yrmx)/4,yrmi,4,0);
362       if dB_plot then
363           write_y_coordinates(xmin-20,ymin+7,(ymax-ymin) div 5,
364               0,-10,-50,3,0)
365       else
366           write_y_coordinates(xmin-20,ymin+7,(ymax-ymin) div 5,
367               ylmx,(ylmi-ylmx)/5,ylmi,3,1);

```

```

368 end; {write_y_axis}
369
370 procedure write_x_axis;
371 begin
372     write_x_coordinates (xmin,ymax+14,(xmax-xmin) div 5,
373                         min,(max-min)/5,max,4,1);
374 end; {write_x_axis}
375
376 procedure plot_result(on_or_off : integer);
377 begin
378     graphwindow (xmin,ymin,xmax,ymax);
379     trp3 := yrmx - phase*180.0/pi;
380     if (on_off and $8) <> 0 then plot(x*xd,round(trp3*yrscale),on_or_off);
381     if dB_plot then begin
382         if odd(x) then
383             if (on_off and $4) <> 0 then
384                 plot(x*xd,round(-dB(transmittance)/50*(ymax-ymin)),on_or_off);
385             if odd(x div 2) then
386                 if (on_off and $2) <> 0 then
387                     plot(x*xd,round(-dB(reflectance)/50*(ymax-ymin)),on_or_off);
388             if odd(x div 3) then
389                 if (on_off and $1) <> 0 then
390                     plot(x*xd,round(-dB(absorptance)/50*(ymax-ymin)),on_or_off);
391         end {if}
392     else begin
393         trp1 := ylmx - transmittance;
394         trp2 := ylmx - reflectance;
395         trp3 := ylmx - absorptance;
396         if odd(x) then
397             if (on_off and $4) <> 0 then
398                 plot(x*xd,round(trp1*ylscale),on_or_off);
399             if odd(x div 2) then
400                 if (on_off and $2) <> 0 then
401                     plot(x*xd,round(trp2*ylscale),on_or_off);
402             if odd(x div 3) then
403                 if (on_off and $1) <> 0 then
404                     plot(x*xd,round(trp3*ylscale),on_or_off);
405         end; {else}
406 end; {plot_result}
407
408
409 {COMPLEX NUMBERS routines}
410
411
412 function co (s,t : real) : complex; {makes a complex number}
413 var
414     u : complex;
415 begin
416     new(u);
417     u^.r := s;
418     u^.i := t;
419     co := u;
420 end;

```

```

421
422 function sc(s : real; t : complex) : complex;
423 {multiply a real number s by t}
424 var
425   u : complex;
426 begin
427   new(u);
428   u^.r := s * t^.r;
429   u^.i := s * t^.i;
430   sc := u;
431 end;
432
433 function cc(s : complex) : complex; {complex conj}
434 var
435   u : complex;
436 begin
437   new(u);
438   u^.r := s^.r;
439   u^.i := -s^.i;
440   cc := u;
441 end;
442
443 function ma(s : complex) : real; {magnitude}
444 begin
445   ma := sqrt(sqr(s^.r)+sqr(s^.i));
446 end;
447
448 function ph(s : complex) : real; {phase in radians, (-pi,+pi]}
449 begin
450   if s^.r > 0 then ph := arctan(s^.i/s^.r);
451   if s^.r < 0 then if s^.i >= 0 then ph := arctan(s^.i/s^.r) + pi
452     else ph := arctan(s^.i/s^.r) - pi;
453   if s^.r = 0 then begin
454     if s^.i > 0 then ph := pi/2;
455     if s^.i < 0 then ph := -pi/2;
456     if s^.i = 0 then ph := 0;
457   end; {real part 0}
458 end;
459
460 function su(s,t : complex) : complex; {sum}
461 var
462   u : complex;
463 begin
464   new(u);
465   u^.r := s^.r + t^.r;
466   u^.i := s^.i + t^.i;
467   su := u;
468 end;
469
470 function pr(s,t : complex) : complex; {product}
471 var
472   u : complex;
473 begin

```

```

474   new(u);
475   u^.r := s^.r*t^.r - s^.i*t^.i;
476   u^.i := s^.r*t^.i + s^.i*t^.r;
477   pr := u;
478 end;
479
480 function di(s,t : complex) : complex; {difference s minus t}
481 var
482   u : complex;
483 begin
484   new(u);
485   u^.r := s^.r - t^.r;
486   u^.i := s^.i - t^.i;
487   di := u;
488 end;
489
490 function qu(s,t : complex) : complex; {quotient s over t}
491 var
492   u : complex;
493 begin
494   new(u);
495   u^.r := (s^.r*t^.r + s^.i*t^.i)/(sqr(t^.r) + sqr(t^.i));
496   u^.i := (-s^.r*t^.i + s^.i*t^.r)/(sqr(t^.r) + sqr(t^.i));
497   qu := u;
498 end;
499
500 function sq(s : complex) : complex; {square}
501 var
502   u : complex;
503 begin
504   new(u);
505   u^.r := s^.r*s^.r - s^.i*s^.i;
506   u^.i := 2*s^.r*s^.i;
507   sq := u;
508 end;
509
510 function rr(s : complex) : complex;
511 {square root in the right half plane.}
512 var
513   u : complex;
514 begin
515   new(u);
516   u^.r := sqrt(ma(s))*cos(ph(s)/2);
517   u^.i := sqrt(ma(s))*sin(ph(s)/2);
518   rr := u;
519 end;
520
521 function ur(s : complex) : complex;
522 {square root in the upper half plane.}
523 var
524   u : complex;
525   theta : real;
526 begin

```



```

527   new(u);
528   if ph(s) >= 0 then theta := ph(s)/2 else theta := ph(s)/2 + pi;
529   u^.r := sqrt(ma(s))*cos(theta);
530   u^.i := sqrt(ma(s))*sin(theta);
531   ur := u;
532 end;
533
534 function lr(s : complex) : complex;
535 {the square root in the lower half plane.}
536 var
537   u : complex;
538   theta : real;
539 begin
540   new(u);
541   if ph(s) <= 0 then theta := ph(s)/2 else theta := ph(s)/2 - pi;
542   u^.r := sqrt(ma(s))*cos(theta);
543   u^.i := sqrt(ma(s))*sin(theta);
544   lr := u;
545 end;
546
547 function ex(s : complex) : complex; {exponential function}
548 var
549   u : complex;
550 begin
551   new(u);
552   u^.r := exp(s^.r)*cos(s^.i);
553   u^.i := exp(s^.r)*sin(s^.i);
554   ex := u;
555 end;
556
557 function sinh(s : complex) : complex; {hyperbolic sine}
558 var
559   u : complex;
560 begin
561   new(u);
562   u^.r := cos(s^.i)*(exp(s^.r)-exp(-s^.r))/2;
563   u^.i := sin(s^.i)*(exp(s^.r)+exp(-s^.r))/2;
564   sinh := u;
565 end;
566
567 function cosh(s : complex) : complex; {hyperbolic cosine}
568 var
569   u : complex;
570 begin
571   new(u);
572   u^.r := cos(s^.i)*(exp(s^.r)+exp(-s^.r))/2;
573   u^.i := sin(s^.i)*(exp(s^.r)-exp(-s^.r))/2;
574   cosh := u;
575 end;
576
577
578 function sine(s : complex) : complex; {sine function}
579 var

```

```

580   u : complex;
581 begin
582   new(u);
583   u^.r := sin(s^.r)*(exp(s^.i)+exp(-s^.i))/2;
584   u^.i := cos(s^.r)*(exp(s^.i)-exp(-s^.i))/2;
585   sine := u;
586 end;
587
588 function cosine(s : complex) : complex; {cosine function}
589 var
590   u : complex;
591 begin
592   new(u);
593   u^.r := cos(s^.r)*(exp(s^.i)+exp(-s^.i))/2;
594   u^.i := sin(s^.r)*(-exp(s^.i)+exp(-s^.i))/2;
595   cosine := u;
596 end;
597
598 function cm(a11,a12,a21,a22:complex):complex_matrix;
599 {makes a complex matrix}
600
601 var
602   u : complex_matrix;
603
604   function eq (s : complex) : complex; {makes an equal complex number.}
605   var
606     u : complex;
607   begin
608     new(u);
609     u^.r := s^.r; u^.i := s^.i;
610     eq := u;
611   end; {eq}
612
613 begin
614   new(u);
615   with u^ do begin
616     t11 := eq(a11); t12 := eq(a12);
617     t21 := eq(a21); t22 := eq(a22);
618   end; {with}
619   cm := u;
620 end; {cm}
621
622 function mp (a,b : complex_matrix) : complex_matrix;
623 {complex matrix product}
624 var
625   u : complex_matrix;
626 begin
627   new(u);
628   with u^ do begin
629     t11 := su(pr(a^.t11,b^.t11),pr(a^.t12,b^.t21));
630     t12 := su(pr(a^.t11,b^.t12),pr(a^.t12,b^.t22));
631     t21 := su(pr(a^.t21,b^.t11),pr(a^.t22,b^.t21));
632     t22 := su(pr(a^.t21,b^.t12),pr(a^.t22,b^.t22));

```

```

633   end; {with}
634   mp := u;
635 end; {function mp}
636
637
638 {LINE_EDITOR routines}
639
640
641 function edit(x,y : integer; test_string : line) : line;
642
643 var
644   ins: boolean; {true for insert mode on}
645   position : 1..150;
646   row, col,px,py : integer;
647
648 label quit;
649
650 procedure write_cursor_position;
651
652 begin
653   gotoxy(row,col);
654   write(' ');
655   gotoxy(row,col);
656   write(position);
657 end; {write_cursor_position}
658
659 begin
660   ins:= true; {insert mode on}
661   position:= 1;
662   row := 72; col := 4;
663   write_cursor_position;
664   gotoxy(x,y); write(test_string);
665   repeat
666     {draw cursor}
667     draw(8*(x+position-1)-8,8*y-1,8*(x+position-1)-1,8*y-1,1);
668     draw(8*(x+position-1)-8,8*y-2,8*(x+position-1)-1,8*y-2,1);
669     gotoxy(x+position-1,y);
670     read(kbd,c);
671     {erase cursor}
672     draw(8*(x+position-1)-8,8*y-1,8*(x+position-1)-1,8*y-1,0);
673     draw(8*(x+position-1)-8,8*y-2,8*(x+position-1)-1,8*y-2,0);
674     if c in structure_set then begin
675       insert(c,test_string,position);
676       position:= position+1;
677       write_cursor_position;
678       if not(ins) then delete(test_string,position,1);
679     end;
680     if c in edit_set then begin
681       case c of
682         ^d : if position <= ord(test_string[0]) then begin
683           position:= position+1;
684           write_cursor_position;
685         end;

```

```

686      ^g : begin
687          delete(test_string,position,1);
688          gotoxy(x+ord(test_string[0]),y);
689          write(' ');
690          end;
691      ^v : ins:= not(ins);
692      ^s : if position > 1 then begin
693          position:= position-1;
694          write_cursor_position;
695          end;
696      backspace : if position > 1 then begin
697          position:= position-1;
698          write_cursor_position;
699          delete(test_string,position,1);
700          gotoxy(x+ord(test_string[0]),y);
701          write(' ');
702          end;
703      end; {case}
704  end;
705  if c = 'e' then begin
706      gotoxy(x,y);
707      write('There are 3 examples. Enter 0 -> quit & 1,2 or 3 -> exmpl: ');
708      read(kbd,c);
709      case c of
710          '1' : test_string := 'i1 z0,.11 b3.6 t.233 z0,.11 f1 s0 ' +
711                'w1.7:2.7';
712          '2' : test_string := 'i1 b3.6 t.233 z.054,.27 b1 t0:2.0 ' +
713                'm s0 w3.36';
714          '3' : test_string := 'i1 b3.6 t.233 z.054,.1:.3 b1 t1.5 ' +
715                'm s1 w3.36';
716          '4' : test_string := 'i1 b1 T.68;.69 Z.042;.043,.085;.09 ' +
717                'b3.6 t.218 b1 T2.49;2.5 b1 t0:2 m ' +
718                's0 w3.36';
719          '5' : test_string := 'i1 b1 T.001;.2 b1.96 t.434 b3.6 t.231 ' +
720                'Z.045;.065,.25;.31 b1 T.6;0.8 b1 t0:2.0 ' +
721                'm s0 w3.36 o10';
722          '6' : test_string := 'i1 b1.96 t.59 b1 t0:2.3 b1.96 t.59 b1 ' +
723                't0:2.3 b3.23 t6.72 b1 t2.3 b3.6 t.238 ' +
724                'z.16,-.13 b1 t1.15 m s0 w4.6';
725          '7' : test_string := 'i1 b1.96 t.3 b1 t0:1.2 b1.96 t.3 b1 ' +
726                't0:1.2 b3.23 t6.72 b1 t2.3 z.27,.29 ' +
727                'b3.6 t.238 b1 t.86 m s0 w2.3';
728      end; {case}
729      gotoxy(x,y);
730      writeln(' ');
731      if (c = '1') or (c = '2') or (c = '3') or
732         (c = '4') or (c = '5') or (c = '6') or
733         (c = '7') then c := enter;
734      end; {if}
735      gotoxy(x,y);
736      write(test_string);
737  until c in [enter, 'q'];
738  edit:= test_string;

```

```

739 quit:
740 end; {edit}
741
742
743 {CALCULATION routines}
744
745
746 function rp : real; {finds a real parameter in structure description}
747
748   begin
749     rp := 0;
750     test_string := '';
751     pointer := pointer + 1;
752     while not((pointer > ord(structure[0])) or
753              (structure[pointer] in delimiter_set)) do begin
754       if structure[pointer] in number_set then
755         test_string := test_string + structure[pointer];
756       pointer := pointer + 1;
757     end; {while}
758     if test_string <> '' then begin
759       val(test_string,temp,error_code);
760       rp := temp;
761     end; {if}
762     if (pointer < ord(structure[0])) and (structure[pointer] = ':')
763     then begin
764       min := temp;
765       max := rp;
766       if optimization_flag=1 then
767         rp := trp[idata,1]
768       else
769         rp := min + (max-min)*x*xd/(xmax-xmin);
770     end; {if}
771   end; {rp}
772
773 function cp : complex;
774 {finds a complex parameter in structure description}
775   var
776     u : complex;
777   begin
778     new(u);
779     u^.r := rp;
780     if (structure[pointer] = ',') or (structure[pointer] = ';') then
781     begin
782       u^.i := rp;
783     end {if}
784     else u^.i := 0;
785     cp := u;
786   end; {cp}
787
788 function orp : real; {returns a real value for optimization}
789   begin
790     nparam := nparam + 1;
791     orp := vert[nparam];

```

```

792   end;
793
794 function ocp : complex; {returns a complex value for optimization}
795   begin
796     nparam := nparam + 1;
797     ocp.r := vert[nparam];
798     nparam := nparam + 1;
799     ocp.i := vert[nparam];
800   end;
801
802 function ymnj : complex;
803   begin
804     la := (1*1)/(nj*nj*a*a);
805     mla := m*m*la;
806     nla := n*n*la;
807     ymnj := qu(co((1-mla)*nj,0),lr(di(co(1,0),su(co(mla,0),co(nla,0))))));
808   end; {equivalent characteristic admittance}
809
810 procedure incident_medium;
811   begin
812     if structure[position] = 'i' then cno := cc(cp)
813     else cno := ocp;
814     if p_flag = 'TE' then cnoe := sc(cos(ao),cno);
815     if p_flag = 'TM' then cnoe := sc(1/cos(ao),cno);
816     {if G_flag='of' then
817     write('i',round(cno.r),',',round(-cno.i),' ');}
818   end; {incident_medium}
819
820 procedure neff;
821   begin
822     caj := lr(di(co(1,0),sc(sin(ao)*sin(ao),sq(qu(cno,cnj)))));
823     if p_flag = 'TE' then begin
824       cnje := pr(cnj,caj); write('');
825     end;
826     if p_flag = 'TM' then begin
827       cnje := qu(cnj,caj); write('');
828     end;
829   end; {effective refractive index}
830
831 procedure transmit_admittance;
832   begin
833     if G_flag = 'on'
834     then
835       begin
836         ld := 1/nj;
837         mla := m*m*ld*ld/(a*a);
838         nla := n*n*ld*ld/(a*a);
839         pj := sc(2*pi*lj/ld,lr(co(1-mla-nla,0)));
840         if pj.i <= -200 then pj.i := -200;
841       end
842     else
843       begin
844         pj := sc(((2*pi)/1)*lj,cnj);

```

```

845         end;
846         pjao := pr(pj,caj);
847         yjmin := su(pr(yj,cosine(pjao)),pr(pr(co(0,1),cnje),sine(pjao)));
848         yjm1d := su(cosine(pjao),qu(pr(pr(co(0,1),yj),sine(pjao)),cnje));
849         yjm1 := qu(yjmin,yjm1d);
850     end; {transmit_admittance}
851
852 procedure loss;
853     begin
854         if yjm1^.r <>0 then
855             begin
856                 sjt := yj^.r/(yjm1^.r*ma(yjm1d)*ma(yjm1d));
857                 sj := sj*sjt;
858             end
859         else
860             begin
861                 sj :=0;
862             end;
863     end; {loss}
864
865 procedure boundary;
866     begin
867         if structure[position] = 'b' then cnj := cc(cp)
868         else cnj := ocp;
869         nj := cnj^.r;
870         {if G_flag='of' then
871         write('b',cnj^.r:5:4,',',-cnj^.i:5:4,' ');}
872         if G_flag = 'on' then cnj := ymnj;
873         if scan = 'leftt' then
874             begin
875                 neff;
876                 transmit_admittance;
877                 loss;
878                 yj := yjm1;
879             end;
880     end; {boundary}
881
882 procedure thickness;
883     begin
884         if structure[position] = 't' then lj := rp
885         else lj := orp;
886         {if G_flag='of' then write('t',lj:5:4,' ');}
887         if scan = 'right' then
888             begin
889                 neff;
890                 transmit_admittance;
891                 loss;
892                 yj := yjm1;
893             end;
894     end; {thickness}
895
896 procedure admittance;
897     begin

```

```

898     if structure[position] = 'a' then aj := cp
899     else aj := ocp;
900     {if G_flag='of' then write('a',aj^.r:5:3,',',aj^.i:5:3,' ');}
901     yjm1 := su(yj,aj);
902     sj := sj*(yj^.r/yjm1^.r);
903     yj := yjm1;
904     end; {admittance}
905
906 procedure impedance;
907     begin
908     if structure[position] = 'z' then zj := cp
909     else zj := ocp;
910     {if G_flag='of' then write('z',zj^.r:5:4,',',zj^.i:5:4,' ');}
911     yjm1 := su(yj,qu(co(1,0),zj));
912     sj := sj*(yj^.r/yjm1^.r);
913     yj := yjm1;
914     end; {admittance}
915
916 function FF(t : real) : real;
917     begin
918     QQ := 1/sqrt(1-sqr(p/l))-1;
919     ccc := sqr(cos((pi/2)*(t/p)));
920     ss := 1-ccc;
921     f1 := QQ*ccc*ccc/(1+QQ*ss*ss);
922     f2 := sqr(p*ccc*(1-3*ss)/(4*1));
923     FF := f1+f2;
924     end;
925
926 function LL(tt : real) : real;
927     begin
928     LL := ln(1/sin((pi*tt)/(2*p)));
929     end;
930
931 procedure jerusalum_grid;
932     begin
933     p := rp;
934     d := rp;
935     v := rp;
936     h := rp;
937     XX := (p/l)*(LL(v)+FF(v));
938     BB := 4*d*(LL(h)+FF(h))/l;
939     yjm1 := su(yj,co(0,1/(XX-1/BB)));
940     sj := sj*(yj^.r/yjm1^.r);
941     yj := yjm1;
942     end; {jerusalum_grid}
943
944 procedure quasi_static_grid;
945     begin
946     a := rp;
947     g := rp;
948     w := rp;
949     rs := rp;
950     rd := rp;

```



```

951     cv := rp;
952     bcv := co(0, (2.0*pi*3E11/l)*cv);
953     zd := sc(1/377, su(co(rs, 0), qu(co(1, 0), su(bcv, co(1/rd, 0)))));
954     {if G_flag='of' then write('g', round(a), ' ', round(g), ' ', round(w),
955         ' ', round(rs), ' ', round(rd), ' ', round(cv), ' ');}
956     bc := co(0, (4*a/l)*((1+nj*nj)/2)*ln(1/sin((pi*g)/(2*a))));
957     bl := co(0, -(1/((g/l)*ln(1/(sin((pi*w)/(2*a)))))));
958     gj := su(bc, qu(co(1, 0), su(zd, qu(co(1, 0), bl))));
959     xyz := qu(co(1, 0), gj);
960     yjm1 := su(yj, gj);
961     sj := sj*(yj^.r/yjm1^.r);
962     yj := yjm1;
963     end; {quasi_static_grid}
964
965 procedure final_medium;
966     begin
967         if structure[position] = 'f' then
968             begin
969                 cnj := cc(cp);
970                 nj := cnj^.r;
971             end
972         else
973             begin
974                 cnj := cc(ocp);
975                 nj := cnj^.r;
976             end;
977         {if G_flag='of' then
978             write('f', round(cnj^.r), ' ', round(-cnj^.i), ' ');}
979         if G_flag = 'on' then cnj := ymnj;
980         neff;
981         yj := cnje;
982     end; {final_medium}
983
984 procedure wavelength;
985     begin
986         if structure[position] = 'w' then l := rp
987         else l := orp;
988         {if G_flag='of' then write('w', round(l), ' ');}
989     end; {wavelength}
990
991 procedure te;
992     begin
993         ao := rp*pi/180;
994         p_flag := 'TE';
995         pointer := 1;
996         cno := cc(cp);
997         {if G_flag='of' then write('s', round(ao*180/pi), ' ');}
998     end; {te}
999
1000 procedure tm;
1001     begin
1002         ao := rp*pi/180;
1003         p_flag := 'TM';

```

```

1004     pointer := 1;
1005     cno := cc(cp);
1006     {if G_flag='of' then write('p',round(ao*180/pi),' ');}
1007 end; {tm}
1008
1009 procedure mirror;
1010 begin
1011     yj := co(1E3,-1E3);
1012     {if G_flag='of' then
1013     write('m',round(yj^.r),' ',round(yj^.i),' ');}
1014 end; {mirror}
1015
1016 procedure parasitic_radiation;
1017 begin
1018     par := cp;
1019     {if G_flag='of' then
1020     write('r',round(par^.r),' ',round(par^.i),' ');}
1021 end; {parasitic_radiation}
1022
1023 procedure normalization_constant;
1024 begin
1025     normalization := rp;
1026     {if G_flag='of' then write('n',round(normalization),' ');}
1027 end;
1028
1029 procedure on_off_plot;
1030 begin
1031     on_off := trunc(rp);
1032 end;
1033
1034 procedure eisenhart_kahn_grid;
1035
1036 label skip;
1037
1038 function sinc2(x, y, z : real) : real;
1039 begin
1040     if x = 0 then sinc2 := 1
1041     else
1042     begin
1043         sinc := sin(x*pi*y/z)/(x*pi*y/z);
1044         sinc2 := sinc*sinc;
1045     end;
1046 end; {sinc square}
1047
1048 procedure com_library;
1049 begin
1050     if structure[position] in command_set then begin
1051         pointer := position;
1052         case structure[position] of
1053             'i' : incident_medium;
1054             'b' : boundary;
1055             't' : thickness;
1056             'a' : admittance;

```

```

1057         'z' : impedance;
1058         'j' : jerusalum_grid;
1059         'g' : quasi_static_grid;
1060         'f' : final_medium;
1061         'm' : mirror;
1062         's' : te;
1063         'p' : tm;
1064         'w' : wavelength;
1065         'r' : parasitic_radiation;
1066         'n' : normalization_constant;
1067         'o' : on_off_plot;
1068     end; {case}
1069 end; {if}
1070 end; {com_library}
1071
1072 procedure cal_driver;
1073
1074     begin
1075     if scan = 'leftt'
1076     then
1077         begin
1078             repeat {decode structure from right to left}
1079                 com_library;
1080                 position := position - 1;
1081                 until position <= positionstop - 1;
1082             end
1083         else
1084             begin
1085                 repeat {decode structure from left to right}
1086                     com_library;
1087                     position := position + 1;
1088                     until position >= positionstop + 1;
1089                 end;
1090             end; {calculation_driver}
1091
1092     begin {special_grid}
1093         a := rp;
1094         g := rp;
1095         w := rp;
1096         rs := rp;
1097         rd := rp;
1098         cv := rp;
1099         bcv := co(0, (2.0*pi*3E11/l)*cv);
1100         zd := sc(1/377, su(co(rs,0), qu(co(1,0), su(bcv, co(1/rd,0)))));
1101         {if G_flag='of' then write('G', ' ', round(a), ' ', round(g), ' ',
1102             round(w), ' ', round(rs), ' ',
1103             round(rd), ' ', round(cv), ' ');}
1104         {no choice, must store in real #, freemem cause mess}
1105         sjsave := sj;
1106         yjsaver := yj^.r;
1107         yjsavei := yj^.i;
1108         cnosaver := cno^.r;
1109         cnosavei := cno^.i;

```

```

1110     positionsave := position;
1111     structuresave := copy(structure,1,150);
1112     G_flag := 'on';
1113     positionG := pos('G',structure);
1114     if pos('f',structure) = 0 then positionf := pos('m',structure)
1115     else positionf := pos('f',structure);
1116     bcsun := co(0,0);
1117     blsum := co(0,0);
1118     {writeln(lst,sjsave,yjsaver,yjsavei);
1119     writeln(lst,positionsave,zd^.r,zd^.i);
1120     writeln(lst,positionG,cnosaver,cnosavei);
1121     writeln(lst,a,g,w);
1122     writeln(lst,rs,rd,cv);}
1123     for m := 0 to 85 do
1124         begin
1125             densum := co(0,0);
1126             for n := 0 to 5 do
1127                 begin
1128                     pointer := 1;
1129                     cnj := cc(cp);
1130                     nj := cnj^.r;
1131                     cnj := ymnj;
1132                     neff;
1133                     cno := cnje;
1134                     scan := 'leftt';
1135                     position := positionf;
1136                     positionstop := positionG;
1137                     cal_driver; write('');
1138                     ymnp := yj;
1139                     position := 1;
1140                     structure[1] := 'f';
1141                     scan := 'right';
1142                     cal_driver; write('');
1143                     ymnm := yj;
1144                     if (m=0) and (n>=1) then begin
1145                         bcsun := su(bcsun,sc(sinc2(n,g,a),su(ymnp,ymnm)));
1146                     end;
1147                     if m>=1 then begin
1148                         en := 2;
1149                         if n = 0 then en := 1;
1150                         densum := su(densum,sc(en*sinc2(n,g,a),su(ymnp,ymnm)));
1151                     end;
1152                 end; {begin of n}
1153             if m>=1 then begin
1154                 {writeln(lst,'m=',m,'blsum=',blsum^.r,blsum^.i);}
1155                 if m=1 then blsum := co(0,0);
1156                 {writeln(lst,'m=',m,'blsum=',blsum^.r,blsum^.i);}
1157                 blsum := su(blsum,qu(co(sinc2(m,w,a),0),densum));
1158                 {writeln(lst,'m=',m,'blsum=',blsum^.r,blsum^.i);}
1159             end;
1160             freemem(b_heap_pointer,-0);
1161         end; {begin of m}
1162     yj^.r := yjsaver;

```

```

1163     yj^.i := yjsavei;
1164     sj := sjsave;
1165     cno^.r := cnosaver;
1166     cno^.i := cnosavei;
1167     position := positionsave;
1168     positionstop := 1;
1169     G_flag := 'of';
1170     structure := structuresave;
1171     scan := 'leftt';
1172     {writeln(lst,sjsave,yj^.r,yj^.i);
1173     writeln(lst,positionsave,zd^.r,zd^.i);
1174     writeln(lst,positionG,cno^.r,cno^.i);
1175     writeln(lst,a,g,w);
1176     writeln(lst,rs,rd,cv);}
1177     bcv := co(0,5.65E-4*cv); {modify the constant if freq change}
1178     zd := sc(1/377,su(co(rs,0),qu(co(1,0),su(bcv,co(1/rd,0)))));
1179     {calculating here to avoid freemem over write}
1180     bc := sc(2*(1-g/a),bcsum);
1181     bl := qu(co(1,0),sc(2,blsum));
1182     gj := su(bc,qu(co(1,0),su(zd,qu(co(1,0),bl))));
1183     xyz := qu(co(1,0),gj);
1184     yjm1 := su(yj,gj);
1185     sj := sj*(yj^.r/yjm1^.r);
1186     yj := yjm1;
1187     end; {special_grid}
1188
1189 procedure command_library;
1190     begin
1191         if structure[position] in command_set then begin
1192             pointer := position;
1193             case structure[position] of
1194                 'i' : incident_medium;
1195                 'I' : incident_medium;
1196                 'b' : boundary;
1197                 'B' : boundary;
1198                 't' : thickness;
1199                 'T' : thickness;
1200                 'a' : admittance;
1201                 'A' : admittance;
1202                 'z' : impedance;
1203                 'Z' : impedance;
1204                 'j' : jerusalum_grid;
1205                 'g' : quasi_static_grid;
1206                 'G' : eisenhart_kahn_grid;
1207                 'f' : final_medium;
1208                 'F' : final_medium;
1209                 'm' : mirror;
1210                 's' : te;
1211                 'p' : tm;
1212                 'w' : wavelength;
1213                 'W' : wavelength;
1214                 'd' : dB_plot := true;
1215                 'r' : parasitic_radiation;

```

```

1216         'n' : normalization_constant;
1217         'o' : on_off_plot;
1218     end; {case}
1219 end; {if}
1220 end; {command_library}
1221
1222 procedure calculation_driver;
1223
1224     begin
1225     if scan = 'leftt'
1226     then
1227         begin
1228             repeat {decode structure from right to left}
1229                 command_library;
1230                 position := position - 1;
1231                 until position <= positionstop - 1;
1232             end
1233         else
1234             begin
1235                 repeat {decode structure from left to right}
1236                     command_library;
1237                     position := position + 1;
1238                     until position >= positionstop + 1;
1239                 end;
1240             end; {calculation_driver}
1241
1242
1243 {SIMULATION routines}
1244
1245
1246 procedure simulation;
1247
1248 label quit;
1249 var
1250 next_data : integer;
1251
1252 procedure find_trap;
1253 begin
1254     b_heap_pointer := heapptr;
1255     gotoxy(9,23);
1256     sj := 1;
1257     yj := co(0,0);
1258     position := ord(structure[0]);
1259     positionstop := 1;
1260     scan := 'leftt';
1261     G_flag := 'of';
1262     calculation_driver;
1263     rc := qu(di(cnoe,yj),su(cnoe,yj));
1264     reflectance:=ma(pr(su(rc,par),su(cc(rc),cc(par))))/normalization;
1265     phase := ph(rc);
1266     transmittance := (1-reflectance)*sj;
1267     absorptance := 1-transmittance-reflectance;
1268 end;

```

```

1269
1270 procedure accumulate_error;
1271 var
1272     datavalue : real;
1273 begin
1274     datavalue := trp[data_index,2];
1275     ref_err := ref_err + abs( reflectance - datavalue ) ;
1276 end;
1277
1278 {main procedure simulation}
1279 begin
1280     dB_plot := false;
1281     write_y_axis;
1282     structure := edit(14,3,structure);
1283     x := 1;
1284     b := 'a';
1285     par := co(0,0);
1286     normalization := 1.0;
1287     optimization_flag := 0;
1288     on_off := 15;
1289     ref_err := 0;
1290     data_index := 1;
1291     data_inc := round ( int( (xmax-xmin)/(xd*ndata) ) );
1292     next_data := 1;
1293     repeat {sweep variable x}
1294         if (x = next_data) then
1295             begin
1296                 if data_index <= ndata then begin
1297                     find_error_flag := true;
1298                     find_trap;
1299                     accumulate_error;
1300                     find_error_flag := false;
1301                     data_index := data_index + 1;
1302                     next_data := next_data + data_inc;
1303                 end;
1304             end;
1305             find_trap;
1306             if x>=1 then
1307                 begin
1308                     {gotoxy(4,13); write('      ')};
1309                     gotoxy(4,13); write(ref_err:6:2);
1310                     gotoxy(4,14); write('      ')};
1311                     gotoxy(4,14); write(bc^.i:6:4);
1312                     gotoxy(4,15); write('      ')};
1313                     gotoxy(4,15); write(bl^.i:6:4);
1314                     gotoxy(5,16); write('      ')};
1315                     gotoxy(5,16); write(zd^.r:8:2);
1316                     gotoxy(5,17); write('      ')};
1317                     gotoxy(5,17); write(zd^.i:8:2);
1318                     gotoxy(5,18); write('      ')};
1319                     gotoxy(5,18); write(XX:6:4);
1320                     gotoxy(3,19); write('      ')};
1321                     gotoxy(3,19); write(sj:3:2);

```

```

1322      gotoxy(3,20); write('    ');}
1323      gotoxy(3,20); write(transmittance:3:2);
1324      gotoxy(3,21); write('    ');
1325      gotoxy(3,21); write(reflectance:3:2);
1326      gotoxy(3,22); write('    ');
1327      gotoxy(3,22); write(absorptance:3:2);
1328      gotoxy(3,23); write('    ');
1329      gotoxy(3,23); write(round(phase*180/pi));
1330      end;
1331      if x=1 then
1332      begin
1333      {gotoxy(1,13); write('er=');
1334      gotoxy(1,14); write('bc=');
1335      gotoxy(1,15); write('bl=');
1336      gotoxy(1,16); write('zdr=');
1337      gotoxy(1,17); write('zdi=');
1338      gotoxy(1,18); write('X=');
1339      gotoxy(1,19); write('s=');}
1340      gotoxy(1,20); write('t=');
1341      gotoxy(1,21); write('r=');
1342      gotoxy(1,22); write('a=');
1343      gotoxy(1,23); write('p=');
1344      end;
1345      i := ((seg(heapptr^) - seg(b_heap_pointer^)) shl 4) +
1346      (ofs(heapptr^) - ofs(b_heap_pointer^));
1347      freemem(b_heap_pointer,i);
1348      heapptr := ptr(seg(b_heap_pointer^),ofs(b_heap_pointer^));
1349      freemem(b_heap_pointer,-0);
1350      {gotoxy(3,4);      what for?}
1351      delay(10);
1352      if KeyPressed then read(kbd,b);
1353      case b of
1354      'a' : begin
1355      plot_result(1);
1356      x := x + 1;
1357      end;
1358      'l' : begin
1359      plot_result(0);
1360      read(kbd,b);
1361      x := x - 1;
1362      end;
1363      'r' : begin
1364      plot_result(1);
1365      read(kbd,b);
1366      x := x + 1;
1367      end;
1368      'q' : goto quit;
1369      end; {case b}
1370      until x >= ((xmax-xmin) div xd) + 1; {end of x}
1371      write_x_axis;
1372      read(kbd,c);
1373      quit:
1374      end;

```



```

1375
1376
1377 {OPTIMIZATION routines}
1378
1379
1380 function efunc(vertice : glnp) : real;
1381 {find error between predicted and measured}
1382
1383 var
1384     i : integer;
1385     error, r, p : real;
1386     rm : complex;
1387     q : char;
1388 begin
1389 for i := 1 to ndim do begin
1390     vert[i] := vertice[i];
1391 end;
1392 error := 0;
1393 optimization_flag := 1;
1394 mark(b_heap_pointer);
1395 for idata := 1 to ndata do begin
1396 nparam := 0;
1397 gotoxy(9,23);
1398 sj := 1.0;
1399 yj := co(0,0);
1400 position := ord(structure[0]);
1401 positionstop := 1;
1402 scan := 'leftt';
1403 G_flag := 'of';
1404 calculation_driver;
1405 rc := qu(di(cnoe,yj),su(cnoe,yj));
1406 r := sqrt(trp[idata,2]);
1407 face := trp[idata,3]*(pi/180.0);
1408 rm := co( r*cos(face) , r*sin(face) );
1409 error := error + ma( di(rc,rm) );
1410 end; {do}
1411 efunc := error;
1412 release(b_heap_pointer);
1413 end;
1414
1415 procedure scalar_range;
1416 begin
1417     range_variable := cp;
1418     range[1,range_pointer] := range_variable^.r;
1419     range[2,range_pointer] := range_variable^.i;
1420     range_pointer := range_pointer + 1;
1421 end;
1422
1423 procedure complex_range;
1424 begin
1425     scalar_range;
1426     if structure[pointer] = ',' then
1427     begin

```

```

1428     scalar_range;
1429 end
1430 else
1431 begin
1432     range[1,range_pointer] := 0;
1433     range[2,range_pointer] := 0;
1434     range_pointer := range_pointer + 1;
1435 end;
1436 end; {of complex_range}
1437
1438
1439 procedure initialize_optimization;
1440
1441 var
1442     i, j : integer;
1443     temp : real;
1444
1445 begin
1446
1447 {rightmost parameter goes into col 1, etc. & row 1 takes minimum}
1448 range_pointer := 1;
1449 position := ord(structure[0]);
1450 positionstop := 1;
1451 scan := 'leftt';
1452 repeat
1453     if structure[position] in command_set then begin
1454         pointer := position;
1455         case structure[position] of
1456             'I' : complex_range;
1457             'B' : complex_range;
1458             'T' : scalar_range;
1459             'A' : complex_range;
1460             'Z' : complex_range;
1461             'F' : complex_range;
1462             'W' : scalar_range;
1463         end; {of case}
1464     end; {of if}
1465     position := position - 1;
1466 until position <= positionstop - 1;
1467 ftol := 0.0001;
1468 iter := 50;
1469 ndim := range_pointer - 1;
1470 mvertices := ndim + 1;
1471
1472 for i := 1 to mvertices do begin
1473     for j := 1 to ndim do begin
1474         if i = j then poly[i,j] := range[2,j]
1475         else poly[i,j] := range[1,j];
1476     end;
1477 end;
1478
1479 gotoxy(1,6); write('
1480 writeln('intializing ... '); gotoxy(1,6);

```

```
1481 for i := 1 to mvertices do
1482 begin
1483   for j := 1 to ndim do
1484     begin
1485       vert[j] := poly[i,j];
1486     end;
1487   y[i] := efunc(vert);
1488 end;
1489 gotoxy(1,7); writeln;
1490 for i := 1 to mvertices do
1491 begin
1492   for j := 1 to ndim do
1493     begin
1494       temp := poly[i,j];
1495       write(temp:6:4,' ');
1496     end;
1497     temp := y[i];
1498     write(temp:6:4,' ');
1499     writeln;
1500 end;
1501 writeln('iteration cycle #0 ');
1502 writeln;
1503 writeln;
1504 end; {of optimization}
1505
1506 procedure amoeba;
1507
1508 label
1509   pau, loop;
1510
1511 const
1512   alpha = 1.0; beta = 0.5; gamma = 2.0; itmax = 500;
1513
1514 var
1515   mpts, i, j, inhi, ilo, ihi : integer;
1516   ypr, ypr, rtol : real;
1517   pr, prr, pbar : glnp;
1518   q : char;
1519   temp : real;
1520 begin
1521   mpts := ndim + 1;
1522   iter := 0;
1523 loop:
1524   ilo := 1;
1525   if ( y[1] > y[2] ) then
1526     begin
1527       ihi := 1;
1528       inhi := 2;
1529     end
1530   else begin
1531     ihi := 2;
1532     inhi := 1;
1533   end;
```

```

1534   for i := 1 to mpts do
1535   begin
1536       if ( y[i] < y[i]lo ) then ilo := i;
1537       if ( y[i] > y[i]hi ) then begin
1538           inhi := ihi;
1539           ihi := i;
1540       end
1541       else begin
1542           if ( y[i] > y[inhi] ) and ( i <> ihi ) then inhi := i;
1543       end;
1544   end;
1545   rtol := 2.0 * abs( y[i]hi - y[i]lo ) / ( abs(y[i]hi) + abs(y[i]lo) );
1546   if ( rtol < ftol ) then goto pau;
1547   if ( iter = itmax ) then
1548   begin
1549       writeln('pause in AMOEBA - too many iterations');
1550       readln(q);
1551   end;
1552   iter := iter + 1;
1553   for j := 1 to ndim do
1554   begin
1555       pbar[j] := 0.0;
1556   end;
1557   for j := 1 to ndim do
1558   begin
1559       begin
1560           for i := 1 to mpts do
1561           begin
1562               if ( i <> ihi ) then pbar[j] := pbar[j] + poly[i,j];
1563           end;
1564       end;
1565   end;
1566   for j := 1 to ndim do
1567   begin
1568       pbar[j] := pbar[j]/ndim;
1569       pr[j] := pbar[j] + alpha * ( pbar[j] - poly[ihi,j] );
1570   end;
1571   ypr := efunc(pr);
1572   if ( ypr <= y[i]lo ) then
1573   begin
1574       {write('reflection');}
1575       for j := 1 to ndim do
1576       begin
1577           prr[j] := pbar[j] + gamma * ( pr[j] - pbar[j] );
1578       end;
1579       yprr := efunc(prr);
1580       if ( yprr < y[i]lo ) then
1581       begin
1582           {writeln(' & expansion');}
1583           for j := 1 to ndim do
1584           begin
1585               poly[ihi,j] := prr[j];
1586           end;

```

```

1587     y[ihi] := ypr;
1588 end
1589 else
1590 begin
1591     {writeln(' only');}
1592     for j := 1 to ndim do
1593     begin
1594         poly[ihi,j] := pr[j];
1595     end;
1596     y[ihi] := ypr;
1597 end;
1598 end
1599 else
1600 begin
1601     if ( ypr >= y[inhi] ) then
1602     begin
1603         if ( ypr < y[ihi] ) then
1604         begin
1605             {writeln('line contraction');}
1606             for j := 1 to ndim do
1607             begin
1608                 poly[ihi,j] := pr[j];
1609             end;
1610             y[ihi] := ypr;
1611             for j := 1 to ndim do
1612             begin
1613                 prr[j] := pbar[j] + beta * ( poly[ihi,j] - pbar[j] );
1614             end;
1615             ypr := efunc(prr);
1616             if ( ypr < y[ihi] ) then
1617             begin
1618                 for j := 1 to ndim do
1619                 begin
1620                     poly[ihi,j] := prr[j];
1621                 end;
1622                 y[ihi] := ypr;
1623             end;
1624         end
1625     else
1626     begin
1627         {writeln('surface contraction');}
1628         for i := 1 to mpts do
1629         begin
1630             if ( i <> ilo ) then
1631             begin
1632                 for j := 1 to ndim do
1633                 begin
1634                     pr[j] := ( poly[i,j] + poly[ilo,j] ) / 2;
1635                     poly[i,j] := pr[j];
1636                 end;
1637                 y[i] := efunc(pr);
1638             end;
1639         end;

```

```

1640         end;
1641     end
1642     else
1643     begin
1644         {writeln('reflection with mild success only');}
1645         for j := 1 to ndim do
1646             begin
1647                 poly[ihi,j] := pr[j];
1648             end;
1649             y[ihi] := ypr;
1650         end;
1651     end;
1652     writeln;
1653     for i := 1 to mpts do
1654     begin
1655         for j := 1 to ndim do
1656             begin
1657                 temp := poly[i,j];
1658                 write(temp:6:4,' ');
1659             end;
1660             temp := y[i];
1661             write(temp:6:4,' ');
1662             writeln;
1663         end;
1664         writeln('iteration cycle #',iter,' ');
1665         writeln;
1666         goto loop;
1667     pau:
1668     end;
1669
1670     procedure optimization;
1671     begin
1672         dB_plot := false;
1673         structure := edit(14,3,structure);
1674         initialize_optimization;
1675         amoeba;
1676         writeln; writeln;
1677         writeln('Optimization finish. ');
1678         writeln('Please record optimized values. ');
1679         writeln('Press Q to Quit and R to continue. ');
1680     end;
1681
1682
1683     {DATA_BASE routines}
1684
1685
1686     procedure graph_units;
1687
1688     begin
1689         gotoxy(1,2); write('
1690         write('
1691         gotoxy(1,2); write('enter ylmi, ylmx, yrmi, yrmx : ');
1692         readln(ylmi, ylmx, yrmi, yrmx);

```

```

1693   write('select curve : (0 = refl), (1 = phase) and (2 = both) ');
1694   readln(curve_select);
1695   ylrage := ylmx - ylmi;      ylscale := (ymax-ymin)/ylrange;
1696   yrrange := yrmx - yrmi;      yrscale := (ymax-ymin)/yrange;
1697 end;
1698
1699 procedure database;
1700
1701 var
1702   i : integer;
1703
1704 begin
1705   gotoxy(31,2); write(' '); read(filename);
1706   gotoxy(12,3); writeln('inputting data from ',filename);
1707   assign(data_transfer,filename);
1708   ndata := 0;
1709   reset(data_transfer);
1710   while not eof(data_transfer) do begin
1711     ndata := ndata + 1;
1712     readln(data_transfer,trp[ndata,1],trp[ndata,2],trp[ndata,3]);
1713   end;
1714   close(data_transfer);
1715   xmi := trp[1,1]; xmx := trp[ndata,1];
1716   for i := 2 to ndata do begin
1717     if trp[i,1] > xmx then xmx := trp[i,1];
1718     if trp[i,1] < xmi then xmi := trp[i,1];
1719   end;
1720   graphwindow (xmin,ymin,xmax,ymax);
1721   xscale := (xmax - xmin)/(xmx - xmi);
1722   write_x_coordinates (xmin,ymax+14,(xmax-xmin) div 5,
1723                       xmi,(xmx-xmi)/5,xmx,4,1);
1724   write_y_coordinates (xmin-20,ymin+7,(ymax-ymin) div 5,
1725                       ylmx,(ylmi-ylmx)/5,ylmi,3,1);
1726   write_y_coordinates (xmax+20,ymin+7,(ymax-ymin) div 4,
1727                       yrmx,(yrmi-yrmx)/4,yrmi,4,0);
1728   for i := 1 to ndata do begin
1729     gotoxy(47,3); writeln(i,' ',trp[i,1]:4:2,' ',trp[i,2]:4:2,' ',
1730     trp[i,3]:4:2);
1731     trp1 := trp[i,1] - xmi;
1732     {plot reflectance}
1733     if ( curve_select = 0 ) or ( curve_select = 2 ) then begin
1734       trp2 := ylmx - trp[i,2];
1735       plot( round(trp1*xscale) - 1 , round(trp2*ylscale) , 1 );
1736       plot( round(trp1*xscale) , round(trp2*ylscale) , 1 );
1737       plot( round(trp1*xscale) + 1 , round(trp2*ylscale) , 1 );
1738       plot( round(trp1*xscale) , round(trp2*ylscale) + 1 , 1 );
1739       plot( round(trp1*xscale) , round(trp2*ylscale) - 1 , 1 );
1740     end;
1741     {plot phase}
1742     if ( curve_select = 1 ) or ( curve_select = 2 ) then begin
1743       trp3 := yrmx - trp[i,3];
1744       plot( round(trp1*xscale) - 1 , round(trp3*yrscale) + 1 , 1 );
1745       plot( round(trp1*xscale) , round(trp3*yrscale) , 1 );

```

```

1746         plot( round(trp1*xscale) + 1 , round(trp3*yrscale) - 1 , 1 );
1747         plot( round(trp1*xscale) + 1 , round(trp3*yrscale) + 1 , 1 );
1748         plot( round(trp1*xscale) - 1 , round(trp3*yrscale) - 1 , 1 );
1749     end;
1750     read(kbd,c);
1751     end;
1752     read(kbd,c);
1753 end;
1754
1755
1756 {MAIN PROGRAM}
1757
1758
1759 begin
1760 ylmi := 0.0;   yrmi := -180.0;
1761 ylmx := 1.0;   yrmx := 180.0;
1762 ylrage := ylmx - ylmi;   ylscale := (ymax-ymin)/ylrange;
1763 yrorage := yrmx - yrmi;   yrscale := (ymax-ymin)/yrorage;
1764 select := 'm';
1765 ndata := 1;
1766 curve_select := 2;
1767 bc := co(0.0,0.0);
1768 bl := co(0.0,0.0);
1769 zd := co(0.0,0.0);
1770 XX := 0.0;
1771 c := 'q';
1772 main :
1773     repeat
1774     set_up;
1775     if select = 'q' then goto pau;
1776     case select of
1777         'd' : database;
1778         's' : simulation;
1779         'o' : optimization;
1780         'g' : graph_units;
1781         'q' : goto pau;
1782     end;
1783     if (c = 'q') or (c = 'g') or (c = 'o') then select := 'm';
1784     if (c = 'd') or (c = 's') then select := c;
1785     until c = 'q';
1786     goto main;
1787 pau:
1788     textmode(bw80);
1789 end.

```


Appendix C

Computer Program Listing for Reflection Measurement

```

1 program measure_reflection_coefficient;
2
3 {   The name of this program is reflmeas. It was developed to
4     measure the reflection coefficient of the diode-grid in conjunction
5     with the small aperture reflectometer. An IBM-PC is used
6     to control the Data Translation A/D and D/A converter and
7     the Hp3478A multimeter, whose address should be 23, for
8     voltage measurements. When the program is run, it prompts the
9     user for vertical plotting units, scanning distance in [mm],
10    operating frequency in [GHz], etc. The measured reflection
11    coefficients are plotted in real time. At the end of the
12    measurement, the data can be saved in a file for later editing
13    or processing.   }
14
15
16
17 type
18   complex = ^complex_record;
19   complex_record = record
20     r,i : real;
21   end;
22   complex_matrix = ^complex_matrix_record;
23   complex_matrix_record = record
24     t11,t12,t21,t22 : complex;
25   end;
26   sdesc = record
27     len : byte;
28     addr : integer;
29   end;
30   datatype = array[1..100] of real;
31
32 const
33   xmin = 190; xmax = 550;
34   ymin = 43; ymax = 163;
35
36 label
37   pau, restart, next_data;
38
39 var
40   vout, vin, gain : real;
41   channel, dac : integer;
42   peak, valley, i1, i2, i3, i4, pin : real;
43   amp, face, facex, facey : real;
44   ylstart, yrstart, ylstop, yrstop, xstart, xstop : real;
45   q, c, select : char;
46   dB_plot : boolean;
47   on_off : byte;
48   x, xcor, ycor : integer;
49   norm, min, max : real;

```

```

50  reflectance, transmittance, phase, absorptance : real;
51  ndata, j : integer;
52  lap : array [1..100 , 1..3] of real;
53  {1 = len. , 2 = amp. ,and 3 = phase}
54  data_transfer : text;
55  filename : string[15];
56  port_num, bit_num, bit_val, port_0_byte, port_1_byte : byte;
57  n_step, over_drive_n_step : integer;
58  n_step_per_um, freq, inc_90, lamda : real;
59  lmirror, lprevious, lphase, lstart, lstep, lstop : real;
60  i, mode, p_step, m_step, total_step : integer;
61  cmd, recv, set_up_store : string[200];
62  cmddesc, recvdesc : sdesc;
63  my_address, system_controller, len, status, code : integer;
64  quote : char;
65  offset, pnorm, initial_sep : real;
66
67
68  function co (s,t : real) : complex; {makes a complex number}
69  var
70    u : complex;
71  begin
72    new(u);
73    u^.r := s;
74    u^.i := t;
75    co := u;
76  end;
77
78  function ph(s : complex) : real; {phase in radians, (-pi,+pi]}
79  begin
80    if s^.r > 0 then ph := arctan(s^.i/s^.r);
81    if s^.r < 0 then if s^.i >= 0 then ph := arctan(s^.i/s^.r) + pi
82      else ph := arctan(s^.i/s^.r) - pi;
83    if s^.r = 0 then begin
84      if s^.i > 0 then ph := pi/2;
85      if s^.i < 0 then ph := -pi/2;
86      if s^.i = 0 then ph := 0;
87    end; {real part 0}
88  end; {of phase}
89
90  procedure draw_box (x1,y1,x2,y2,color : integer);
91  begin
92    draw (x1,y1,x1,y2,1);
93    draw (x1,y2,x2,y2,1);
94    draw (x2,y2,x2,y1,1);
95    draw (x2,y1,x1,y1,1);
96  end;
97
98  procedure draw_x_ticks(x1,y1,x_increment,x2 : integer);
99  begin
100  while (x1<=x2) do begin
101    draw (x1,(y1+1),x1,(y1-2),1);
102    x1 := x1 + x_increment;

```

```

103   end; {while}
104 end; {draw_x_ticks}
105
106 procedure draw_y_ticks(x1,y1,y_increment,y2 : integer);
107 begin
108   while (y1<=y2) do begin
109     draw ((x1+2),y1,(x1-4),y1,1);
110     y1 := y1 + y_increment;
111   end; {while}
112 end; {draw_y_ticks}
113
114 procedure draw_graph(x1,y1,x2,y2,
115   x_increment, y_increment_left, y_increment_right :integer);
116 begin
117   draw_box(x1,y1-2,x2,y2+2,1);
118   draw_x_ticks (x1,y1,x_increment,x2);
119   draw_x_ticks (x1,y2,x_increment,x2);
120   draw_y_ticks (x1,y1,y_increment_left,y2);
121   draw_y_ticks (x2,y1,y_increment_right,y2);
122 end; {procedure draw_graph}
123
124 procedure write_x_coordinates(x1,y1,x_increment :integer;
125   x1c,xc_increment,x2c : real; field,fix :integer);
126 begin
127   repeat
128     gotoxy((x1 div 8), (y1 div 8));
129     write (x1c:field:fix);
130     x1c := x1c + xc_increment;
131     x1 := x1 + x_increment;
132   until x1c > x2c;
133 end; {write_x_coordinates}
134
135 procedure write_y_coordinates(x1,y1,y_increment :integer;
136   y1c,yc_increment,y2c : real; field, fix : integer);
137 begin
138   repeat
139     gotoxy((x1 div 8), (y1 div 8));
140     write (y1c:field:fix);
141     y1c := y1c + yc_increment;
142     y1 := y1 + y_increment;
143   until y1c < y2c;
144 end; {write_y_coordinates}
145
146 function dB(x : real) : real; {give the dB difference}
147 begin
148   dB := 10*ln(x)/ln(10);
149 end; {dB}
150
151 procedure set_up;
152 begin
153   hires; hirescolor(15);
154   draw_graph(xmin,ymin,xmax,ymax,(xmax-xmin)div 5,
155     (ymax-ymin)div 5, (ymax-ymin)div 4);

```

```

156     gotoxy(1,7); write ('Pha.  xxxxx');
157     gotoxy(1,8); write('Refl.  +++++ ');
158 end; {set_up}
159
160 procedure write_y_axis;
161 begin
162     write_y_coordinates(xmax+20,ymin+7,(ymax-ymin) div 4,
163         yrstop,(yrstart-yrstop)/4,yrstart,4,0);
164     if dB_plot then
165         write_y_coordinates(xmin-20,ymin+7,(ymax-ymin) div 5,
166             0,-10,-50,3,0)
167     else
168         write_y_coordinates(xmin-40,ymin+7,(ymax-ymin) div 5,
169             ylstop,(ylstart-ylstop)/5,ylstart,3,1);
170 end; {write_y_axis}
171
172 procedure write_x_axis;
173 begin
174     write_x_coordinates (xmin,ymax+14,(xmax-xmin) div 5,
175         xstart,(xstop-xstart)/5,xstop,4,1);
176 end; {write_x_axis}
177
178 procedure initialize(var addr, level : integer);
179     external 't488init';
180
181 procedure transmit(var s : sdesc; var status : integer);
182     external 't488xmit';
183
184 procedure receive(var r : sdesc; var len, status : integer);
185     external 't488recv';
186
187 procedure hp_vm_initialize;
188 {Initialization procedure for Hp voltmeter}
189 begin
190     quote := chr(39);
191     cmddesc.addr := ofs(cmd) + 1;
192     recvdesc.addr := ofs(recv) + 1;
193
194     my_address := 21;
195     system_controller := 0;
196     initialize(my_address, system_controller);
197
198     cmd := 'LISTEN 23' ;
199     cmddesc.len := length(cmd);
200     transmit(cmddesc, status);
201
202     cmd := 'DATA ' + quote + 'Hi T1' + quote + '13 10' ;
203     cmddesc.len := length(cmd);
204     transmit(cmddesc, status);
205
206     cmd := 'TALK 23' ;
207     cmddesc.len := length(cmd);
208     transmit(cmddesc, status);

```

```

209 end;
210
211 procedure hp_vm(var voltage : real);
212
213 begin
214     recv := '
215     recvdesc.len := length(recv);
216     receive(recvdesc,len,status);
217
218     recv := '
219     recvdesc.len := length(recv);
220     receive(recvdesc,len,status);
221     if recv[1] = '+' then delete(recv,1,1);
222     val(recv,voltage,code);
223     voltage := ( voltage - offset ) * 1e3 ;
224
225 end; {of hp_vm}
226
227 procedure wait_for_DIF;
228 {check data register of Data Translation board before writing to it}
229
230 var
231     ready,int : byte;
232     timeout : integer;
233
234 begin
235     ready := 0;
236     timeout := $8000;
237     while ready = 0 do
238         begin
239             int := Port[$2ED] and 2;
240             if int = 0 then ready := 1;
241             delay(1);
242             timeout := timeout+1;
243             if timeout = 32767 then
244                 begin
245                     writeln('Device time-out on DT-2801 board,');
246                     writeln('during wait for DIF. ');
247                     writeln;
248                     ready := 1;
249                 end;{if}
250             end;{while}
251 end;{procedure wait_for_DIF}
252
253
254 procedure wait_for_DOR;
255 {check if there is new data in Data Translation board}
256
257 var
258     ready,int : byte;
259     timeout : integer;
260
261 begin

```

```

262     ready := 0;
263     timeout := $8000;
264     while ready = 0 do
265         begin
266             int := Port[$2ED] and 1;
267             if int = 1 then ready := 1;
268             delay(1);
269             timeout := timeout+1;
270             if timeout = 32767 then
271                 begin
272                     writeln('Device time-out on DT-2801 board,');
273                     writeln('during wait for DOR. ');
274                     writeln;
275                     ready := 1;
276                 end;{if}
277             end;{while}
278 end;{procedure wait_for_DOR}
279
280
281 procedure wait_for_ready;
282 {check if ready to execute another command in the
283 Data Tranlation board}
284
285 var
286     ready,int : byte;
287     timeout : integer;
288
289 begin
290     ready := 0;
291     timeout := $8000;
292     while ready = 0 do
293         begin
294             int := Port[$2ED] and 4;
295             if int = 4 then ready := 1;
296             delay(1);
297             timeout := timeout+1;
298             if timeout = 32767 then
299                 begin
300                     writeln('Device time-out on DT-2801 board,');
301                     writeln('during wait for Ready. ');
302                     writeln;
303                     ready := 1;
304                 end;{if}
305             end;{while}
306 end;{procedure wait_for_ready}
307
308
309 procedure write_d_to_a_immediate(vout: real; dac: integer);
310
311 var
312     dat_low_byte, dat_high_byte, ps : byte;
313     code : integer;
314

```

```

315 begin
316     wait_for_ready;
317     Port[$2ED] := $08; {write voltage command}
318     code := round((vout+10)*4095/20.0);
319     if code > 4095 then code := 4095;
320     if code < 0 then code := 0;
321     dat_high_byte := code and $0F00 shr 8;
322     dat_low_byte := code and $FF;
323     if dac = 0 then ps := $00;
324     if dac = 1 then ps := $01;
325     wait_for_DIF;
326     wait_for_DIF;
327     Port[$2EC] := ps;
328     wait_for_DIF;
329     Port[$2EC] := dat_low_byte;
330     wait_for_DIF;
331     Port[$2EC] := dat_high_byte;
332 end;{procedure write_d_to_a_immediate}
333
334
335 procedure read_a_to_d_immediate(var vin, gain : real; channel : integer);
336 {Read data form the Data Translation board}
337 var
338     dat_low_byte, dat_high_byte, G, chan : byte;
339     code : integer;
340     temp1 : real;
341     i : integer;
342
343 begin
344     temp1 := 0;
345     {for i := 1 to 10 do begin}
346     wait_for_ready;
347     port[$2ED] := $0C; {A/D command}
348     wait_for_DIF;
349     if gain = 1.0 then G := $00;
350     if gain = 10.0 then G := $01;
351     if gain = 100.0 then G := $02;
352     if gain = 500.0 then G := $03;
353     port[$2EC] := G; {Gain select}
354     wait_for_DIF;
355     case channel of
356     0 : chan := $00;
357     1 : chan := $01;
358     2 : chan := $02;
359     3 : chan := $03;
360     4 : chan := $04;
361     5 : chan := $05;
362     6 : chan := $06;
363     7 : chan := $07;
364     end;
365     port[$2EC] := chan; {Channel select}
366     wait_for_DOR;
367     dat_low_byte := port[$2EC];

```

```

368     wait_for_DOR;
369     dat_high_byte := port[$2EC];
370     code := 256 * dat_high_byte + dat_low_byte;
371     temp1 := temp1 + 20.0 * code/65536.0;
372     vin := temp1/gain;
373 end;{procedure read_a_to_d_immediate}
374
375
376 procedure clear_DT_2801;
377 {Clear procedure for Data Translation board}
378 var
379     temp : byte;
380
381 begin
382     Port[$2ED] := $FF;
383     temp := Port[$2EC];
384     wait_for_ready;
385     Port[$2ED] := 0;
386     wait_for_DOR;
387     temp := Port[$2EC];
388 end;{procedure clear_DT_2801}
389
390 procedure wait_for_keypress;
391
392 begin
393     if mode = 0 then read(kbd,q)
394     else delay(2000);
395 end;{procedure wait_for_keypress}
396
397 procedure write_digital_out(port_number, bit_number, on_off : byte);
398 {Write a command to motor driver}
399
400 var
401     mask : byte;
402 begin
403     wait_for_ready;
404     port[$2ED] := $05;
405     wait_for_DIF;
406     port[$2EC] := port_number;
407     wait_for_ready;
408     port[$2ED] := $07;
409     wait_for_DIF;
410     port[$2EC] := port_number;
411     wait_for_DIF;
412     mask := 1 shl bit_number;
413     if port_number = 1 then
414         begin
415             if on_off = 1 then port_1_byte := mask or port_1_byte
416             else port_1_byte := (not mask) and port_1_byte;
417             port[$2EC] := port_1_byte;
418         end
419     else
420         begin

```



```

421         if on_off =1 then port_0_byte := mask or port_0_byte
422         else port_0_byte := (not mask) and port_0_byte;
423         port[$2EC] := port_0_byte;
424     end;
425 end;
426
427 procedure enable_motor(port_number, on_off : byte);
428 {Enable motor: 1 for on }
429
430 begin
431     if on_off = 1 then write_digital_out(port_number, 2, 1)
432     else write_digital_out(port_number, 2, 0);
433 end;
434
435 procedure direction(port_number, for_bak : byte);
436 {Motor direction: 1 for forward }
437
438 begin
439     if for_bak = 1 then write_digital_out(port_number, 5, 1)
440     else write_digital_out(port_number, 5, 0);
441 end;
442
443 procedure scan(port_number : byte ; n_step : integer);
444 {Move motor: 1 for move}
445 var
446     i : integer;
447
448 begin
449     if n_step > 0 then direction(port_number,1)
450     else direction(port_number,0);
451     for i := 1 to abs(n_step) do begin
452         write_digital_out(port_number, 7, 0);
453         write_digital_out(port_number, 7, 1);
454         delay (20);
455         write_digital_out(port_number, 7, 0);
456     end;
457 end; {of scan}
458
459 begin {main program}
460     hp_vm_initialize;
461     dB_plot := false;
462     set_up;
463     gotoxy(1,1); write('enter ylstart, ylstop, yrstart and yrstop : ');
464     readln(ylstart,ylstop,yrstart,yrstop);
465     write_y_axis;
466     gotoxy(1,2); write('enter xstart and xstop : ');
467     readln(xstart,xstop);
468     gotoxy(1,3); write('enter mode ( manual = 0 & auto = 1 ) : ');
469     readln(mode);
470     write_x_axis;
471
472     port_0_byte := 0;
473     port_1_byte := 0;

```

```

474 clear_DT_2801;
475 enable_motor(0,1);
476 enable_motor(1,1);
477 n_step_per_um := 1.01; {Motor calibration constant}
478 {Last calibrated on 10/86}
479 offset := 0.10e-3;
480 over_drive_n_step := 25;
481 ndata := 1;
482 total_step := 0;
483 q := 'z';
484
485 gotoxy(1,3); writeln(' ');
486 gotoxy(1,3); write('enter frequency in GHz : ');
487 readln(freq);
488 lamda := 300.0/freq; {convert into millimeter}
489 inc_90 := lamda/8.0;
490 p_step := round( inc_90 * 1000.0 * n_step_per_um );
491
492 gotoxy(60,3); write('STATE :');
493 if mode = 1 then begin
494     gotoxy(1,4); write('enter lstart, lstep, lstop : ');
495     readln(lstart, lstep, lstop);
496     lmirror := lstart;
497     lprevious := lmirror;
498 end
499 else begin
500     gotoxy(70,3); write('L ');
501     gotoxy(1,14); write(' L > ');
502     gotoxy(7,14); readln(lmirror);
503     lprevious := xstart;
504 end;
505 hp_vm_initialize;
506 {To eliminate back-lash in the translation}
507 scan(0,-over_drive_n_step);
508 scan(0,over_drive_n_step);
509 scan(0,-over_drive_n_step);
510 scan(0,over_drive_n_step);
511 scan(1,-over_drive_n_step);
512 scan(1,over_drive_n_step);
513 scan(1,-over_drive_n_step);
514 scan(1,over_drive_n_step);
515 gotoxy(1,4);
516 writeln(' ');
517 gotoxy(1,4); write('enter initial seperation : ');
518 readln(initial_sep);
519 writeln(lst,'initial seperation = ',initial_sep);
520 writeln(lst,' ');
521 write(lst,' L Pin i1 ');
522 writeln(lst,'i2 i3 i4 Phase Ampc');
523 writeln(lst,' ');
524 next_data:
525 restart:
526 if ( mode = 0 ) and ( ndata <> 1 ) then begin

```

```

527     gotoxy(70,3); write('L ');
528     gotoxy(1,14); write(' L > ');
529     gotoxy(7,14); readln(lmirror);
530 end;
531 m_step := round( (lmirror-lprevious) * 1000.0 * n_step_per_um );
532 scan(0,m_step);
533 gotoxy(70,3); write('L ');
534 gotoxy(1,14); write(' L = ');
535 if mode = 1 then write(lmirror:6:3);
536
537 gain := 1.0;
538 channel := 0;
539 gotoxy(70,3); write('Pin');
540 gotoxy(1,15); write('Pin = ');
541 wait_for_keypress;
542 {hp_vm(pin);}
543 read_a_to_d_immediate(pin, gain, channel);
544 gotoxy(7,15); write(pin:7:6);
545 gotoxy(70,3); write('i1 ');
546
547 gotoxy(1,16); write(' i1 = ');
548 wait_for_keypress;
549 hp_vm(i1);
550 read_a_to_d_immediate(pnorm, gain, channel);
551 i1 := i1/pnorm;
552 gotoxy(7,16); write(i1:7:6);
553
554 gotoxy(70,3); write('i2 ');
555 scan(1,p_step);
556 gotoxy(1,17); write(' i2 = ');
557 wait_for_keypress;
558 hp_vm(i2);
559 read_a_to_d_immediate(pnorm, gain, channel);
560 i2 := i2/pnorm;
561 gotoxy(7,17); write(i2:7:6);
562
563 gotoxy(70,3); write('i3 ');
564 scan(1,p_step);
565 gotoxy(1,18); write(' i3 = ');
566 wait_for_keypress;
567 hp_vm(i3);
568 read_a_to_d_immediate(pnorm, gain, channel);
569 i3 := i3/pnorm;
570 gotoxy(7,18); write(i3:7:6);
571
572 gotoxy(70,3); write('i4 ');
573 scan(1,p_step);
574 gotoxy(1,19); write(' i4 = ');
575 wait_for_keypress;
576 if q='r' then goto restart;
577 hp_vm(i4);
578 read_a_to_d_immediate(pnorm, gain, channel);
579 i4 := i4/pnorm;

```

```

580 gotoxy(7,19); write(i4:7:6);
581
582 facey := i4-i2;
583 facex := i1-i3;
584 face := -ph(co(facex,facey))*(180/pi);
585 gotoxy(1,20); write('Pha = ');
586 gotoxy(7,20); write(face:4:1);
587 xcor := round((lmirror-xstart)*(xmax-xmin)/(xstop-xstart));
588 ycor := round((yrstop-face)*(ymax-ymin)/(yrstop-yrstart));
589 graphwindow (xmin,ymin,xmax,ymax);
590 plot(xcor-1,ycor+1,1);
591 plot(xcor+1,ycor+1,1);
592 plot(xcor,ycor,1);
593 plot(xcor+1,ycor-1,1);
594 plot(xcor-1,ycor-1,1);
595
596 amp := sqrt(sqr(i4-i2)+sqr(i1-i3));
597 gotoxy(1,21); write('Amp = ');
598 gotoxy(7,21); write(amp:5:3);
599 xcor := round((lmirror-xstart)*(xmax-xmin)/(xstop-xstart));
600 ycor := round((ylstop-amp)*(ymax-ymin)/(ylstop-ylstart));
601 graphwindow(xmin,ymin,xmax,ymax);
602 plot(xcor-1,ycor,1);
603 plot(xcor+1,ycor,1);
604 plot(xcor,ycor-1,1);
605 plot(xcor,ycor+1,1);
606
607 write(Lst,lmirror:6:4,' ');
608 write(Lst,pin:7:6,' ');
609 write(Lst,i1:7:6,' ');
610 write(Lst,i2:7:6,' ');
611 write(Lst,i3:7:6,' ');
612 write(Lst,i4:7:6,' ');
613 write(Lst,face:4:1,' ');
614 write(Lst,amp:6:3);
615 writeln(Lst,' ');
616
617 lap[ndata,1] := lmirror;
618 lap[ndata,2] := amp;
619 lap[ndata,3] := face;
620 total_step := total_step + m_step;
621 if ( mode = 1 ) and ( lmirror>lstop) or (lmirror=lstop) )
622 then q := 's';
623 if q='s' then begin
624 gotoxy(1,24); write('enter impedance filename : ');
625 readln(filename);
626 assign(data_transfer,filename);
627 rewrite(data_transfer);
628 for j := 1 to ndata do
629 begin
630 writeln(data_transfer,lap[j,1],', ',lap[j,2],', ',lap[j,3]);
631 end;
632 close(data_transfer);

```

```
633         writeln(lst,' ');
634         writeln(lst,'stored in filename : ',filename);
635         goto pau;
636     end; {of begin}
637
638     if q = 'q' then goto pau;
639     gotoxy(70,3); write('Rwd');
640     scan(1,-3*p_step);
641     scan(1,-over_drive_n_step);
642     scan(1,over_drive_n_step);
643     lprevious := lmirror;
644     if mode = 1 then lmirror := lmirror + lstep;
645     ndata := ndata + 1;
646     goto next_data;
647 pau:
648     {Return to original positon}
649     scan(0,-total_step);
650     scan(0,-over_drive_n_step);
651     scan(0,over_drive_n_step);
652     scan(1,-3*p_step);
653     scan(1,-over_drive_n_step);
654     scan(1,over_drive_n_step);
655     enable_motor(0,0);
656     enable_motor(1,0);
657     textmode(bw80);
658     gotoxy(1,1);
659 end.
```

Appendix D

Computer Program Listing
for Diode Parameter Measurement

```

1 program iv_measurement_of_Schottky_diode;
2
3
4 {   The name of this program is ivdiode. It was developed for
5     measuring the circuit parameters of a Schottky diode.
6     The program is written in Turbo Pascal. An IBM-PC is used
7     to control the Hp4145A semiconductor parameter analyzer,
8     whose address is 19. The diode leads should be connected
9     to the terminals labeled as SMU1, and GND on the I-V box
10    are used. The diode parameters are calculated from the measured
11    currents and voltages. They are displayed on the monitor.
12    The diode parameters include the series resistance (Rs),
13    barrier height (Vb), n-factor (nf), and saturation current
14    (Io). The correlation coefficient (cc) of the fit is also
15    displayed. The algorithm used in this calculation
16    is given in Chung-en Zah's Ph.D. thesis, "Millimeter-Wave
17    Monolithic Schottky Diode Imaging Arrays." The program is
18    menu driven. Default value for the diode area is 18 um*2.
19    The current ranges from 0.1 uA to 0.1 mA, and the voltage
20    ranges from 0 V to 2 V. Pressing return after the prompt
21    invokes the above default values. The data can be saved on disk,
22    retrived from disk, displayed on the monitor, or listed on the
23    printer.      }
24
25 type
26   sdesc = record
27       len : byte;
28       addr : integer;
29       end;
30   datarray = array [1..100] of real;
31
32 const
33   xmin = 190; xmax = 450;
34   ymin = 23;  ymax = 163;
35   xd = 3;
36
37 label
38   quit, main_menu, sub_menu, loop1, loop2;
39
40 var
41   cmd, recv : string[200];
42   cmddesc, recvdesc : sdesc;
43   my_address, system_controller, len, status : integer;
44   quote : char;
45   first, nchar, code: integer;
46   voltage, current, va, im, vm : real;
47   filename, buffer : string[10];
48   main_mode, sub_mode, q : char;

```

```

49   time_delay, j, ndata : integer;
50   v, i : datarray;
51   ia, ylstart, ylstop, xstart, xstop, ydata : real;
52   xcor, ycor, yb, ye : integer;
53   vb, area, rs, nf, gf, is : real;
54   number_transfer : file of real;
55   row, col : real;
56
57 procedure initialize(var addr, level : integer);
58   external 't488init';
59
60 procedure transmit(var s : sdesc; var status : integer);
61   external 't488xmit';
62
63 procedure receive(var r : sdesc; var len, status : integer);
64   external 't488recv';
65
66 procedure initialize_hp_4145a;
67
68 begin
69   time_delay := 4000;
70   quote := chr(39);
71   cmddesc.addr := ofs(cmd) + 1;
72   recvdesc.addr := ofs(recv) + 1;
73
74   my_address := 21;
75   system_controller := 0;
76   initialize(my_address, system_controller);
77
78   cmd := 'IFC REN MTA LISTEN 19';
79   cmddesc.len := length(cmd);
80   transmit(cmddesc, status);
81
82   cmd := 'DATA' + quote + 'US' + quote + '13 10';
83   cmddesc.len := length(cmd);
84   transmit(cmddesc, status);
85
86   cmd := 'DATA' + quote + 'IT2 CA1 BC' + quote + '13 10';
87   cmddesc.len := length(cmd);
88   transmit(cmddesc, status);
89 end;
90
91 procedure reset_hp_4145a;
92
93 begin
94   cmd := 'MTA LISTEN 19';
95   cmddesc.len := length(cmd);
96   transmit(cmddesc, status);
97
98   cmd := 'DATA' + quote + 'DCL SDG' + quote + '13 10';
99   cmddesc.len := length(cmd);
100  transmit(cmddesc, status);
101 end; {of reset_hp_4145a}

```

```
102
103 procedure set_voltage(volt : real);
104
105 var
106   v : string[18];
107
108 begin
109   str(volt,v); delete(v,8,6);
110   cmd := 'MTA LISTEN 19';
111   cmddesc.len := length(cmd);
112   transmit(cmddesc, status);
113
114   cmd := 'DATA' + quote + 'DV 1, 0, ' + v + ', 10E-3' +
115   quote + '13 10';
116   cmddesc.len := length(cmd);
117   transmit(cmddesc, status);
118
119   delay(time_delay);
120 end; {of set_voltage}
121
122 procedure set_current(ampere : real);
123
124 var
125   i : string[18];
126
127 begin
128   str(ampere,i); delete(i,8,6);
129   cmd := 'MTA LISTEN 19';
130   cmddesc.len := length(cmd);
131   transmit(cmddesc, status);
132
133   cmd := 'DATA' + quote + 'DI 1, 0, ' + i + ', 5' +
134   quote + '13 10';
135   cmddesc.len := length(cmd);
136   transmit(cmddesc, status);
137
138   delay(time_delay);
139 end; {of set_voltage}
140
141 procedure measure_current(var i : real);
142
143 begin
144   cmd := 'MTA LISTEN 19';
145   cmddesc.len := length(cmd);
146   transmit(cmddesc, status);
147
148   cmd := 'DATA' + quote + 'TI 1' + quote + '13 10';
149   cmddesc.len := length(cmd);
150   transmit(cmddesc, status);
151
152   cmd := 'MLA TALK 19';
153   cmddesc.len := length(cmd);
154   transmit(cmddesc, status);
```



```

155
156   recv := '                               ';
157   recvdesc.len := length(recv);
158   receive(recvdesc, len, status);
159   delete(recv, 1, 3);
160   val(recv, current, code);
161   if current < 0 then i := current
162   else begin
163     delete(recv, 1, 1);
164     val(recv, current, code);
165     i := current;
166   end;
167 end; {of measure_current}
168
169 procedure measure_voltage(var v : real);
170
171 begin
172   cmd := 'MTA LISTEN 19';
173   cmddesc.len := length(cmd);
174   transmit(cmddesc, status);
175
176   cmd := 'DATA' + quote + 'TV 1' + quote + '13 10';
177   cmddesc.len := length(cmd);
178   transmit(cmddesc, status);
179
180   cmd := 'MLA TALK 19';
181   cmddesc.len := length(cmd);
182   transmit(cmddesc, status);
183
184   recv := '                               ';
185   recvdesc.len := length(recv);
186   receive(recvdesc, len, status);
187   delete(recv, 1, 3);
188   val(recv, voltage, code);
189   if voltage < 0 then v := voltage
190   else begin
191     delete(recv, 1, 1);
192     val(recv, voltage, code);
193     v := voltage;
194   end;
195 end; {measure_voltage}
196
197 procedure clear_line;
198 begin
199   gotoxy(1,1); write('                               ');
200               write('                               ');
201 end; {clear_line}
202
203 procedure display(x, y : datarray; ndata : integer);
204 var
205   cont : char;
206   i : integer;
207 begin

```

```

208   clear_line;
209   for i := 1 to ndata do begin
210     gotoxy(1,1);   write(ndata,' - ',i,' ',y[i]:10,' ',x[i]:10);
211     read(cont);
212   end;
213 end; {of display}
214
215 procedure lsf(x, y : datarray; n : integer; var a, b, c : real);
216 {a = slope      b = intercept      c = correlation coeff. }
217 var
218   xsum, ysum, xysum, xssum, yssum : real;
219   xden, yden, rden : real;
220   i : integer;
221
222 begin
223   xsum := 0; ysum := 0;
224   xysum := 0;
225   xssum := 0; yssum := 0;
226   for i := 1 to n do begin
227     xsum := xsum + x[i];
228     ysum := ysum + y[i];
229     xysum := xysum + x[i]*y[i];
230     xssum := xssum + x[i]*x[i];
231     yssum := yssum + y[i]*y[i];
232   end;
233   xden := n * xssum - xsum * xsum;
234   yden := n * yssum - ysum * ysum;
235   rden := sqrt( xden * yden );
236   a := ( n*xysum - xsum*ysum ) / xden;
237   b := ( ysum*xssum - xysum*xsum ) / xden;
238   c := ( n*xysum - xsum*ysum ) / rden;
239 end; {of lsf}
240
241 procedure curve_fit_ivdata(x, y : datarray; n : integer;
242                             var rs, nf, gf, is : real);
243 { x = voltage   y = current   gf = goodness }
244 var
245   lx, ly, yav, dvdlni : datarray;
246   i : integer;
247 begin
248   {for i := 1 to n-1 do begin
249     dvdlni[i] := ( x[i+1]-x[i] ) / ( ln(y[i+1])-ln(y[i]) );
250     yav[i] := ( y[i+1] + y[i] ) / 2.0 ;
251   end;
252   lsf(yav, dvdlni, n-1, rs, nf, gf);
253   nf := nf/0.0259; This method is not accurate because yav is used.}
254   for i := 1 to n-1 do begin
255     lx[i] := ( y[i+1]-y[i] ) / ( x[i+1]-x[i] );
256     ly[i] := ( ln(y[i+1])-ln(y[i]) ) / ( x[i+1]-x[i] );
257   end;
258   lsf(lx, ly, n-1, rs, nf, gf);
259   rs := -rs/nf;
260   nf := 1/(0.0259*nf);

```

```

261   is := 0.0;
262   for i := 1 to n do begin
263     is := is + ln(y[i]) - (x[i]-y[i]*rs)/(nf*0.0259);
264   end;
265   is := exp(is/n);
266   gotoxy(1,7); write('Rs = ',rs:8);
267   gotoxy(1,8); write('nf = ',nf:8);
268   gotoxy(1,9); write('Io = ',is:8);
269   gotoxy(1,10); write('cc = ',gf:8);
270 end; {curve_fit_ivdata}
271
272
273 procedure draw_box (x1,y1,x2,y2,color : integer);
274 begin
275   draw (x1,y1,x1,y2,1);
276   draw (x1,y2,x2,y2,1);
277   draw (x2,y2,x2,y1,1);
278   draw (x2,y1,x1,y1,1);
279 end; {of draw_box}
280
281 procedure draw_x_ticks(x1,y1,x_increment,x2 : integer);
282 begin
283   while (x1<=x2) do begin
284     draw (x1,(y1+1),x1,(y1-2),1);
285     x1 := x1 + x_increment;
286   end; {while}
287 end; {of draw_x_ticks}
288
289 procedure draw_y_ticks(x1,y1,y_increment,y2 : integer);
290 begin
291   while (y1<=y2) do begin
292     draw ((x1+2),y1,(x1-4),y1,1);
293     y1 := y1 + y_increment;
294   end; {while}
295 end; {of draw_y_ticks}
296
297 procedure draw_graph(x1,y1,x2,y2,
298   x_increment, y_increment_left, y_increment_right :integer);
299 begin
300   draw_box(x1,y1-2,x2,y2+2,1);
301   draw_x_ticks (x1,y1,x_increment,x2);
302   draw_x_ticks (x1,y2,x_increment,x2);
303   draw_y_ticks (x1,y1,y_increment_left,y2);
304   draw_y_ticks (x2,y1,y_increment_right,y2);
305 end; {of procedure draw_graph}
306
307 function ten_to_power(n : integer) : real;
308 begin
309   ten_to_power := exp( n*ln(10) );
310 end; {of ten_to_power}
311
312 function ten_to(n : real) : real;
313 begin

```

```

314   ten_to := exp( n*ln(10) );
315 end; {of ten_to}
316
317 procedure write_x_coordinates(x1,y1,x_increment :integer;
318   x1c,xc_increment,x2c : real; field,fix :integer);
319 begin
320   repeat
321     gotoxy((x1 div 8), (y1 div 8));
322     write (x1c:field:fix);
323     x1c := x1c + xc_increment;
324     x1 := x1 + x_increment;
325     until x1c > x2c;
326 end; {of write_x_coordinates}
327
328 procedure write_y_coordinates(x1,y1,y_increment :integer;
329   y1c,yc_increment,y2c : real; field, fix : integer);
330 begin
331   repeat
332     gotoxy((x1 div 8), (y1 div 8));
333     write (y1c:field:fix);
334     y1c := y1c + yc_increment;
335     y1 := y1 + y_increment;
336     until y1c < y2c;
337 end; {of write_y_coordinates}
338
339 procedure set_up;
340 begin
341     hires; hirescolor(15);
342     draw_graph(xmin,ymin,xmax,ymax,(xmax-xmin)div 5,
343       (ymax-ymin)div 6, (ymax-ymin)div 6);
344 end; {of set_up}
345
346 procedure write_y_axis;
347 begin
348   write_y_coordinates(xmin-40,ymin+7,(ymax-ymin) div 6,
349     ylstop,(ylstart-ylstop)/6,ylstart,4,0);
350 end; {of write_y_axis}
351
352 procedure write_x_axis;
353 begin
354   write_x_coordinates (xmin,ymax+14,(xmax-xmin) div 5,
355     xstart,(xstop-xstart)/5,xstop,4,1);
356 end; {of write_x_axis}
357
358 procedure plot_ivdata(x, y : datarray; n : integer);
359 var
360   i : integer;
361 begin
362   display(x, y, n);
363   set_up;
364   gotoxy(1,1); write('enter ylstop ylstop xstart xstop : ');
365   readln(ylstart, ylstop, xstart, xstop);
366   write_y_axis; write_x_axis;

```

```

367   for i := 1 to n do begin
368       xcor := round((x[i]-xstart)*(xmax-xmin)/(xstop-xstart));
369       ycor := round((ylstop-ln(y[i])/ln(10.0))*
370                   (ymax-ymin)/(ylstop-ylstart));
371       graphwindow(xmin,ymin,xmax,ymax);
372       plot(xcor-1,ycor,1);
373       plot(xcor+1,ycor,1);
374       plot(xcor,ycor-1,1);
375       plot(xcor,ycor+1,1);
376   end;
377 end; {plot_datafile}
378
379 procedure plot_ivcurve;
380 var
381   i, yd : integer;
382   amp, l, xc : real;
383   cont : char;
384 begin
385   xc := 1.0;
386   yd :=1;
387   repeat
388       amp := ylstart + (ylstop-ylstart)*xc*yd/(ymax-ymin);
389       amp := ten_to(amp);
390       l := 0.0259*nf*ln(amp/is + 1) + amp*rs;
391       {clear_line;
392       gotoxy(1,1); write(1:8,' ',amp:8);
393       read(cont);}
394       amp := ln(amp)/ln(10.0);
395       xcor := round((l-xstart)*(xmax-xmin)/(xstop-xstart));
396       ycor := round((ylstop-amp)*(ymax-ymin)/(ylstop-ylstart));
397       graphwindow(xmin,ymin,xmax,ymax);
398       plot(xcor,ycor,1);
399       xc := xc + 1.0;
400   until xc >= int(((ymax-ymin) div yd) + 1);
401 end; {of plot_datafile}
402
403 procedure readfile(var x, y : datarray; var ndata : integer);
404 var
405   i : integer;
406   data_transfer : text;
407 begin
408   clear_line;
409   gotoxy(1,1); write('enter filename(read) : ');
410   readln(filename);
411   assign(data_transfer,filename);
412   ndata := 0;
413   reset(data_transfer);
414   while not eof(data_transfer) do begin
415       ndata := ndata + 1;
416       readln(data_transfer,x[ndata],y[ndata]);
417   end;
418   close(data_transfer);
419 end; {of readfile}

```

```

420
421 procedure writefile(x, y : datarray; ndata : integer);
422 var
423     i : integer;
424     data_transfer : text;
425 begin
426     clear_line;
427     gotoxy(1,1); write('enter filename(write) : ');
428     readln(filename);
429     assign(data_transfer,filename);
430     rewrite(data_transfer);
431     for i := 1 to ndata do
432     begin
433         writeln(data_transfer,x[i],' ',y[i]);
434     end;
435     close(data_transfer);
436 end; {of writefile}
437
438 procedure convert_lin_to_log(var z : datarray;
439                             ndata : integer; q : char);
440 var
441     i : integer;
442 begin
443     clear_line;
444     gotoxy(1,1); write('taking log(',q,') ... ');
445     for i := 1 to ndata do begin
446         z[i] := ln(z[i])/ln(10.0);
447     end;
448     write('done');
449     read(q);
450 end; {of convert_lin_to_log}
451
452 procedure calculate(var x, y : datarray; ndata : integer);
453 var
454     i : integer;
455 begin
456     clear_line;
457     gotoxy(1,1); write('log(x) or log(y) : ');
458     read(q);
459     if q = 'x' then convert_lin_to_log(x, ndata, q);
460     if q = 'y' then convert_lin_to_log(y, ndata, q);
461 end; {of calculate}
462
463 procedure list(x, y : datarray; ndata : integer);
464 var
465     i : integer;
466 begin
467     writeln(lst,'filename : ',filename);
468     for i := 1 to ndata do begin
469         writeln(lst,i,' ',y[i]:10,' ',x[i]:10);
470     end;
471 end; {of writedata}
472

```

```

473 procedure plot_a_point(x, y : real);
474 begin
475   xcor := round((x-xstart)*(xmax-xmin)/(xstop-xstart));
476   ycor := round((ylstop-y)*(ymax-ymin)/(ylstop-ylstart));
477   graphwindow(xmin,ymin,xmax,ymax);
478   plot(xcor-1,ycor,1);
479   plot(xcor+1,ycor,1);
480   plot(xcor,ycor-1,1);
481   plot(xcor,ycor+1,1);
482 end; {plot_a_point}
483
484 procedure test(var x, y : datarray; ndata : integer);
485 var
486   i_zero : real;
487 begin
488   ndata := 0;
489   i_zero := 0.0;
490   set_up;
491   gotoxy(1,1); write('enter ylstart ylstop xstart xstop : ');
492   readln(ylstart, ylstop, xstart, xstop);
493   write_y_axis;
494   write_x_axis;
495   initialize_hp_4145a;
496   set_current(i_zero);
497   for j := trunc(ylstart)+1 to trunc(ylstop)-1 do begin
498     ndata := ndata + 1;
499     ia := 1.0 * ten_to_power(j);
500     set_current(ia);
501     measure_current(y[ndata]);
502     measure_voltage(x[ndata]);
503     ydata := ln(y[ndata])/ln(10.0);
504     plot_a_point(x[ndata],ydata);
505
506     ndata := ndata + 1;
507     ia := 2.0 * ten_to_power(j);
508     set_current(ia);
509     measure_current(y[ndata]);
510     measure_voltage(x[ndata]);
511     ydata := ln(y[ndata])/ln(10.0);
512     plot_a_point(x[ndata],ydata);
513
514     ndata := ndata + 1;
515     ia := 4.0 * ten_to_power(j);
516     set_current(ia);
517     measure_current(y[ndata]);
518     measure_voltage(x[ndata]);
519     ydata := ln(y[ndata])/ln(10.0);
520     plot_a_point(x[ndata],ydata);
521   end;
522   set_current(i_zero);
523   curve_fit_ivdata(x, y, ndata, rs, nf, gf, is);
524 end; {of test}
525

```

```

526 procedure find_barrier_height;
527 begin
528   clear_line;
529   gotoxy(1,1); write('Enter diode area : '); readln(area);
530   vb := (-0.0259) * ln( is / (area*8.16*sqr(300)) );
531   gotoxy(1,12); write('Vb = ',vb:8);
532 end;
533
534 procedure create_new_buffer;
535 var
536   select : integer;
537
538 begin
539   clear_line;
540   gotoxy(1,1);
541   write('Enter 1 for create & 0 for append to a file : ');
542   read(select);
543   clear_line;
544   gotoxy(1,1); write('Enter filename : '); readln(buffer);
545   if select = 1 then
546     begin
547       assign(number_transfer,buffer);
548       rewrite(number_transfer);
549       close(number_transfer);
550     end;
551   write(lst,'Row Col      Rs      N_factor      ');
552   writeln(lst,'Is          Vb          CC');
553 end;
554
555 procedure record_diode_parameter;
556 begin
557   assign(number_transfer,buffer);
558   reset(number_transfer);
559   seek(number_transfer,filesize(number_transfer));
560   clear_line;
561   gotoxy(1,1); write('Enter row & col : '); readln(row,col);
562   write(number_transfer,row,col,rs,nf,is,vb,gf);
563   write(lst,row:2:0,' ',col:2:0,' ',rs:10,' ',nf:10,' ');
564   writeln(lst,is:10,' ',vb:10,' ',gf:10);
565   close(number_transfer);
566 end;
567
568 procedure write_diode_parameter;
569 begin
570   write(lst,'Row Col      Rs      N_factor      ');
571   writeln(lst,'Is          Vb          CC');
572   clear_line;
573   gotoxy(1,1); write('Enter filename : '); readln(buffer);
574   assign(number_transfer,buffer);
575   reset(number_transfer);
576   while not(eof(number_transfer)) do
577     begin
578       read(number_transfer,row,col,rs,nf,is,vb,gf);

```



```

579     write(lst,row:2:0,' ',col:2:0,' ',rs:10,' ');
580     writeln(lst,nf:10,' ',is:10,' ',vb:10,' ',gf:10);
581     end;
582     close(number_transfer);
583 end;
584
585 begin {main program}
586 ylstart := -7.0;   ylstop := -2.0;
587 xstart  := 0;     xstop  := 2.0;
588 area := 18e-8;   {um e-8}
589 main_menu:
590     clrscr;
591     textmode(bw80);
592     clear_line;
593     gotoxy(1,1); write('Main Menu>> (t)test-iv (r)read-iv (q)quit : ');
594 loop1:
595     if not(keypressed) then goto loop1
596     else read(kbd, main_mode);
597     case main_mode of
598         't' : test(v, i, ndata);
599         'r' : begin
600                 readfile(v, i, ndata);
601                 plot_ivdata(v, i, ndata);
602                 curve_fit_ivdata(v, i, ndata, rs, nf, gf, is);
603             end;
604         'q' : goto quit;
605     end;
606     find_barrier_height;
607     plot_ivcurve;
608 sub_menu:
609     clear_line;
610     gotoxy(1,1); write('Sub Menu>> ');
611     write('(d)disp (l)lst (s)sav (b)buf r(rec) w(wri) q(qit) : ');
612 loop2:
613     if not(keypressed) then goto loop2
614     else read(kbd, sub_mode);
615     case sub_mode of
616         'd' : display(v, i, ndata);
617         'l' : list(v, i, ndata);
618         's' : writefile(v, i, ndata);
619         'q' : goto main_menu;
620         'b' : create_new_buffer;
621         'r' : record_diode_parameter;
622         'w' : write_diode_parameter;
623     end;
624     goto sub_menu;
625 quit:
626 end.

```

Appendix E

Computer Program Listing
for Doping Profile Measurement

```

1 program cv_measurement_of_Schottky_diode;
2
3
4 {   The name of this program is cvdiode. It was developed
5     for measuring the C-V characteristics and to calculate the
6     doping profile of a diode. The program is written in
7     Turbo Pascal. An IBM-XT is used to control the Hp4280A
8     C-meter, whose address is 17. The measured data can be saved
9     on the disk, retrieved from the disk, displayed on the monitor,
10    or listed on the printer. Once a command is entered, press
11    return to invoke the command. Default value for the diode
12    area is 2.6e-3 cm^2, which is the area of the mercury
13    probe. The doping concentration ranges from 10^16 cm^-3 to
14    10^18 cm^-3, and the depth ranges from 0 um to 0.5 um. The
15    capacitance ranges from 80 pF to 400 pF, and the voltage ranges
16    from -5 V to 0.5 V at 0.5 V step increment. The diffusion barrier
17    potential is assumed to be 0.94 V for aluminum on GaAs with
18    about 10^17 cm^-3 doping concentration. The zero bias
19    capacitance (Co), capacitance ratio (Cr), and capacitance
20    exponent (Gm) are determined from curve-fitting the measured
21    C-V characteristic. The correlation coefficient (cc) for this fit
22    is also displayed on the monitor. The measured doping profile is
23    least-square-fitted with the hyperabrupt doping function.   }
24
25
26
27
28 type
29     sdesc = record
30         len : byte;
31         addr : integer;
32     end;
33     datarray = array[1..100] of real;
34
35 const
36     xmin = 190; xmax = 550;
37     ymin = 33; ymax = 163;
38
39 label
40     quit, loop_1, loop_2;
41
42 var
43     cmd, recv : string[200];
44     vstart, vstop, vstep : string[18];
45     cmddesc, recvdesc : sdesc;
46     my_address, system_controller, len, status : integer;
47     quote : char;
48     i, n, first, nchar, code: integer;

```

```

49   cap, cond, volt, buffer :string[20];
50   capacitance, conductance, voltage : real;
51   slope_a, gamma, intercept_b, correlation_coeff : real;
52   c, v, g, lnv, lnc, lnx, lnnd, nd, xd : datarray;
53   eo, er, eq, ci, area, dcdv, nd1, nd2, nd3, cratio : real;
54   co, nn, no, phi : real;
55   data_transfer : text;
56   filename : string[8];
57   ylstart, ylstop, ylunit, xstart, xstop, xstep, xunit : real;
58   ndata : integer;
59   q, cont : char;
60   number_transfer : file of real;
61   row, col : real;
62
63 procedure initialize(var addr, level : integer);
64   external 't488init';
65
66 procedure transmit(var s : sdesc; var status : integer);
67   external 't488xmit';
68
69 procedure receive(var r : sdesc; var len, status : integer);
70   external 't488recv';
71
72 function ten_to_power(n : integer) : real;
73 begin
74   ten_to_power := exp( n*ln(10) );
75 end; {of ten_to_power}
76
77 function ten_to(n : real) : real;
78 begin
79   ten_to := exp( n*ln(10) );
80 end; {of ten_to}
81
82 procedure clear_line;
83 begin
84   gotoxy(1,1); write(' ');
85   write(' ');
86 end; {clear_line}
87
88 procedure convert_lin_to_log(var z : datarray; ndata : integer; q : char);
89 var
90   i : integer;
91 begin
92   clear_line;
93   gotoxy(1,1); write('taking log(',q,') ... ');
94   for i := 1 to ndata do begin
95     z[i] := ln(z[i])/ln(10.0);
96   end;
97   write('done');
98   read(q);
99 end; {of convert_lin_to_log}
100
101 procedure lsf(ndata : integer; x, y : datarray;

```

```

102         var slope, intercept, cc : real);
103 var
104     xsum, ysum, xysum, xssum, yssum, xden, yden, rden : real;
105 begin
106     xsum := 0; ysum := 0; xysum := 0;
107     xssum := 0; yssum := 0;
108     for i := 1 to ndata do begin
109         xsum := xsum + x[i];
110         ysum := ysum + y[i];
111         xysum := xysum + x[i]*y[i];
112         xssum := xssum + x[i]*x[i];
113         yssum := yssum + y[i]*y[i];
114     end;
115     xden := ndata*xssum-xsum*xsum;
116     yden := ndata*yssum-ysum*ysum;
117     rden := sqrt(xden*yden);
118     slope := (ndata*xysum-xsum*ysum)/xden;
119     intercept := (ysum*xssum-xysum*xsum)/xden;
120     cc := (ndata*xysum-xsum*ysum)/rden;
121 end; {of lsf}
122
123 procedure draw_box (x1,y1,x2,y2,color : integer);
124 begin
125     draw (x1,y1,x1,y2,1);
126     draw (x1,y2,x2,y2,1);
127     draw (x2,y2,x2,y1,1);
128     draw (x2,y1,x1,y1,1);
129 end; {of draw_box}
130
131 procedure draw_x_ticks(x1,y1,x_increment,x2 : integer);
132 begin
133     while (x1<=x2) do begin
134         draw (x1,(y1+1),x1,(y1-2),1);
135         x1 := x1 + x_increment;
136     end; {while}
137 end; {of draw_x_ticks}
138
139 procedure draw_y_ticks(x1,y1,y_increment,y2 : integer);
140 begin
141     while (y1<=y2) do begin
142         draw ((x1+2),y1,(x1-4),y1,1);
143         y1 := y1 + y_increment;
144     end; {while}
145 end; {of draw_y_ticks}
146
147 procedure draw_graph(x1,y1,x2,y2,
148     x_increment, y_increment_left, y_increment_right :integer);
149 begin
150     draw_box(x1,y1-2,x2,y2+2,1);
151     draw_x_ticks (x1,y1,x_increment,x2);
152     draw_x_ticks (x1,y2,x_increment,x2);
153     draw_y_ticks (x1,y1,y_increment_left,y2);
154     draw_y_ticks (x2,y1,y_increment_right,y2);

```

```

155 end; {of procedure draw_graph}
156
157 procedure write_x_coordinates(x1,y1,x_increment :integer;
158   x1c,xc_increment,x2c : real; field,fix :integer);
159 begin
160   repeat
161     gotoxy((x1 div 8), (y1 div 8));
162     write (x1c:field:fix);
163     x1c := x1c + xc_increment;
164     x1 := x1 + x_increment;
165     until x1c > x2c;
166 end; {of write_x_coordinates}
167
168 procedure write_y_coordinates(x1,y1,y_increment :integer;
169   y1c,yc_increment,y2c : real; field, fix : integer);
170 begin
171   repeat
172     gotoxy((x1 div 8), (y1 div 8));
173     write (y1c:field:fix);
174     y1c := y1c + yc_increment;
175     y1 := y1 + y_increment;
176     until y1c < y2c;
177 end; {of write_y_coordinates}
178
179 procedure set_up;
180 begin
181   hires; hirescolor(15);
182   draw_graph(xmin,ymin,xmax,ymax,(xmax-xmin)div 5,
183     (ymax-ymin)div 5, (ymax-ymin)div 5);
184 end; {of set_up}
185
186 procedure write_y_axis;
187 begin
188   write_y_coordinates(xmin-40,ymin+7,(ymax-ymin) div 5,
189     ylstop,(ylstart-ylstop)/5,ylstart,4,1);
190 end; {of write_y_axis}
191
192 procedure write_x_axis;
193
194 begin
195   write_x_coordinates (xmin,ymax+14,(xmax-xmin) div 5,
196     xstart,(xstop-xstart)/5,xstop,4,2);
197 end; {of write_x_axis}
198
199 procedure write_y_axis_exp;
200 begin
201   write_y_coordinates(xmin-40,ymin+7,(ymax-ymin) div 2,
202     ylstop,(ylstart-ylstop)/2,ylstart,4,1);
203 end; {of write_y_axis_exp}
204
205 procedure plot_a_pixel(x, y : real);
206 var
207   xcor, ycor : integer;

```

```

208 begin
209   xcor := round((x-xstart)*(xmax-xmin)/(xstop-xstart));
210   ycor := round((ylstop-y)*(ymax-ymin)/(ylstop-ylstart));
211   graphwindow(xmin,ymin,xmax,ymax);
212   plot(xcor,ycor,1);
213 end;
214
215 procedure plot_a_point(x, y : real);
216 var
217   xcor, ycor : integer;
218 begin
219   xcor := round((x-xstart)*(xmax-xmin)/(xstop-xstart));
220   ycor := round((ylstop-y)*(ymax-ymin)/(ylstop-ylstart));
221   graphwindow(xmin,ymin,xmax,ymax);
222   plot(xcor-1,ycor,1);
223   plot(xcor+1,ycor,1);
224   plot(xcor,ycor-1,1);
225   plot(xcor,ycor+1,1);
226 end; {plot_a_point}
227
228 procedure plot_datafile(x, y : datarray; ndata : integer);
229 var
230   i, xcor, ycor, yd : integer;
231   amp, l, xc : real;
232   q : char;
233
234 begin
235   set_up;
236   gotoxy(1,1); write('enter ylstart ylstop xstart xstop : ');
237   readln(ylstart, ylstop, xstart, xstop);
238   write_y_axis; write_x_axis;
239   for i := 1 to ndata do begin
240     xcor := round((x[i]-xstart)*(xmax-xmin)/(xstop-xstart));
241     ycor := round((ylstop-y[i]*(ymax-ymin)/(ylstop-ylstart));
242     graphwindow(xmin,ymin,xmax,ymax);
243     plot(xcor-1,ycor,1);
244     plot(xcor+1,ycor,1);
245     plot(xcor,ycor-1,1);
246     plot(xcor,ycor+1,1);
247   end;
248 end; {of plot_datafile}
249
250 procedure readfile(var x, y : datarray; var ndata : integer);
251 var
252   i : integer;
253   data_transfer : text;
254 begin
255   clear_line;
256   gotoxy(1,1); write('enter filename(read) : ');
257   readln(filename);
258   assign(data_transfer,filename);
259   ndata := 0;
260   reset(data_transfer);

```

```

261   while not eof(data_transfer) do begin
262       ndata := ndata + 1;
263       readln(data_transfer,x[ndata],y[ndata]);
264   end;
265   close(data_transfer);
266 end; {of readfile}
267
268 procedure writefile(x, y : datarray; ndata : integer);
269 var
270     i : integer;
271     data_transfer : text;
272 begin
273     clear_line;
274     gotoxy(1,1); write('enter filename(write) : ');
275     readln(filename);
276     assign(data_transfer,filename);
277     rewrite(data_transfer);
278     for i := 1 to ndata do
279         begin
280             writeln(data_transfer,x[i],' ',y[i]);
281         end;
282     close(data_transfer);
283 end; {of writefile}
284
285 procedure list(x, y : datarray; ndata : integer);
286 var
287     i : integer;
288 begin
289     for i := 1 to ndata do begin
290         writeln(lst,x[i],' ',y[i]);
291     end;
292 end; {of writedata}
293
294 procedure display(x, y : datarray; ndata : integer);
295 var
296     i : integer;
297     q : char;
298 begin
299     clear_line;
300     for i := 1 to ndata do begin
301         gotoxy(1,1); writeln(ndata,' - ',i,' ',x[i],' ',y[i]);
302         read(q);
303     end;
304 end; {of display}
305
306 procedure create_new_buffer;
307 var
308     select : integer;
309 begin
310     clear_line;
311     gotoxy(1,1); write('Enter 1 for create & 0 for append to a file : ');
312     read(select);
313     clear_line;

```

```

314 gotoxy(1,1); write('Enter filename : '); readln(buffer);
315 if select = 1 then
316 begin
317 assign(number_transfer,buffer);
318 rewrite(number_transfer);
319 close(number_transfer);
320 end;
321 write(lst,'Row Col C.5 Co');
322 writeln(lst,'Cm1 Cm2 Vb Gamma');
323 end;
324
325 procedure record_diode_parameter;
326 var
327 vb, cp5, co, cm1, cm2 : real;
328 begin
329 assign(number_transfer,buffer);
330 reset(number_transfer);
331 seek(number_transfer,filesize(number_transfer));
332 clear_line;
333 gotoxy(1,1); write('Enter row & col : '); readln(row,col);
334 vb := xstart;
335 cp5 := c[ndata];
336 co := c[ndata-1];
337 cm1 := c[ndata-2];
338 cm2 := c[1];
339 write(number_transfer,row,col,cp5,co,cm1,cm2,vb,gamma);
340 write(lst,row:2:0,' ',col:2:0,' ',cp5:9,' ');
341 writeln(lst,co:9,' ',cm1:9,' ',cm2:9,' ',vb:3:1,' ',gamma:9);
342 close(number_transfer);
343 end;
344
345 procedure write_diode_parameters;
346 var
347 vb, cp5, cp0, cm1, cm2, gm : real;
348 begin
349 write(lst,'Row Col C.5 Co ');
350 writeln(lst,'Cm1 Cm2 Vb Gamma');
351 clear_line;
352 gotoxy(1,1); write('Enter filename : '); readln(buffer);
353 assign(number_transfer,buffer);
354 reset(number_transfer);
355 while not(eof(number_transfer)) do
356 begin
357 read(number_transfer,row,col,cp5,cp0,cm1,cm2,vb,gm);
358 write(lst,row:2:0,' ',col:2:0,' ',cp5:9,' ');
359 writeln(lst,cp0:9,' ',cm1:9,' ',cm2:9,' ',vb:9,' ',gm:9);
360 end;
361 close(number_transfer);
362 end;
363
364 procedure cvtest(var v, c : datarray; var ndata : integer);
365 label
366 again, loop;

```



```

367 var
368   cf, vf, vinc : real;
369 begin
370   again:
371     set_up;
372     ylstart := 80.0;   ylstop := 400.0;   ylunit := 1e-12;
373     xstart := -5.0;   xstop := 0.5;     xstep := 0.5;
374     gotoxy(1,1);
375     write('Cd_vs_Vd>> ylstart ylstop ylunit xstart xstop xstep : ');
376     readln(ylstart, ylstop, ylunit, xstart, xstop, xstep);
377     ndata := trunc( (xstop-xstart) / xstep ) + 1;
378     write_y_axis;
379     write_x_axis;
380     quote := chr(39);
381     cmddesc.addr := ofs(cmd) + 1;
382     recvdesc.addr := ofs(recv) + 1;
383
384     my_address := 21;
385     system_controller := 0;
386     initialize(my_address, system_controller);
387
388     cmd := 'IFC REN MTA LISTEN 17' ;
389     cmddesc.len := length(cmd);
390     transmit(cmddesc, status);
391
392     cmd := 'DATA ' + quote + 'LE2 Z0' + quote + '13 10' ;
393     cmddesc.len := length(cmd);
394     transmit(cmddesc, status);
395
396     str(xstart,vstart);
397     str(xstop,vstop);
398     str(xstep,vstep);
399     cmd := 'DATA ' + quote + 'IB2, CE1, PS' + vstart + ', PP' + vstop +
400           ', PE' + vstep + ', PL1, PD1, TR3, SW1' + quote + '13 10' ;
401     cmddesc.len := length(cmd);
402     transmit(cmddesc, status);
403
404     cmd := 'TALK 17' ;
405     cmddesc.len := length(cmd);
406     transmit(cmddesc, status);
407
408     recv := ' ';
409     recvdesc.len := length(recv);
410     receive(recvdesc, len, status);
411     for i := 1 to ndata do begin
412       recv := ' ';
413       recvdesc.len := length(recv);
414       receive(recvdesc, len, status);
415       first := pos('C',recv) + 2;
416       nchar := pos(',',recv) - first;
417       cap := copy(recv,first+1,nchar);
418       delete(recv,1,pos(',',recv));
419       first := pos('G',recv) + 2;

```

```

420     nchar := pos(',',recv) - first;
421     cond := copy(recv,first+1,nchar);
422     delete(recv,1,pos(',',recv)+1);
423     volt := recv;
424     if recv[1] = '+' then delete(volt,1,1);
425     val(cap,capacitance,code);
426     val(cond,conductance,code);
427     val(volt,voltage,code);
428     c[i] := capacitance;
429     v[i] := voltage;
430     g[i] := conductance;
431     plot_a_point(v[i], c[i]/ylunit);
432 end;
433 {if v[i] is replace with v[i]-phi, fit will be better}
434 for i := 1 to ndata do begin
435     lnc[i] := ln(c[i])/ln(10);
436     lnv[i] := ln(1-(v[i]/phi))/ln(10);
437 end;
438 slope_a := 0;
439 intercept_b := 0;
440 correlation_coeff := 0;
441 lsf(ndata, lnv, lnc, slope_a, intercept_b, correlation_coeff);
442 gamma := -slope_a;
443 co := ten_to(intercept_b);
444 cratio := c[ndata]/c[1];
445 gotoxy(1,5); writeln('Cn = ',c[ndata]:8);
446 gotoxy(1,6); writeln('C1 = ',c[1]:8);
447 gotoxy(1,7); writeln('CR = ',cratio:2:1);
448 gotoxy(1,10); writeln('Co = ',co:8);
449 gotoxy(1,11); writeln('Gm = ',gamma:4:3);
450 gotoxy(1,12); writeln('cc = ',correlation_coeff:6:5);
451 vinc := (xstop-xstart)/100.0;
452 vf := xstart;
453 repeat
454     cf := co / exp( gamma*(ln(1-vf/phi)) );
455     plot_a_pixel(vf, cf/ylunit);
456     vf := vf + vinc;
457 until (vf = xstop) or (vf > xstop);
458 loop:
459 clear_line;
460 gotoxy(1,1); write('Cd_vs_Vd>> (s)sav (d)disp ');
461 write(' (l)lst (r)rep (w) wri (c) crt (k) kip (q) n_x : '); read(q);
462 case q of
463     's' : writefile(v, c, ndata);
464     'd' : writefile(v, c, ndata);
465     'l' : list(v, c, ndata);
466     'r' : goto again;
467     'w' : write_diode_parameters;
468     'c' : create_new_buffer;
469     'k' : record_diode_parameter;
470 end;
471 if q <> 'q' then goto loop;
472 end; {of cvtest}

```

```

473
474 procedure read_cv(var x, y : datarray; var ndata : integer);
475 label
476   again, loop;
477 var
478   i : integer;
479   cf, vf, vinc : real;
480 begin
481 again:
482   readfile(x, y, ndata);
483   display(x, y, ndata);
484   set_up;
485   ylstart := 80.0;   ylstop := 400.0;   ylunit := 1e-12;
486   xstart := -5.0;   xstop := 0.5;
487   gotoxy(1,1); write('Cd_vs_Xd>> ylstart ylstop ylunit xstart xstop : ');
488   readln(ylstart, ylstop, ylunit, xstart, xstop);
489   write_y_axis;
490   write_x_axis;
491   for i := 1 to ndata do begin
492     plot_a_point(v[i],c[i]/ylunit);
493   end;
494   {if v[i] is replace with v[i]-phi, fit will be better}
495   for i := 1 to ndata do begin
496     lnc[i] := ln(c[i])/ln(10);
497     lnv[i] := ln(1-(v[i]/phi))/ln(10);
498   end;
499   slope_a := 0;
500   intercept_b := 0;
501   correlation_coeff := 0;
502   lsf(ndata, lnv, lnc, slope_a, intercept_b, correlation_coeff);
503   gamma := -slope_a;
504   co := ten_to(intercept_b);
505   cratio := c[ndata]/c[1];
506   gotoxy(1,5); writeln('Cn = ',c[ndata]:8);
507   gotoxy(1,6); writeln('C1 = ',c[1]:8);
508   gotoxy(1,7); writeln('CR = ',cratio:3:2);
509   gotoxy(1,10); writeln('Co = ',co:8);
510   gotoxy(1,11); writeln('Gm = ',gamma:4:3);
511   gotoxy(1,12); writeln('cc = ',correlation_coeff:6:5);
512   vinc := (xstop-xstart)/100.0;
513   vf := xstart;
514   repeat
515     cf := co / exp( gamma*(ln(1-vf/phi)) );
516     plot_a_pixel(vf, cf/ylunit);
517     vf := vf + vinc;
518   until (vf = xstop) or (vf > xstop);
519 loop:
520   clear_line;
521   gotoxy(1,1); write('Cd_vs_Vd>> (s)save (d)display ');
522     write('(l)list (r) repeat (q)quit : '); read(q);
523   if q = 's' then writefile(v, c, ndata);
524   if q = 'd' then display(v, c, ndata);
525   if q = 'l' then list(v, c, ndata);

```

```

526   if q = 'r' then goto again;
527   if q <> 'q' then goto loop
528 end;
529
530 procedure get_plot_a_file;
531 label
532   again, loop;
533 var
534   x, y : datarray;
535   ndat : integer;
536   q : char;
537
538 begin
539 again:
540   readfile(x, y, ndat);
541   display(x, y, ndat);
542   plot_datafile(x, y, ndat);
543 loop:
544   clear_line;
545   gotoxy(1,1); write('Get & Plot>> (d)display ');
546               write('(l)list (r) repeat (q)quit : '); read(q);
547   if q = 'd' then display(x, y, ndat);
548   if q = 'l' then list(x, y, ndat);
549   if q = 'r' then goto again;
550   if q <> 'q' then goto loop;
551 end;
552
553 begin {main}
554 loop_1:
555   clrscr;
556   textmode(bw80);
557   phi := 0.94; {phi varies slightly with doping concentration}
558   gotoxy(1,1);
559   write('Menu>> m(measure-cv) r(read-cv) p(get&plot) (q)quit : ');
560   read(q);
561   case q of
562     'm' : cvtest(v, c, ndata);
563     'r' : read_cv(v, c, ndata);
564     'p' : get_plot_a_file;
565     'q' : goto quit;
566   end;
567   clrscr;
568   if q = 'p' then goto loop_1;
569   set_up;
570   ylstart := 16.0;   ylstop := 18.0;
571   xstart := 0.0;    xstop := 0.5;   xunit := 1.0;
572   gotoxy(1,1);
573   write('Nd_vs_Xd>> ylstart, ylstop, xstart, xstop, xunit [exp & um] :');
574   readln(ylstart, ylstop, xstart, xstop, xunit);
575   write_y_axis_exp;
576   write_x_axis;
577   eo := 8.85e-14;
578   er := 13.1;

```

```

579  eq := 1.6e-19;
580  area := 2.6e-3; {area of Hg probe given in manual = 2.6e-3 [cm2]}
581  clear_line;
582  gotoxy(1,1); write('Nd_vs_Xd>> diode area [cm2] : ');
583  readln(area);
584  for i := 1 to ndata-1 do begin
585      ci := ( c[i] + c[i+1] )/2;
586      xd[i] := 1e4 * eo * er * area / ci;  {in units of microns}
587      dcdv := ( c[i+1]-c[i] ) / ( v[i+1]-v[i] );
588      nd1 := ci/eq;          {nd1 := ci * ci * ci;}
589      nd2 := ci/(eo*er);    {nd2 := eq * eo *er;}
590      nd1 := nd1 * nd2;     {nd3 := area * area * dcdv;}
591      nd1 := nd1 * ci / area; {nd1 := nd1/nd2;}
592      nd[i] := nd1/(area * dcdv);
593      {nd[i] := nd1/nd3; units of atoms per cm-3}
594      lnnd[i] := ln(nd[i])/ln(10.0);
595      lnxd[i] := ln(xd[i])/ln(10.0);
596      plot_a_point(xd[i]/xunit,lnnd[i]);
597  end;
598  slope_a := 0;
599  intercept_b := 0;
600  correlation_coeff := 0;
601  lsf(ndata-1, lnxd, lnnd, slope_a, intercept_b, correlation_coeff);
602  nn := -slope_a;
603  no := ten_to(intercept_b);
604  gotoxy(1,6); writeln('No = ',no:8);
605  gotoxy(1,7); writeln('n = ',nn:4:3);
606  gotoxy(1,8); writeln('cc = ',correlation_coeff:6:5);
607
608  loop_2:
609      clear_line;
610      gotoxy(1,1);
611      write('Nd_vs_Xd>> (s)save (d)display (l)list (q)quit: '); read(q);
612      if q = 's' then writefile(xd, nd, ndata-1);
613      if q = 'd' then display(xd, nd, ndata-1);
614      if q = 'l' then list(xd, nd, ndata-1);
615      if q <> 'q' then goto loop_2;
616      goto loop_1;
617  quit:
618  end.

```