Chapter 2

Chlorine-Terminated Silicon(111) Surfaces

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2.1 Introduction

Functionalization of Silicon(111) surfaces with covalently bonded organic reagents has received an increasing interest as a method for tailoring the chemical and electrical properties of Si surfaces.²⁻⁶ In particular, alkyl passivated Si(111) surfaces prepared through a two-step chlorination/alkylation route⁴⁻⁶ have shown low charge-carrier surface recombination velocities, oxidation resistance in air and during anodic current flow in electrochemical cells, and a number of other interesting chemical and electronic properties. The chlorinated Si(111) surface (Cl/Si(111)) is a key intermediate in this chemistry. Chemical component analysis of Cl/Si(111) surfaces has been performed using X-ray photoelectron spectroscopy,^{6, 7} infrared,⁸ and high-resolution electron energy loss spectroscopy,⁹ indicating formation of the Cl-Si bond. The expected 1×1 structure of the chlorination layer was confirmed in a recent scanning tunneling microscopy study of Cl/Si(111) surface prepared by the gas phase reaction of H/Si(111) with molecular chlorine.⁹ Cl-Si bond-induced stacking faults were previously observed by Itchkawitz et al.¹⁰ That report was based upon the observation that inequivalent crystallographic

directions ($\langle \overline{112} \rangle$ and $\langle 11\overline{2} \rangle$) were found to exhibit the same bilayer step edge structure. In addition, significant enhancement of surface conductance for Cl/Si(111) was reported by Lopinski et al.¹¹ A p-type inversion layer was used to interpret this effect in terms of the formation of a 2D hole gas.

The morphology of a chlorinated Si(111) surface is apparently dependent upon the nature of the chemistry utilized for that chlorination, and that is the hypothesis that we test in this chapter. For example, when a H/Si(111) surface that is characterized by a low density of step edges is halogenated via the reaction with gas phase molecular chlorine, the resultant Cl/Si(111) surface remains extremely flat.⁹ However, when a similarly flat H/Si(111) is chlorinated via the reaction with PCl₅ in chlorobenzene solution and subsequently alkylated, the resultant alkyl/Si(111) is characterized by a high density of etch pits, with a corresponding large fraction of alkylated Si atoms residing at the edge of those pits.^{12, 13} Those atoms are more chemically accessible than the alkylated Si atoms that reside on terraces, and so can play a role in subsequent chemical processes on the surface.¹⁴ They can also play important roles in driving the formation of a stacking fault.¹²

The etch pits presumably originate during the chlorination procedure. In this chapter, we present detailed results on the scanning tunneling microscopy (STM) and tunneling spectroscopy (STS) investigations of wet-chemically prepared Cl/Si(111) surfaces. We show that this wet-chemical chlorination of H/Si(111) does, in fact, introduce a large density of etch pits of one or more atomic steps in depth. Nevertheless, Cl atoms still passivate 100% of the atop Si atom sites on the unreconstructed Si(111) surface. The etch pits result in fragmented $<11\overline{2}>$ steps and do not lead to stacking faults

on the chlorinated surface. Unlike the case for H/Si(111), methyl/Si(111) or ethyl/Si(111),¹⁵ tunneling spectroscopy revealed the absence of a band gap: current-voltage traces exhibited a non-zero slope at zero applied bias, implying a non-zero density-of-states (DOS) at the Fermi level.

2.2 Wet-chemical preparation of Cl-terminated Si(111) surfaces

The substrates utilized were (111)-oriented, Sb-doped, n-type Si wafers with a low miscut angle of $\pm 0.5^{\circ}$ and a resistivity of 0.005-0.02 ohm·cm. Anhydrous inhibitor-free tetrahydrofuran (THF, \geq 99.9%, water content <0.002%) was purchased from Sigma-Aldrich. Phosphorus pentachloride (PC15, 99.998%) was purchased from Alfa Aesar. These reagents were used as supplied and stored in a glovebox purged with nitrogen.

Silicon wafers were cut into samples of 0.5×1 cm pieces. A standard RCA cleaning process was then carried out. Briefly, the substrate was immersed in a basic peroxide solution which is composed of 1:1:4 by volume of 28% NH₃•H₂O(aq)/30% H₂O₂/H₂O at 80°C for at least 10 min and then rinsed thoroughly in running Milli-Q water. H-terminated Si(111) surface was then formed after immersing the cleaned sample in 40% NH₄F for about 15 min. This step produces large, atomically flat terraces.¹⁶ Chlorination was performed in a nitrogen-purged glovebox. A few grains of benzoyl peroxide were added to a saturated solution of PCl₅ in chlorobenzene and the solution was then heated to 90-100°C for 50 min. For lower temperature experiments, reaction time was adjusted so that a complete chlorination of the Si surface was reached. This was monitored by the surface conductivity test, since the conductivity was observed to increase significantly in comparison with H/Si. After the reaction, the sample was

removed from the solution and rinsed thoroughly with tetrahydrofuran. The samples were then dried under streaming $N_2(g)$, mounted onto a sample stage, and quickly introduced into an Omicron low-temperature STM system.

The STM microscope is contained in an ultrahigh vacuum chamber with an operating pressure of $<6\times10^{-11}$ Torr. Both topographic and spectroscopic data were acquired at 77 K by use of a mechanically cut or etched Pt-Ir tip. In a typical topographic imaging measurement, the tip is brought 5-15 Å away from the sample surface. In our measurements, a tip/vacuum/sample junction resistance of 5-20 G Ω was maintained by setting up the sample voltage and tunneling current. Surface electronic properties are examined by measuring the tunneling spectroscopy simultaneously with topographic image acquisition. Briefly, at each topographic raster scanning point the feedback was temporarily switched off, and the tip-sample distance was then fixed. The tip-sample tunneling current I_t was then recorded while scanning the tip-sample voltages, which results in a total number of ~2500 current-voltage I_t(V) traces in an STM image. The derivative dI_t/dV is then numerically calculated from the averaged I_t(V) spectroscopic data.

2.3 STM results

Figure 2.1(a)-(c) shows constant-current STM images of the Cl/Si(111) surfaces prepared at three temperatures. The etch hillocks of a single bilayer step are clearly seen, and are pointing to the $\langle \overline{112} \rangle$ orientation. In comparison with the H/Si(111),¹⁶ an obvious increase in etch pit density was observed for Cl/Si(111) prepared at all three temperatures. These etch pits are of one or more bilayer steps in depth. Previous results

from methylated and ethylated Si(111) surfaces displayed a similar etch pit morphology,⁶ although again with atomically flat terraces. The conclusion is that the increased etch pit density is most certainly caused by the wet-chemical chlorination procedure. Since the step sites are expected to be more chemically accessible than the terrace sites, the chlorination chemistry essentially transforms these relatively inert terrace sites to more reactive step sites that can be exploited in subsequent chemical reactions. It may thus be possible to tailor the surface chemical activity by changing the terrace-to-step site ratio via control over the chlorination chemistry. The etching chemistry could, in fact, be modified by changing the reaction temperature (Figure 2.1). The coverage of etch pit and edge atom were quantified and plotted versus temperature in Figure 2.1(d). An increase in reaction temperature led to an increase in both etch pit coverage and terrace-to-step ratio. A dependence of etch pit density upon the reaction conditions was also observed for Cl/Si(111) prepared via reactions of H/Si(111) with Cl₂(g). Under UV irradiation and at room temperature, Lopinski and coworkers¹¹ prepared Cl/Si(111) surfaces with a considerable number of etch pits by reaction with molecular chlorine, whereas the reaction without UV irradiation resulted in a completely flat surface with few etch pits.9 The surface morphology is apparently dependent upon the relative etch rates of the terraces, step edges, kinks and other surface structures. The large number of etch pits in the Cl/Si(111) surface observed on surfaces prepared at higher temperatures is likely ascribable to the increased ratio of the terrace etch rate to the kink etch rate.



Figure 2.1. Large-field view of STM topographic images. (a)-(c) Constant-current STM images of the Cl/Si(111) surfaces prepared at different temperatures - (a) 30° C, 5 hr, (b) 60° C, 3hr, (c) 90° C, 50 min - acquired at a sample voltage of -1.0 V. Scan area is 100 nm × 100 nm for all three images. (d) Plot of etch pit and edge atom coverage against reaction temperature.

In spite of the increased number of etch pits in the chlorinated surface, a full coverage of Cl-Si on the Si(111) atop sites could be achieved through the solution phase chemical approach. This nearly 100% coverage is suggested by the constant-height atomic-resolution image as shown in Figure 2.2, and by experiments in which methyl and acetyl Grignard reagents have been shown to fully alkylate such a chlorinated surface.^{6, 14} Constant-current mode images, at this resolution, were of slightly poorer quality but still revealed the full coverage and hexagonal structure of the Cl-Si on the surface. While it

would have been interesting to obtain an image of a step edge at a similarly high resolution to that presented in Figure 2.2, such an image would require constant-current-mode imaging. Statistical analysis of the nearest-neighbor-atom distances revealed a peak value of 3.90±0.12 Å, which is in agreement with the distance between silicon atop sites on the unreconstructed Si(111) surface.¹⁷ This suggests an unreconstructed 1×1 structure and hence a full passivation of the Si atop sites by Cl, consistent with reported results from the gas phase chlorination of H-terminated Si(111).⁹ Similar images were obtained from other terrace sites. Atomically flat surfaces with a minimal amount of contamination can be identified from the image, indicating that the wet-chemically prepared Cl/Si(111) can survive at least moderate post-processing conditions that include a several minute air exposure prior to introduction into the UHV STM chamber.



Figure 2.2. Close-up STM images with atomic resolution. (a) Constant height STM image of surface prepared at 90°C showing uniform CI atom coverage of the atop sites of the unreconstructed surface. Scan area = 5×5 nm, V_b = -0.8 V, I_t = 0.27 nA. (b) Histogram of the distribution of distance between nearest neighbor atoms. A lognormal fit is indicated by a red line, which peaks at 0.39 nm. The full-width at half maximum gives an error of about ±0.012 nm.



Figure 2.3. Schematics of the structure of steps on CI-terminated Si(111) surfaces. The $<11\overline{2}>$ and $<\overline{1}\overline{1}2>$ steps are indicated by arrows.

Another interesting set of observations are the fragmented $\langle 11\overline{2} \rangle$ steps with the outward normal along the $\langle 11\overline{2} \rangle$ orientation. Figure 2.3a is an illustration of the etch hillock observed in STM images. For H/Si(111) surfaces, it has been shown that a $\langle 11\overline{2} \rangle$ step is terminated by the lower Si atoms of the bilayer, forming a horizontal monohydride termination structure.¹⁶ By contrast, $\langle \overline{112} \rangle$ steps are terminated by the upper Si atoms, resulting in a vertical dihydride structure.¹⁶ Upon chlorination, a rebonded step geometry was formed at the $\langle \overline{112} \rangle$ steps, where the step terminating Si atoms are bonded to the bottom terrace silicon atoms (see Figure 2.3b).¹⁰ The full chlorination of the $\langle 11\overline{2} \rangle$ step terminating Si atoms has been shown to be energetically unfavorable. This might be attributed to the significant electrostatic repulsion between the negatively charged

chlorine atoms along the upper and lower terraces of the $<11\overline{2}>$ steps.¹⁰ Periodic DFT calculations indicated that the Si-Cl bond on $<\overline{112}>$ is 0.58 eV stronger than on $<11\overline{2}>$. For CH₃/Si(111) the difference is even greater (0.67 eV).¹²

A stacking fault basically switches a $<11\overline{2}>$ step to a $<\overline{112}>$ step. Calculations indicated that a full stacking fault on the terraces is energetically possible for etched CH₃/Si(111) and Cl/Si(111) surfaces. Partly faulted regions were observed by Itchckawitz et al.¹⁰ on the Cl/Si(111), and were inferred for CH₃/Si(111) by us through a combination of experiment⁶ and theory.¹² For our Cl/Si(111) surfaces prepared at 90°C, the terrace-to-step site ratio (estimated to be ~13 from the STM images) indicates that stacking faults would be energetically possible.¹² However, no stacking faults were observed for any of our preparations. Careful inspection of the etch hillock edges (Figure 2.1c) revealed that the $<11\overline{2}>$ step edges were actually segmented, or further etched. An etched $<11\overline{2}>$ step edge site creates two kink sites, each of which possesses the $<\overline{112}>$ step edge structure. This further etching therefore partially removes the $<11\overline{2}>$ step edge atoms and essentially changes the $<11\overline{2}>$ step structure to the $<\overline{112}>$ step structure, thus stabilizing the surface.

2.4 STS results

We also investigated the tunneling spectroscopy of wet-chemically prepared Cl/Si(111). Current-voltage $I_t(V)$ traces, averaged over the area shown in Figure 2.2a are represented in Figure 2.4a, together with a similarly averaged trace collected from H/Si(111). The derivative dI_t/dV , which is roughly proportional to the local density of states, is shown in Figure 2.4b. The set points for bias voltage and current were

experimentally varied to optimize image quality. For the data shown in Figure 2.4a, the parameters were -0.8 V and 0.27 nA for Cl/Si(111) and -1.7 V and 0.12 nA for H/Si(111).



Figure 2.4. Scanning tunneling spectroscopy of CI/Si(111). (a) Representative low temperature (77K) I-V spectra for H- and CI-terminated Si(111) surfaces. (b) Corresponding dI_t/dV tunneling spectrum for the above two surfaces.

Fully passivated H/Si(111) exhibits a large conductance gap, and this was observed in a bias voltage range down to -1.7 V, where a limiting value of 1.6 V was obtained. Similarly large conductance gaps have been recently reported for methyl/Si(111) (100% passivation) and ethyl/Si(111) (80% ethyl-; 20% H-passivation).¹⁵ This has been attributed to the absence of surface states for these chemically functionalized surfaces. The apparent larger value of the measured band gap compared to the standard bulk silicon gap (1.12 eV at 300 K) can be attributed to the tip-induced band bending (TIBB, see Figure 2.5a).¹⁸ Since only part of the sample-tip voltage is applied across the vacuum gap, the onset of current is postponed till larger voltage values are reached. Interestingly, at small negative sample voltages just below the sample Fermi level, the expected tunneling current component (I_D) from the conduction-band electrons was not observed,

while this is commonly observed at cleaved n-type GaAs surfaces.¹⁹ The absence of this current component suggests a rather small tunneling transmission coefficient of conduction-band electrons from the sample to the tip. A possibility may be that the very sharp tip may not accommodate those states with non-zero momentum parallel to the semiconductor sample surface.²⁰



Figure 2.5. Schematic illustration of energy bands, showing the conduction-band minimum $E_{\rm C}$ and valence-band maximum $E_{\rm V}$. The Fermi level of the sample is denoted by $E_{\rm F}$ and that of the tip $E_{\rm F}$ +eV, where V is the sample voltage. (a) Tip-induced band bending (TIBB). The arrows indicate the tunneling direction of electrons. (b) Adatom-induced band bending. The residue TIBB is also indicated at non-zero sample-tip voltages.

The current-voltage characteristics of Cl/Si(111) exhibited no evidence of a band gap (see Figure 2.4a), with the I-V curve possessing a finite slope at zero bias voltage. Varying the bias voltage in a range of $-1.5 \sim -0.3$ V showed consistent results. This indicates a non-zero DOS at the Fermi level. From the dI_t/dV versus voltage plot (Figure 2.4b), three different conductance regions are noticed. At positive values of sample voltage, a conductance response attributable to tunneling out of filled levels of the tip and into the empty levels of Si is observed. At negative-valued sample voltages below about -1.1 V the current is dominated by the tunneling of the valence band electrons from the silicon into the tip. The third region is that which is bracketed by negative-valued sample biases between 0 and -1.1 V.

Since the UHV STM images show atomically flat surfaces with minimal surface contamination, the finite conductance observed at zero bias could then be explained in terms of the intrinsic Cl/Si(111) surface properties. Unlike the H/Si(111) where the Fermi level is unpinned, the strong electronegativity of chlorine is expected to induce a large upward band bending (Figure 2.5b), where the Fermi level approaches the top of the valence band edge.²¹ Strong surface band bending induced by adsorbates such as metal and semiconductor atoms, oxygen and halogens have been observed at even submonolayer coverages.²² These adsorbates have also been shown to induce intrinsic surface states, pinning the Fermi level (E_F) at a position dependent upon the difference between the electronegativity of the adsorbate and the substrate atoms.²² E_F pinning close to the edge of the valence band has been previously observed on Cl/GaAs(110) surfaces.²¹ Recently, four-point measurements on Cl/Si(111) by Lopinski et al.¹¹ showed a high surface conductance, which is about 3 times of the bulk value. A p-type inversion

layer was invoked to explain this effect. According to this model, at small positive voltages above the Fermi level, tunneling of electrons into the inversion layer may dominate the current till the onset of the conduction-band current (Figure 2.5b). At small negative sample bias, tunneling of both conduction-band electrons and surface valence-band edge electrons may both contribute to the current. The probability of thermionic emission of silicon conduction-band electrons followed by tunneling through the vacuum barrier is expected to be rather low.²³ At increasing values of negative sample bias, the surface barrier decreases and hence the tunneling current increased. A theoretical model of this surface would likely provide clarification of this picture. Surface recombination velocity measurements²⁴ have revealed a low velocity for a freshly prepared Cl/Si sample, followed by a sharp increase in recombination rate upon oxidation. Interestingly, although the observation of finite conductance between 0 and -1.1 V may obscure the observation of the valence band edge, the present data do exhibit a gap voltage difference that is very close to the bulk band gap value.

2.5 Conclusion

In this chapter, we have reported on the scanning tunneling microscopy and spectroscopic studies of Cl-terminated Si(111) surfaces. Cryo-STM images of wetchemically prepared Cl/Si(111) surfaces showed an unreconstructed 1×1 structure and hence full replacement of hydrogen by chlorine atoms. An increased etch pit density relative to gas phase prepared Cl/Si(111) was observed, and could be controlled experimentally. Such control may enable the tailoring of the surface chemical reactivity toward subsequent alkylation and other functionalization processes. Tunneling spectroscopy revealed a non-zero DOS near zero applied bias, in contrast to analogous measurements on H/Si(111), methyl/Si(111), and ethyl/Si(111) surfaces. This may indicate that the electronegative Cl atoms are inducing surface states which are contributing to current observed within the usual forbidden region.

2.6 References

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